

# BCT65N15M1

## N-Channel Silicon Carbide Power MOSFET

650 V, 134 A, 15 mΩ



bestirpower

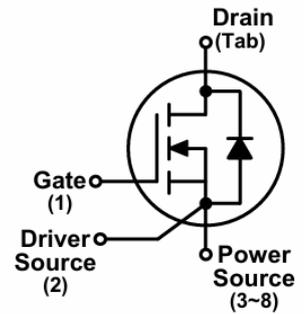
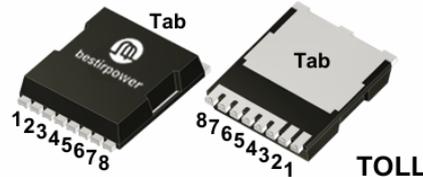
### Features

- High switching speed with a low gate charge
- Fast intrinsic diode with low reverse recovery
- Robust Avalanche Capability
- 100% Avalanche Tested
- Pb-free, Halogen Free, and RoHS Compliant

$BV_{DSS}$	$I_D, T_C=25^\circ\text{C}$	$R_{DS(on),typ}$	$Q_{g,typ}$
650 V	134A	15 mΩ	146 nC

### Benefits

- System efficiency improvement
- Higher frequency applicability
- Increased power density
- Reduced cooling effort



### Applications

- Server & Telecom power
- EV charging station
- Solar inverter / ESS / UPS
- Industrial power supply



### Absolute Maximum Ratings ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Unit
$V_{DSS}$	Drain to Source Voltage	650	V
$V_{GS}$	Gate to Source Voltage (DC)	-10 / +22	V
$V_{GSop}$	Recommended Operation Value	-5 / +18	V
$I_D$	Drain Current	$T_C = 25^\circ\text{C}$	134
		$T_C = 100^\circ\text{C}$	96
$I_{DM}$	Drain Current	Pulsed (Note 1)	375
$P_D$	Power Dissipation	$(T_C = 25^\circ\text{C})$	500
		Derate Above $25^\circ\text{C}$	3.43
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to 175	$^\circ\text{C}$
$T_L$	Maximum Lead Temperature for Soldering, 1/8" from Case for 10 Seconds	260	$^\circ\text{C}$

※Note 1 : Limited by maximum junction temperature.

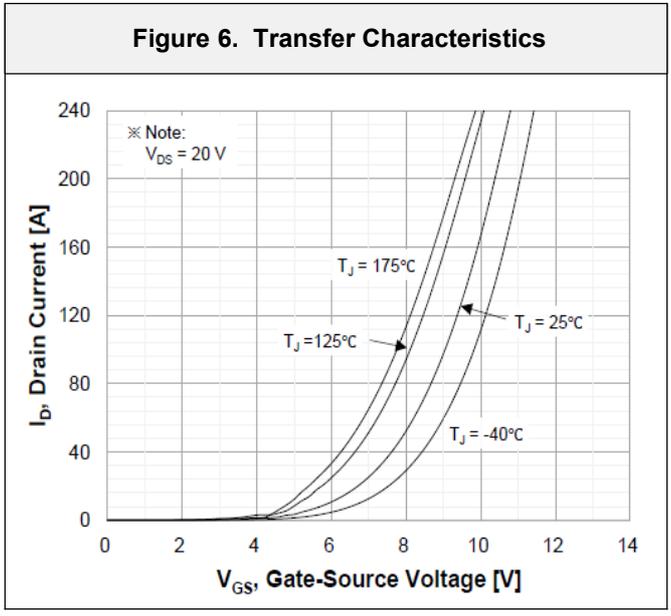
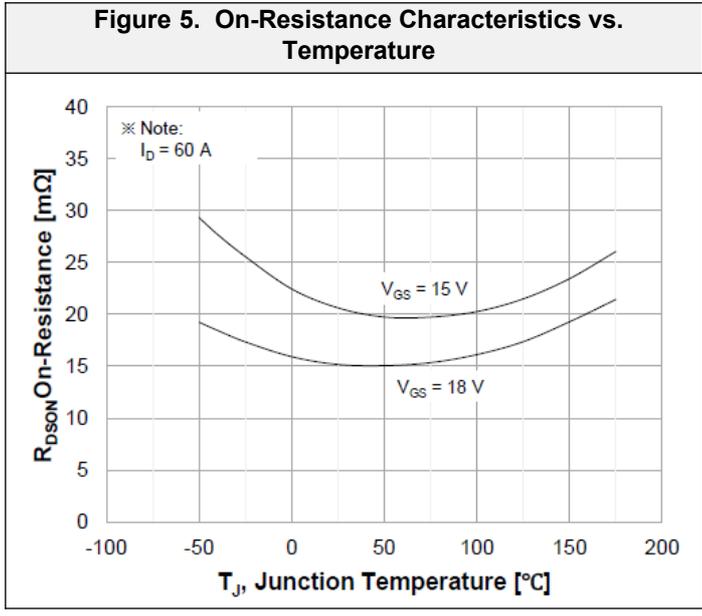
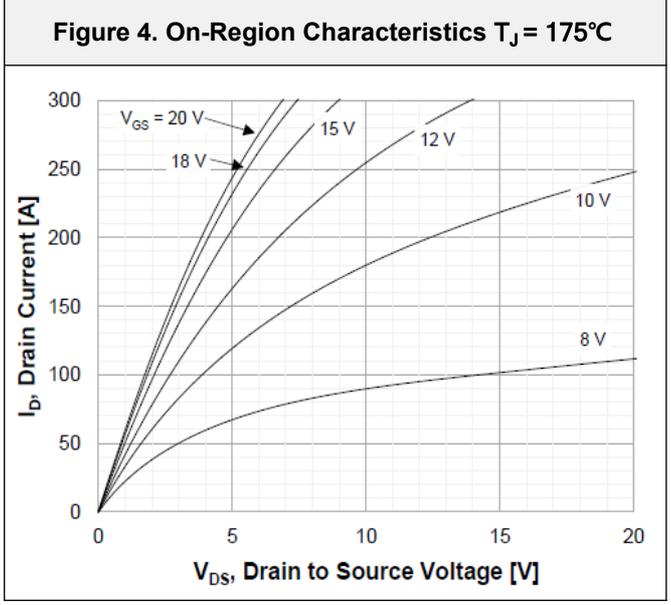
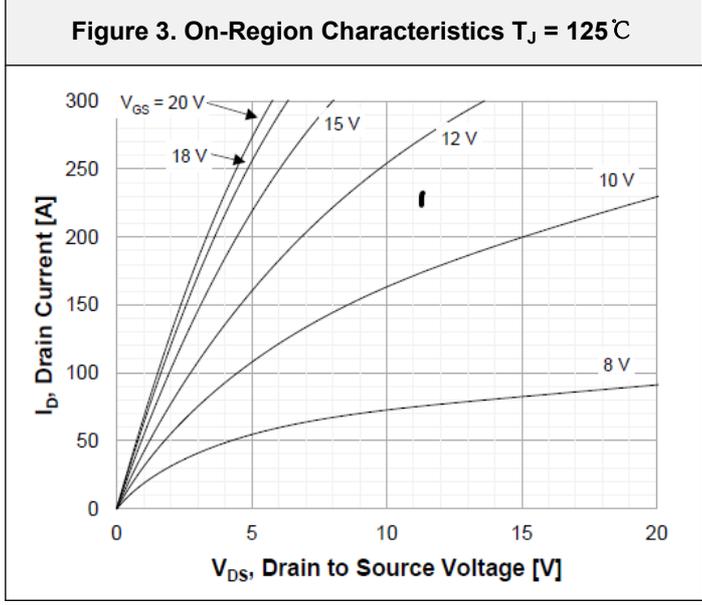
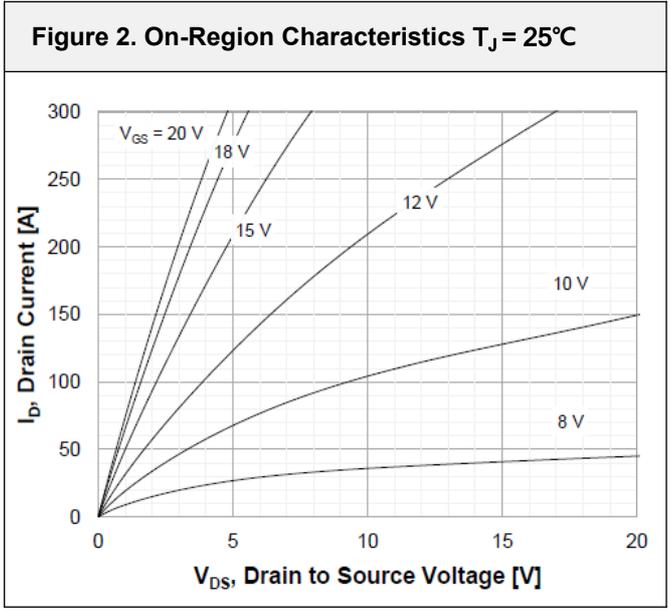
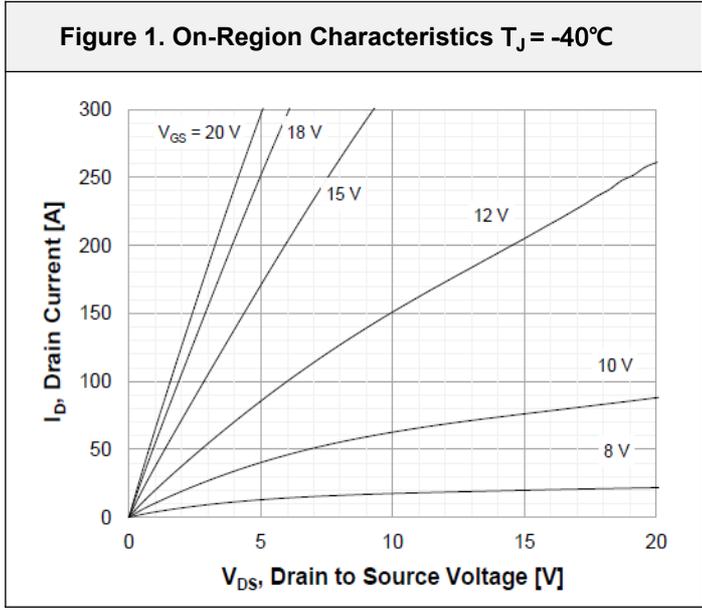
### Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.3	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	40	

**Electrical Characteristics** ( $T_J = 25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}$	650	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	-	1	100	$\mu\text{A}$
		$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}, T_J = 175^\circ\text{C}$	-	10	-	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS} = +22\text{ V}, V_{DS} = 0\text{ V}$	-	-	+100	nA
		$V_{GS} = -10\text{ V}, V_{DS} = 0\text{ V}$	-	-	-100	
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 20.3\text{ mA}$	2.0	2.8	4.5	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 18\text{ V}, I_D = 60\text{ A}$	-	15	21	$\text{m}\Omega$
		$V_{GS} = 18\text{ V}, I_D = 60\text{ A}, T_J = 175^\circ\text{C}$	-	20	-	
$g_{fs}$	Transconductance	$V_{DS} = 20\text{ V}, I_D = 60\text{ A}$	-	40.8	-	S
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = 400\text{ V}, V_{GS} = 0\text{ V}, f = 250\text{ kHz}$	-	3525	-	$\text{pF}$
$C_{oss}$	Output Capacitance		-	297	-	
$C_{riss}$	Reverse Capacitance		-	16	-	
$E_{oss}$	Stored Energy in Output Capacitance	$V_{DS} = 0\text{ V to } 400\text{ V}, V_{GS} = 0\text{ V}$	-	36.5	-	$\mu\text{J}$
$C_{o(er)}$	Energy Related Output Capacitance		-	456	-	$\text{pF}$
$C_{o(tr)}$	Time Related Output Capacitance		-	648	-	
$Q_{g(tot)}$	Total Gate Charge	$V_{DS} = 400\text{ V}, I_D = 60\text{ A},$ $V_{GS} = -5\text{ V} / 18\text{ V}$	-	146	-	nC
$Q_{gs}$	Gate to Source Charge		-	45	-	
$Q_{gd}$	Gate to Drain "Miller" Charge		-	36	-	
$R_G$	Internal Gate Resistance	$f = 1\text{ MHz}$	-	2.2	-	$\Omega$
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{DS} = 400\text{ V}, I_D = 60\text{ A},$ $V_{GS} = -5\text{ V} / 18\text{ V}, R_G = 5.6\ \Omega,$	-	28.8	-	ns
$t_r$	Turn-On Rise Time		-	25.7	-	
$t_{d(off)}$	Turn-Off Delay Time		-	60.1	-	
$t_f$	Turn-Off Fall Time		-	10.5	-	
$E_{on}$	Turn-on Switching Energy		-	146	-	$\mu\text{J}$
$E_{off}$	Turn-off Switching Energy		-	279	-	
$E_{tot}$	Total Switching Energy		-	425	-	
<b>Source-Drain Diode Characteristics</b>						
$I_S$	Maximum Continuous Diode Forward Current	-	-	134	A	
$I_{SD}$	Maximum Pulsed Diode Forward Current	-	-	375		
$V_{SD}$	Diode Forward Voltage	$V_{GS} = -5\text{ V}, I_{SD} = 60\text{ A},$	-	4.4	-	V
$t_{rr}$	Reverse Recovery Time	$V_{DD} = 400\text{ V}, I_{SD} = 60\text{ A},$ $di_F/dt = 1000\text{ A}/\mu\text{s},$ Includes $Q_{OSS}$	-	29.5	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	303	-	nC
$I_{rrm}$	Peak Reverse Recovery Current		-	17.4	-	A

### Typical Performance Characteristics



### Typical Performance Characteristics

Figure 7. Diode Forward Voltage Characteristics vs. Source-Drain Current  $T_J = -40^\circ\text{C}$

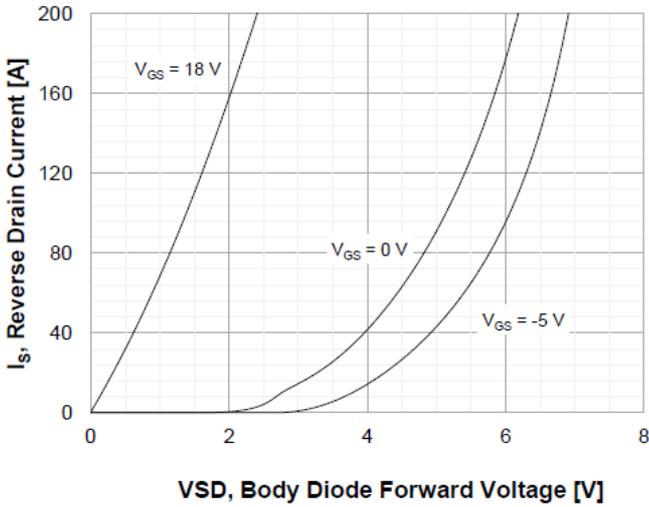


Figure 8. Diode Forward Voltage Characteristics vs. Source-Drain Current  $T_J = 25^\circ\text{C}$

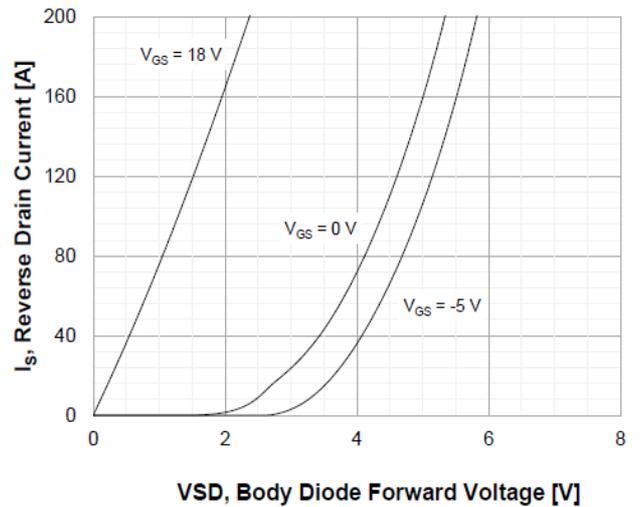


Figure 9. Diode Forward Voltage Characteristics vs. Source-Drain Current  $T_J = 125^\circ\text{C}$

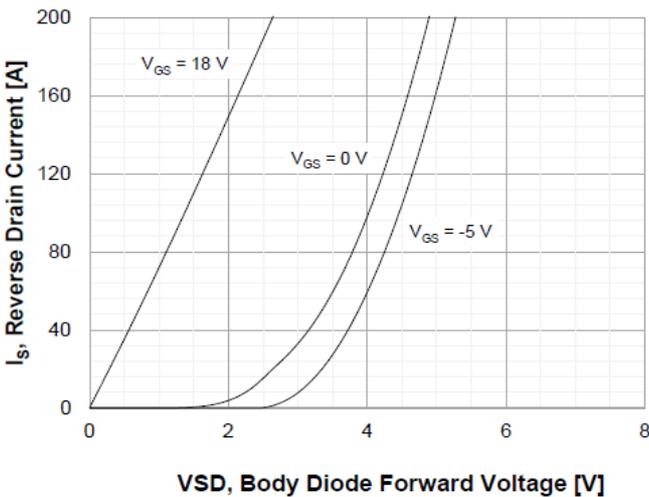


Figure 9. Diode Forward Voltage Characteristics vs. Source-Drain Current  $T_J = 175^\circ\text{C}$

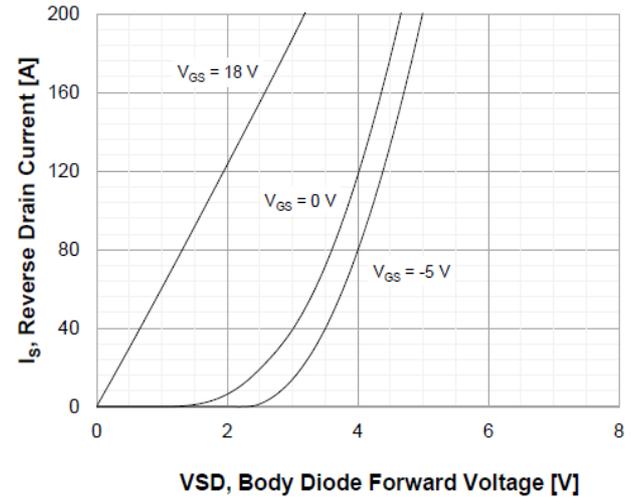


Figure 11. Threshold Voltage vs. Temperature

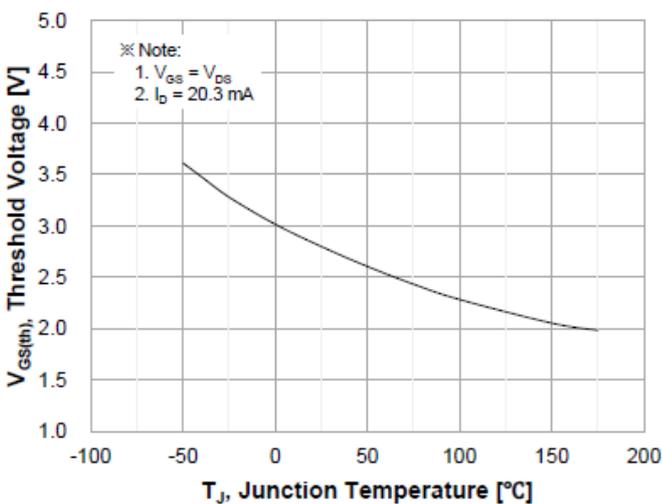
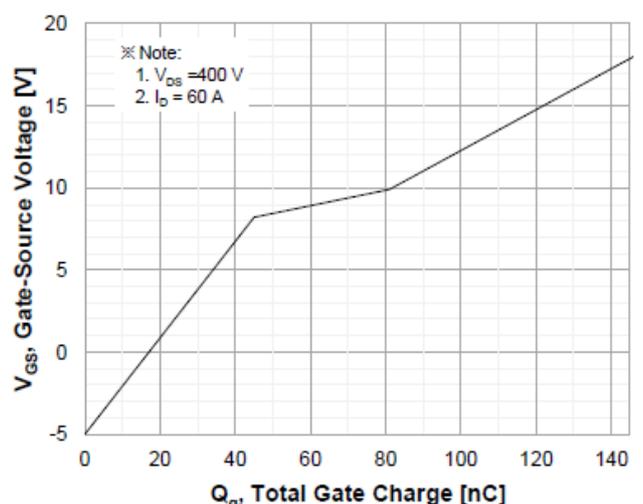
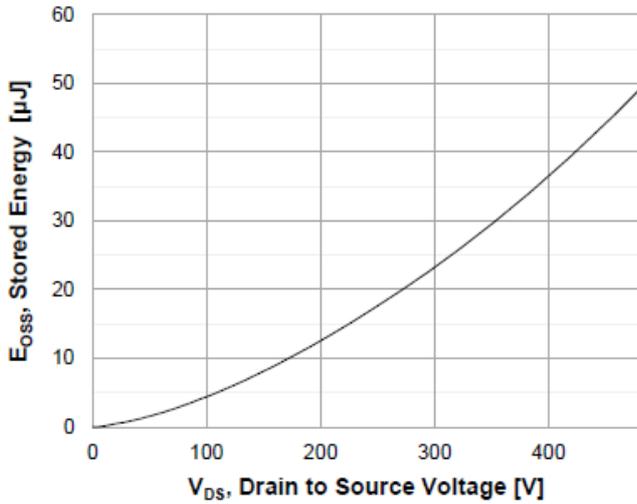


Figure 12. Gate Charge Characteristics

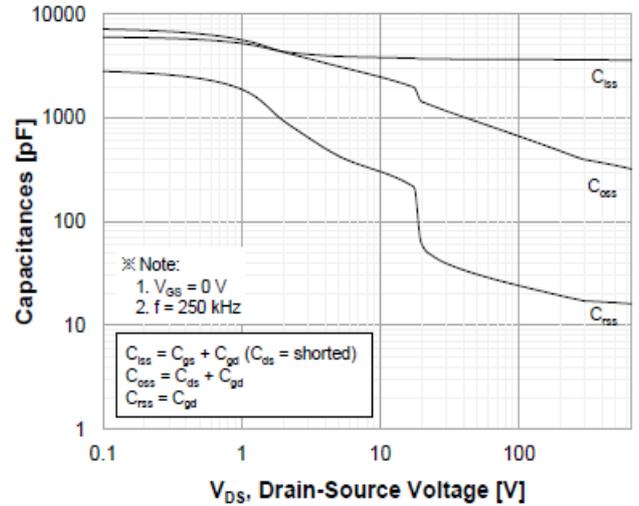


### Typical Performance Characteristics

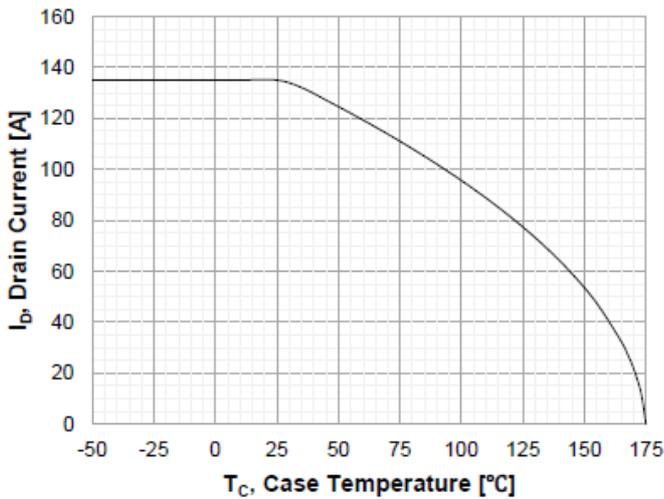
**Figure 13. Stored Energy in Output Capacitance**



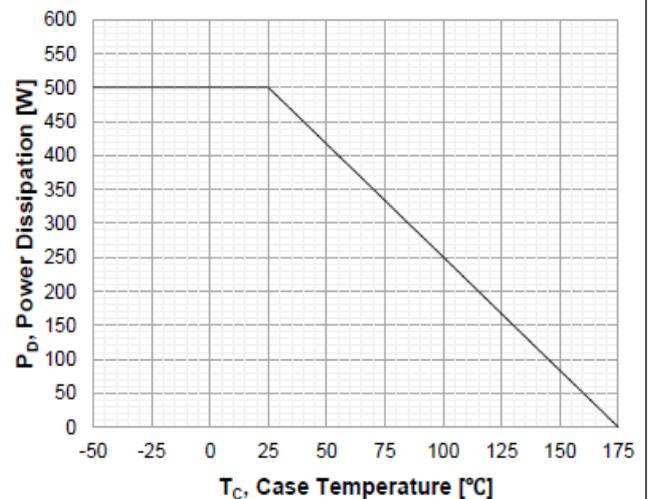
**Figure 14. Capacitance Characteristics**



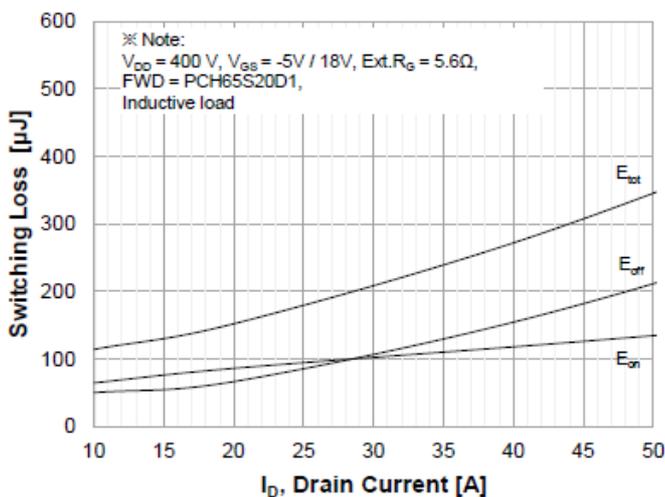
**Figure 15. Continuous Drain Current Derating vs. Case Temperature**



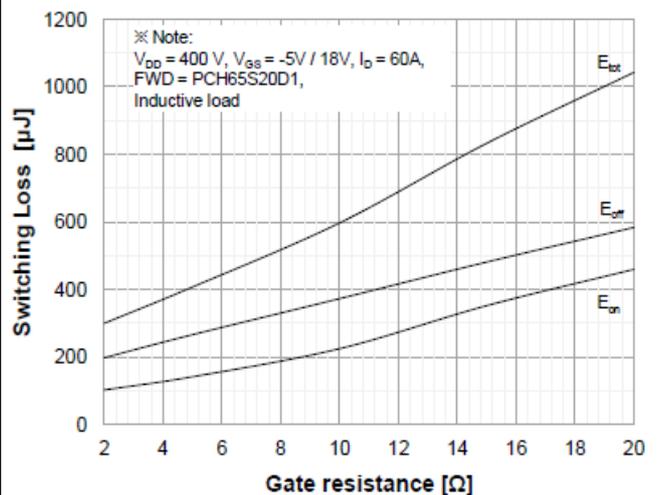
**Figure 16. Maximum Power Dissipation Derating vs. Case Temperature**



**Figure 17. Typ. Switching Losses vs. Drain Current**

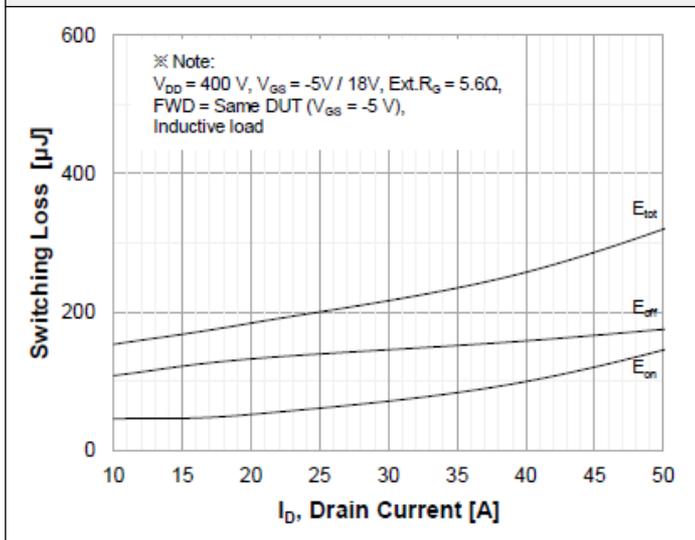


**Figure 18. Typ. Switching Losses vs. Gate Resistance**

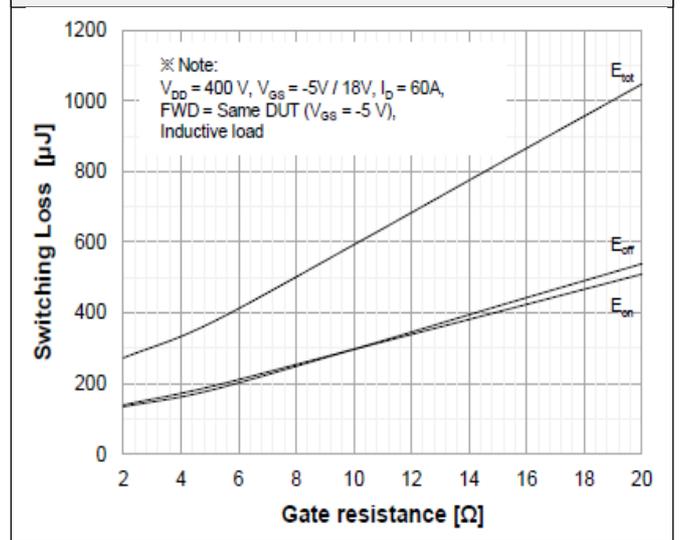


### Typical Performance Characteristics

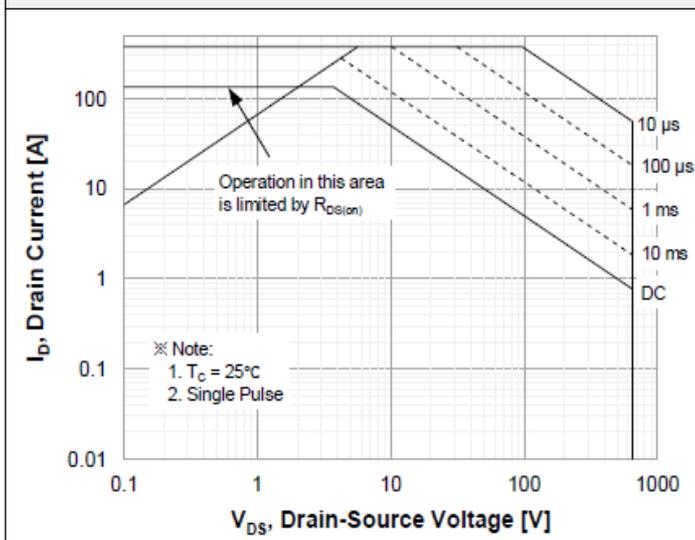
**Figure 19. Typ. Switching Losses vs. Drain Current**



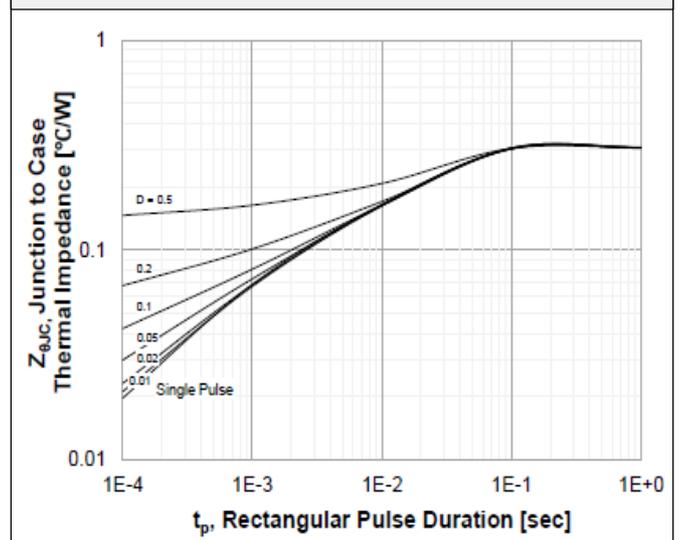
**Figure 20. Typ. Switching Losses vs. Gate Resistance**



**Figure 21. Maximum Safe Operating Area**

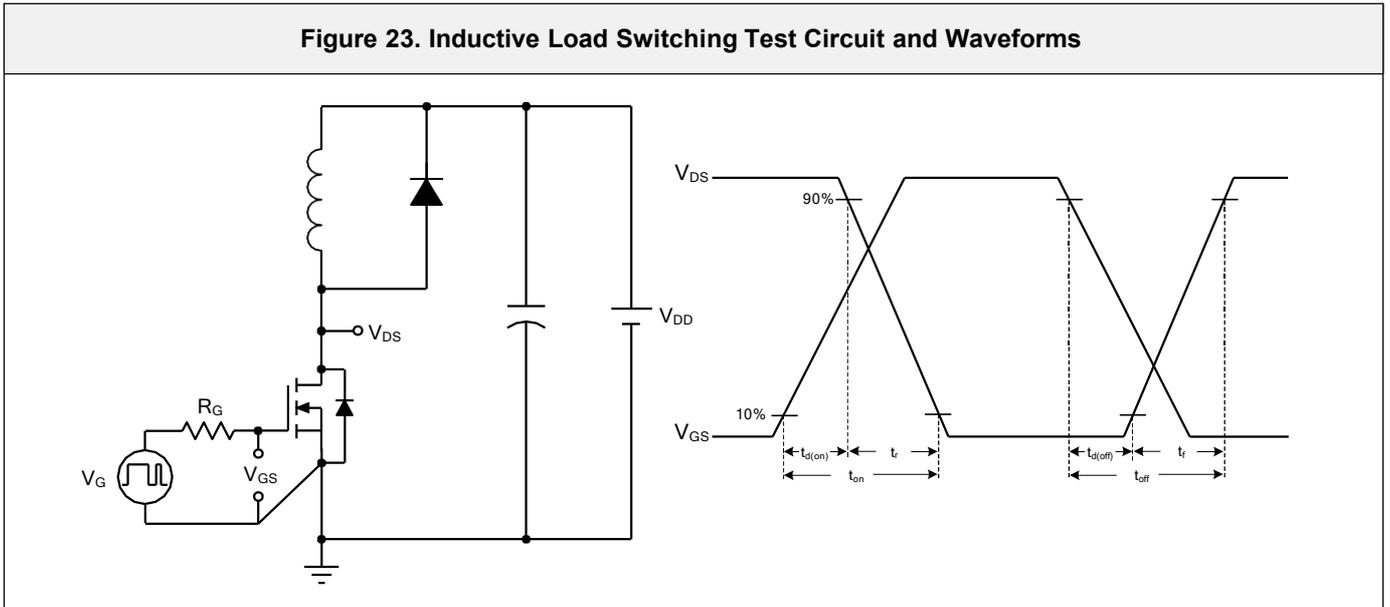


**Figure 22. Transient Thermal Response Curve**

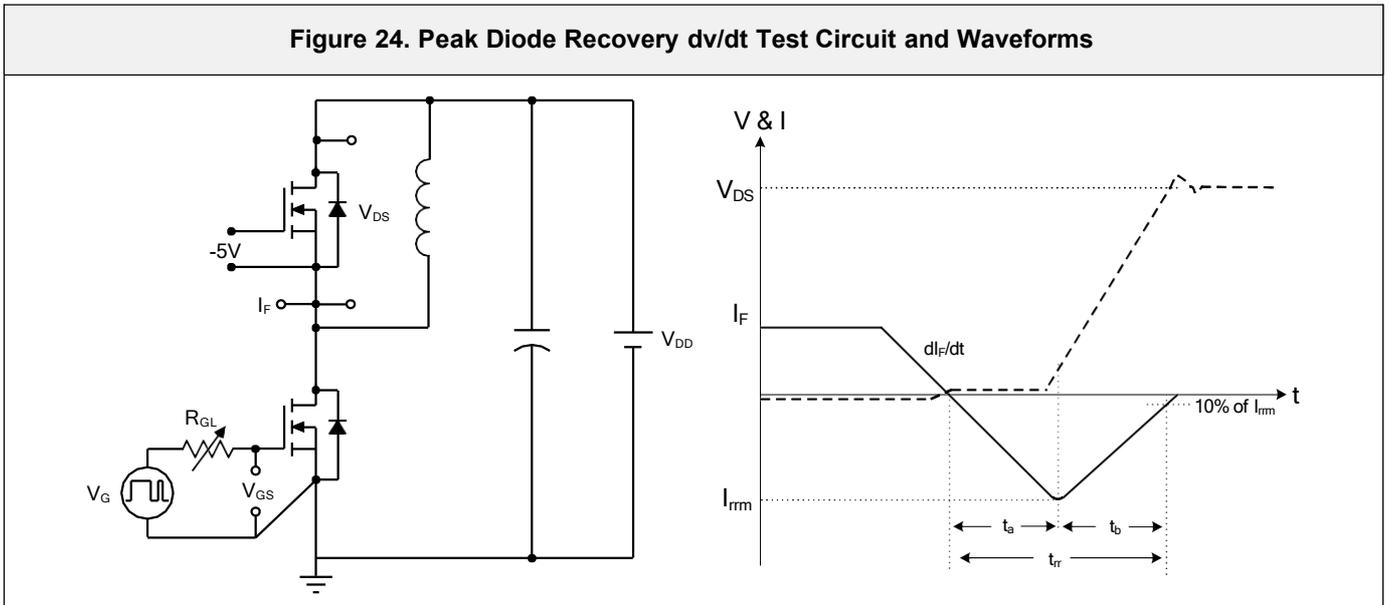


## Typical Performance Characteristics

**Figure 23. Inductive Load Switching Test Circuit and Waveforms**

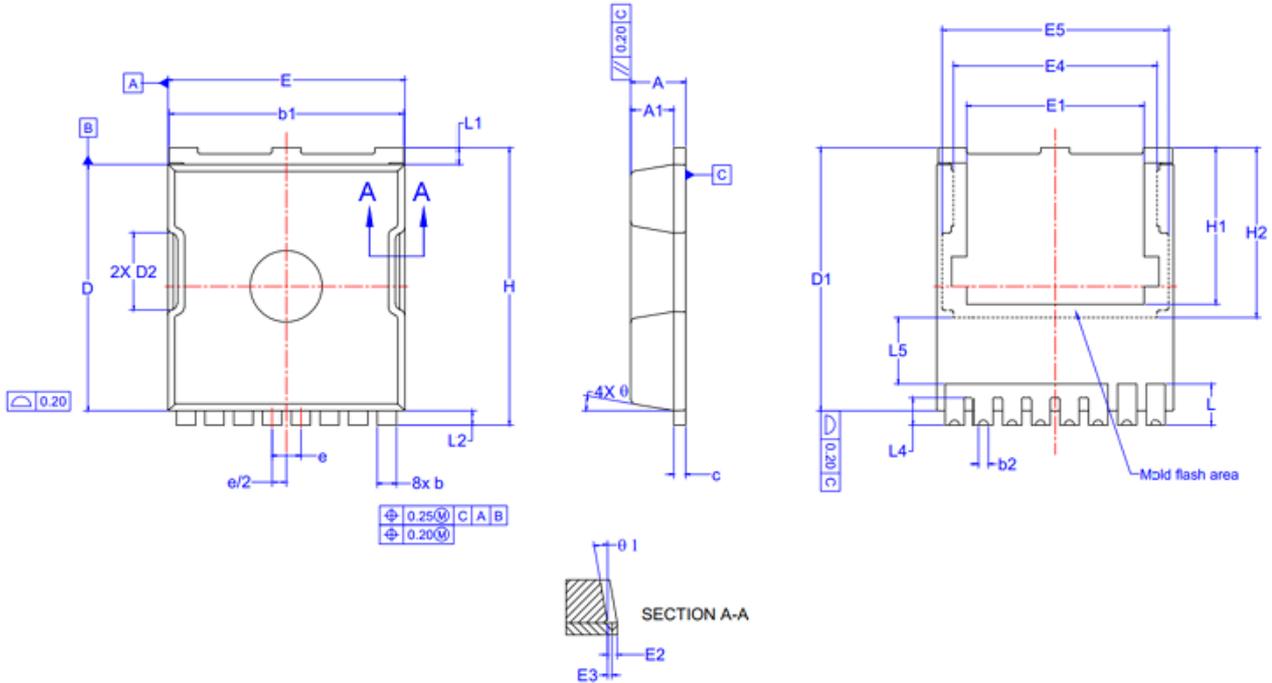


**Figure 24. Peak Diode Recovery  $dv/dt$  Test Circuit and Waveforms**



**Package Outlines**

**TOLL**



SYMBOL	MIN.	NOM.	MAX.
A	2.20	2.30	2.40
A1	1.70	1.80	1.90
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b2	0.36	0.41	0.51
c	0.40	0.50	0.60
D	10.28	10.38	10.48
D1	10.98	11.08	11.18
D2	3.30		
E	9.80	9.90	10.00
E1	7.32	7.42	7.52
E2	0.30	0.40	0.50
E3	0.15	0.18	0.21
E4	8.50		
E5	9.46		
e	1.20 BASIC		
H	11.58	11.68	11.78
H1	6.55	6.65	6.75
H2	7.05	7.15	7.25
L	1.63	1.73	1.83
L1	0.60	0.70	0.80
L2	0.50	0.60	0.70
L4	1.00	1.15	1.30
L5	2.70	2.80	2.90
N	8		
θ	10° REF.		
θ1	10° REF.		

\* Dimensions in millimeters

## Package Marking and Ordering Information

Part Number	Top Marking	Package	Packing Method	Quantity
BCT65N15M1	BCT65N15M1	TOLL	Tape & Reel	1000 units

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