



# BMT65N065UC1

## Super Junction Power MOSFET

650 V, 55 A , 65 mΩ

### Description

BMT65N065UC1 is power MOSFET using bestirpower' s advanced super junction technology that can realize very low on resistance and gate charge.

It will provide much high efficiency by using optimized charge coupling technology. These user friendly devices give an advantage of Low EMI to designers as well as low switching loss.

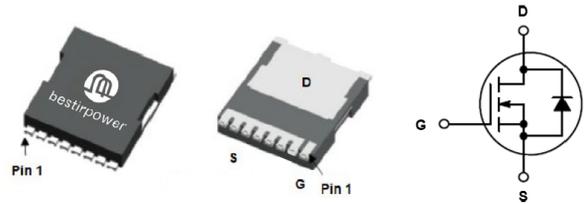
### Applications

- PC power.
- Server power supply.
- Telecom.
- Solar inverter.
- Super charger for automobiles.

### Features

$BV_{DSS} @ T_{J,max}$	$I_D$	$R_{DS(on),max}$	$Q_{g,typ}$
700V	55 A	65 mΩ	73 nC

- Ultra-fast body diode.
- Extremely low losses due to very low FOM  $R_{ds(on)} * Q_g$  and  $E_{oss}$ .
- Very high commutation ruggedness.



### Absolute Maximum Ratings ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Unit
$V_{DSS}$	Drain to Source Voltage <sup>1)</sup>	650	V
$V_{GSS}$	Gate to Source Voltage	$\pm 30$	V
$I_D$	Drain Current <sup>2)</sup>	$V_{GS} = 10 \text{ V}, (T_C = 25^\circ\text{C})$	55
		$V_{GS} = 10 \text{ V}, (T_C = 100^\circ\text{C})$	35
$I_{DM}$	Drain Current	Pulsed( $T_C = 25^\circ\text{C}$ )	165
$E_{AS}$	Single Pulsed Avalanche Energy <sup>3)</sup>	1000	mJ
$I_{AR}$	Avalanche Current	6	A
dv/dt	MOSFET dv/dt	50	V/ns
	Peak Diode Recovery dv/dt <sup>4)</sup>	50	
$P_{tot}$	Power Dissipation	( $T_C = 25^\circ\text{C}$ )	500
$di_f/dt$	Maximum diode commutation speed <sup>4)</sup>	500	A/ $\mu\text{s}$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_S$	Continuous diode forward current	$T_C=25^\circ\text{C}$	55
$I_{S,pulse}$	Diode pulse current <sup>2)</sup>	$T_C=25^\circ\text{C}$	165

1) Limited by  $T_J$  max. Maximum duty cycle  $D=0.75$ .

2) Pulse width  $t_p$  limited by  $T_J$ , max.

3)  $V_{DD}=50\text{V}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .

4)  $V_{DClk}=400\text{V}$ ;  $V_{DS,peak} < V_{(BR)DSS}$ ; identical low side and high side switch with identical  $R_G$ .

### Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.25	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62	
$T_{sold}$	Soldering temperature, wavesoldering only allowed at leads	260	°C

### Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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#### Off Characteristics

$BV_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}$	650	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}, T_J = 25^\circ\text{C}$	-	-	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS} = \pm 30\text{ V}, V_{DS} = 0\text{ V}$	-	-	$\pm 100$	nA

#### On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 1\text{ mA}$	2.8	3.8	4.8	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10\text{ V}, I_D = 23.5\text{ A}, T_J = 25^\circ\text{C}$	-	58	65	mΩ
$R_G$	Gate resistance	$F=1\text{MHz}$ open drain	-	3.5	-	Ω

#### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{ V}, V_{DS} = 100\text{V},$ $f = 250\text{ kHz}$	-	3990	-	pF
$C_{oss}$	Output Capacitance		-	120	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	5	-	pF
$C_{o(tr)}$	Time Related Output Capacitance <sup>1)</sup>	$V_{DS} = 0\text{ to }400\text{ V}, V_{GS} = 0\text{ V}$	-	637	-	pF
$C_{o(er)}$	Energy Related Output Capacitance <sup>2)</sup>		-	125	-	pF
$Q_{g(tot)}$	Total Gate Charge at 10 V	$V_{DD} = 400\text{ V}, I_D = 25\text{ A},$ $V_{GS} = 0\text{ to }10\text{ V}$	-	73	-	nC
$Q_{gs}$	Gate to Source Charge		-	18	-	nC
$Q_{gd}$	Gate to Drain "Miller" Charge		-	23	-	nC
$V_{plateau}$	Gate plateau voltage		-	4.5	-	V

#### Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 400\text{ V}, I_D = 23\text{ A},$ $V_{GS} = 10\text{ V}$	-	66	-	ns
$t_r$	Turn-On Rise Time		-	79	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	139	-	ns
$t_f$	Turn-Off Fall Time		-	12	-	ns

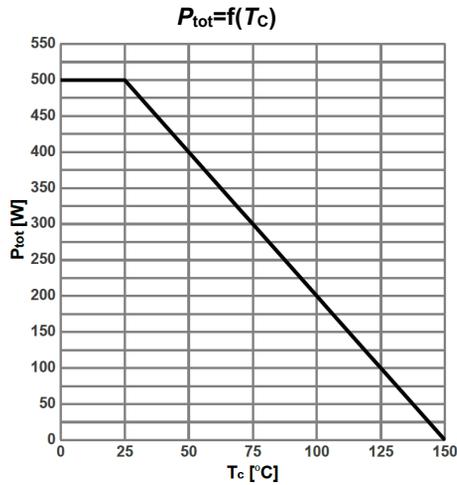
#### Source-Drain Diode Characteristics

$V_{SD}$	Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_F = 23\text{ A}, T_J = 25^\circ\text{C}$	-	0.9	-	V
$t_{rr}$	Reverse Recovery Time	$V_R = 400\text{V}, I_F = 23\text{A},$ $di_F/dt = 100\text{A}/\mu\text{s}$	-	165	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	1.9	-	$\mu\text{C}$
$I_{rrm}$	Peak reverse recovery current		-	22	-	A

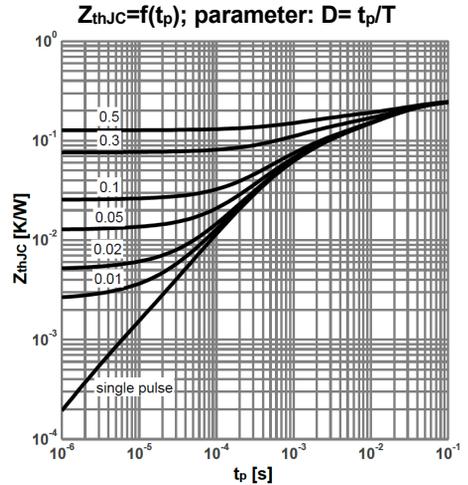
- $C_{o(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 400V.
- $C_{o(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 400V.

### Typical Performance Characteristics

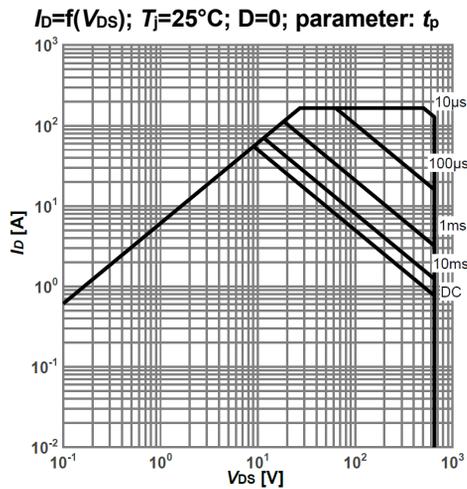
**Figure 1. Power dissipation**



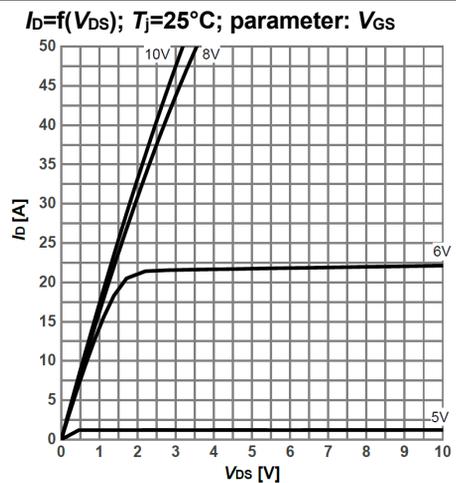
**Figure 2. Max. transient thermal impedance**



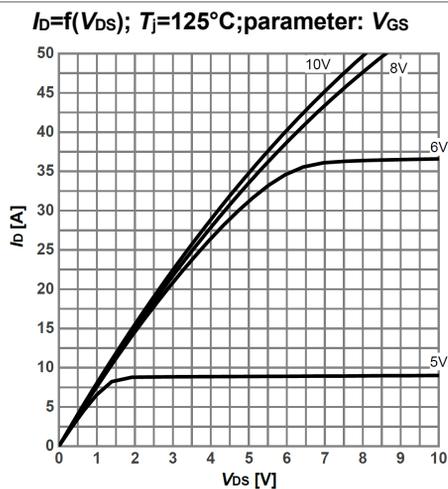
**Figure 3. Safe operating area**



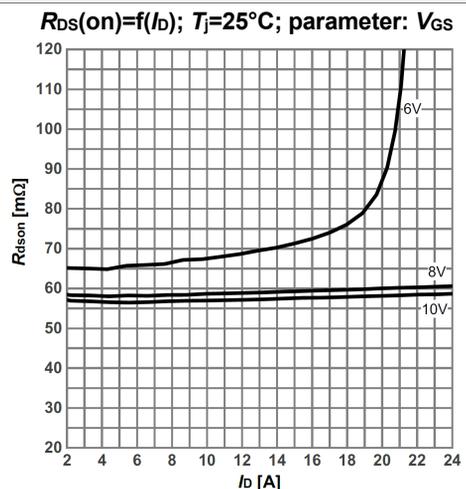
**Figure 4. Typ. Output characteristics**



**Figure 5. Typ. Output characteristics**

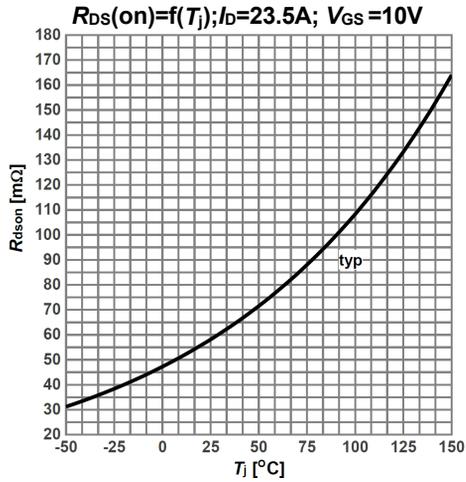


**Figure 6. Typ. drain-source on-state resistance**

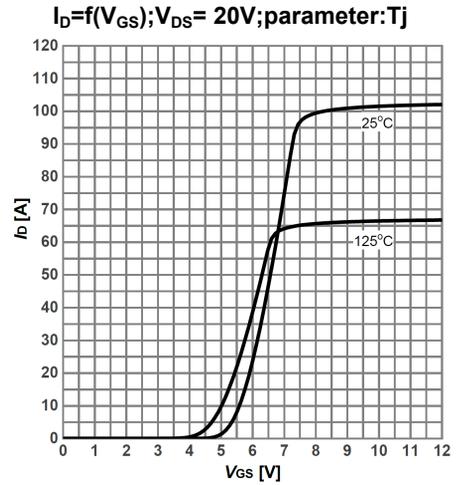


### Typical Performance Characteristics

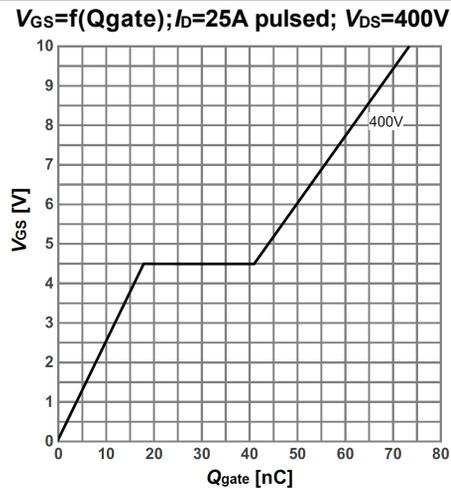
**Figure 7. Typ. drain-source on-state resistance**



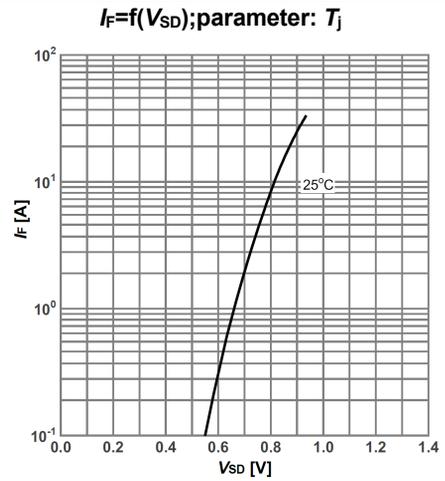
**Figure 8. Typ. transfer characteristics**



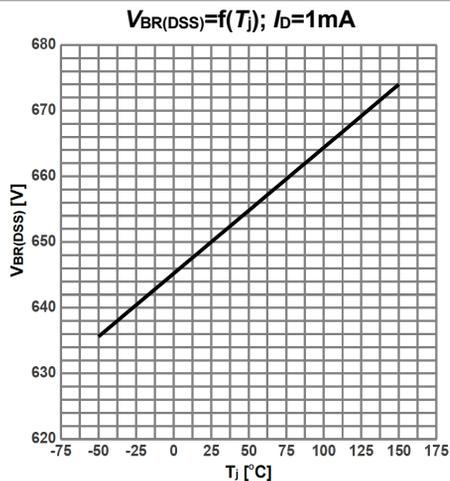
**Figure 9. Typ. gate charge**



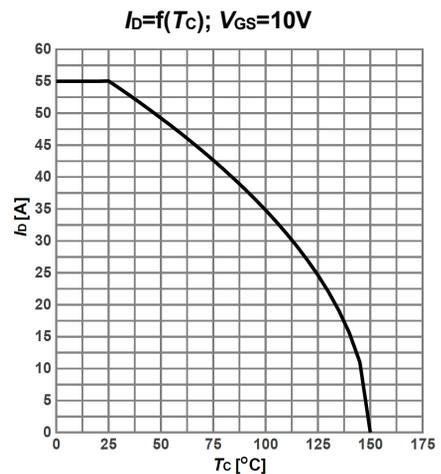
**Figure 10. Typ. forward characteristics of reverse diode**



**Figure 11. Typ. drain-source breakdown voltage**



**Figure 12. Maximum drain current**



### Typical Performance Characteristic

Figure 13. Typ. Capacitances

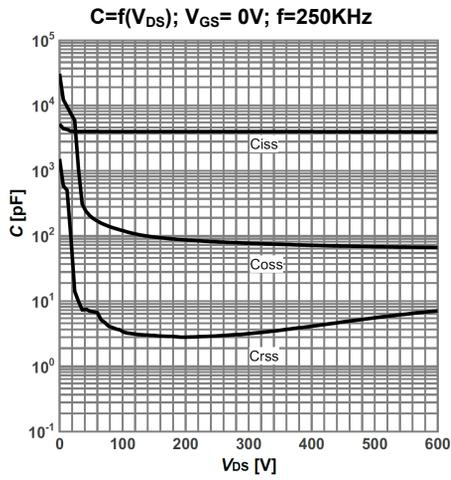
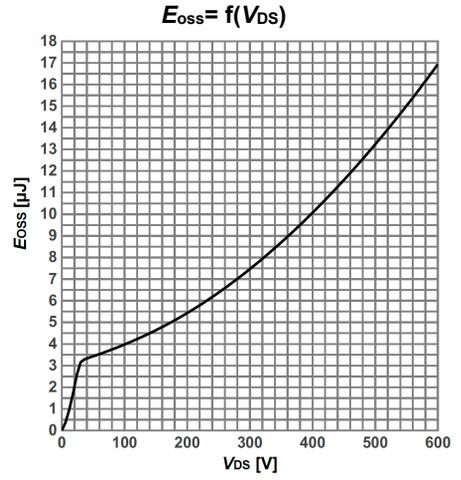
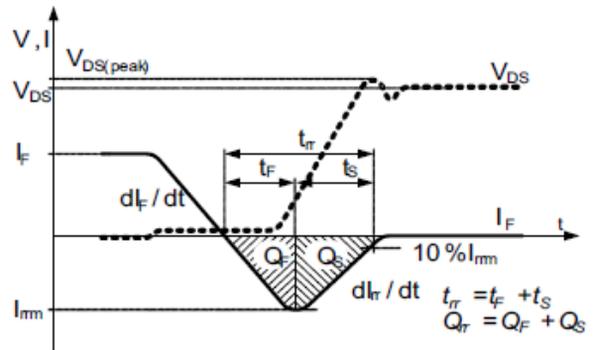
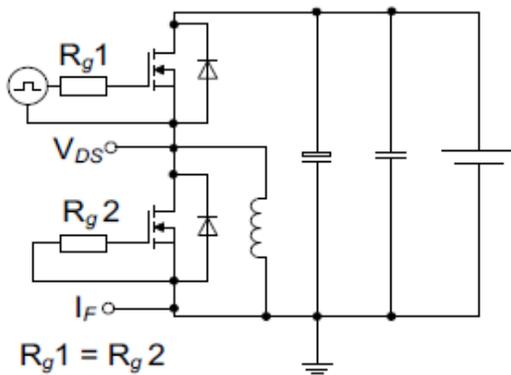


Figure 14. Typ. coss stored energy



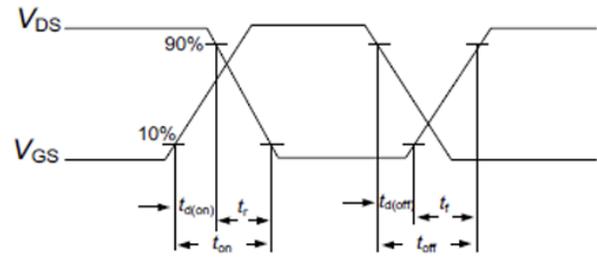
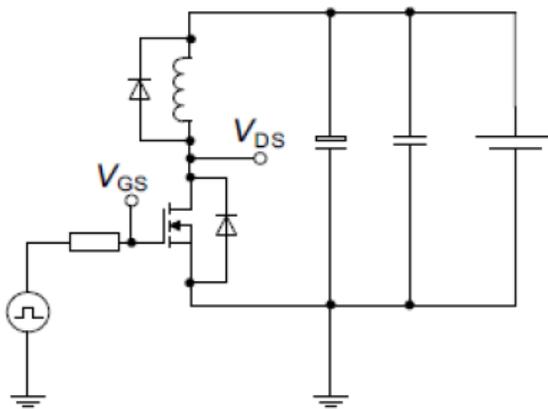
**Figure 15. Diode Characteristics**

Test circuit for diode characteristics and Diode recovery waveform



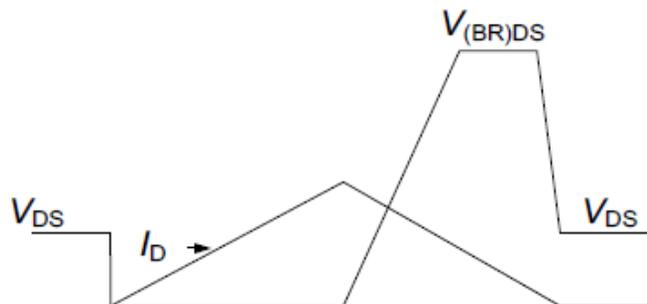
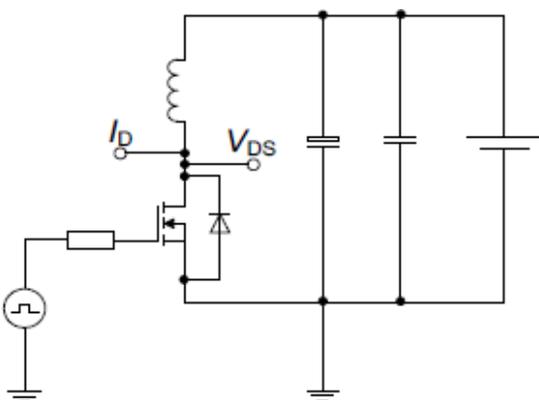
**Figure 16. Switching Times**

Switching times test circuit for inductive load and Switching times waveform



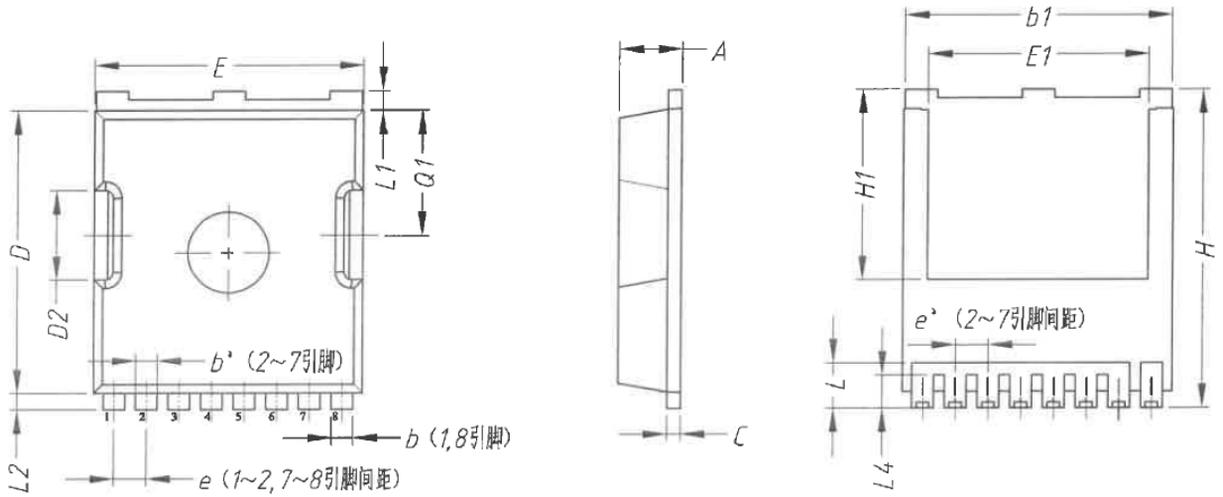
**Figure 17. Unclamped Inductive Load**

Unclamped inductive load test circuit and Unclamped inductive waveform



Package Outlines

TOLL



SYMBOL	MM		
	MIN	NOM	MAX
A	2.15	2.30	2.45
b	0.75	0.75	0.85
b'	0.70	0.70	0.80
b1	9.65	9.80	9.95
C	0.45	0.50	0.60
D	10.18	10.38	10.58
D2	3.15	3.30	3.45
E	9.70	9.90	10.10
E1	7.95	8.10	8.25
e	BSC 1.225		
e'	BSC 1.20		
Q1	4.40	4.55	4.70
H	11.48	11.68	11.88
H1	6.80	6.95	7.10
L	1.60	1.80	2.00
L1	0.50	0.70	0.90
L2	0.48	0.60	0.72
L4	1.00	1.15	1.30

\* Dimensions in millimeters

**Package Marking and Ordering Information**

Part Number	Top Marking	Package	Packing Method	Quantity
BMT65N065UC1	BMT65N065UC1	TOLL	Tape and Reel	1200 Units

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