



## MMIC SURFACE MOUNT

# Power Splitter/Combiner

## GP2Y1A+

50Ω 1550 to 4400 MHz

### THE BIG DEAL

- Wide Bandwidth, 1550 to 4400 MHz
- Excellent Insertion Loss, Typ. 1.1 dB
- Excellent Amplitude Unbalance, Typ. 0.03 dB
- Good Phase Unbalance, Typ. 0.6 Deg
- Power Handling as a Splitter, Max 10W
- 3x3 mm 12-Lead QFN-Style Package

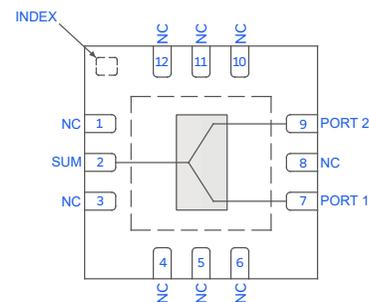


Generic photo used for illustration purposes only

### APPLICATIONS

- Back Haul Radio Systems
- Radar, EW, and ECM Defense Systems
- Satellite Communications

### FUNCTIONAL DIAGRAM



### PRODUCT OVERVIEW

Mini-Circuits' GP2Y1A+ is a 50Ω GaAs MMIC 2-way splitter/combiner that operates from 1550 to 4400 MHz. The GP2Y1A+ provides typical performance of 1.1 dB insertion loss, 22 dB isolation, 0.03 dB amplitude unbalance, and 0.6 deg phase unbalance. In conjunction, it has excellent power handling capabilities of 10 W max as a splitter. This combination of characteristics makes it the perfect device for maintaining signal integrity and low signal distortion during signal splits, while also handling high power RF signals.

### KEY FEATURES

Features	Advantages
Low Insertion Loss, Typ. 1.1 dB (Above 3.0 dB Splitter Loss)	Low insertion loss ensures minimized signal power loss through the device, limiting the need for compensating power requirements at the respective outputs.
Excellent Unbalance <ul style="list-style-type: none"> <li>• Amplitude Unbalance, Typ. 0.03 dB</li> <li>• Phase Unbalance, Typ. 0.6 deg.</li> </ul>	Strong unbalance characteristics allow for low signal distortion and maintaining signal integrity when splitting RF signals between respective outputs.
3x3 mm 12-Lead QFN-Style Package	Small footprint saves space in dense layouts while providing low inductance, repeatable transitions, and excellent thermal contact to the PCB. Industry standard packaging allows for ease of assembly in high volume manufacturing processes.



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### ELECTRICAL SPECIFICATIONS<sup>1</sup> AT +25°C & P<sub>IN</sub> = -10 dBm, UNLESS NOTED OTHERWISE

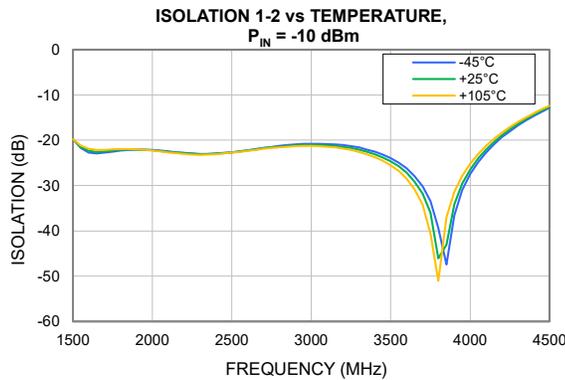
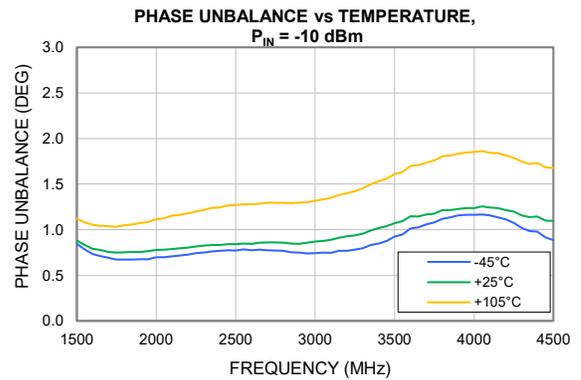
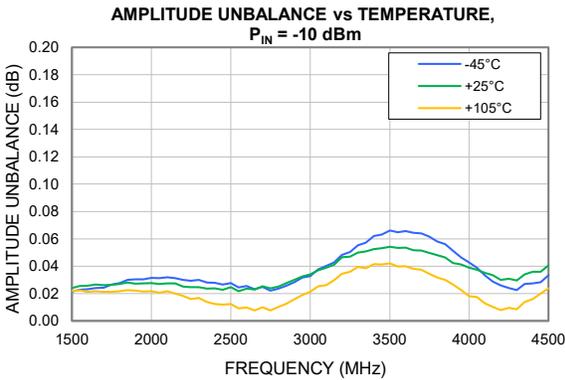
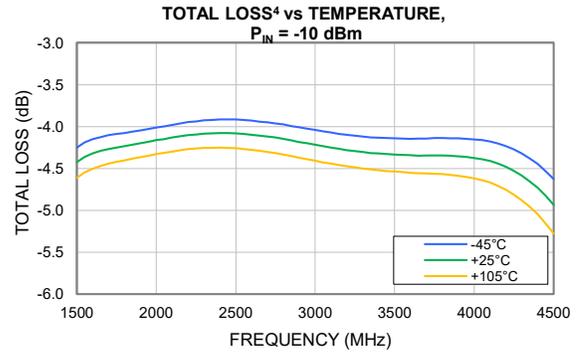
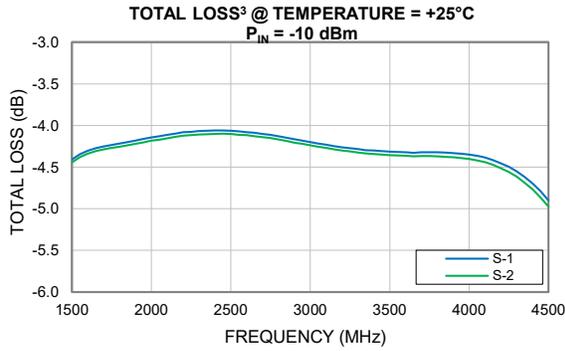
Parameter	Condition (MHz)	Min.	Typ.	Max.	Units
Frequency Range		1550		4400	MHz
Insertion Loss (Above 3.0 dB Splitter Loss)	1550		1.3	1.6	dB
	2500		1.1	1.4	
	3700		1.2	1.6	
	4400		1.6	2.0	
Isolation	1550	17	22		dB
	2500	17	22		
	3700	28	33		
	4400	10	14		
Amplitude Unbalance (±)	1550		0.03	0.2	dB
	2500		0.03	0.2	
	3700		0.05	0.2	
	4400		0.06	0.2	
Phase Unbalance	1550		0.9	3.0	Degrees
	2500		0.8	4.0	
	3700		1.2	4.0	
	4400		1.1	4.0	
Return Loss (Sum Port)	1550		21		dB
	2500		33		
	3700		17		
	4400		18		
Return Loss (Ports 1, 2) <sup>2</sup>	1550		20		dB
	2500		29		
	3700		18		
	4400		21		

1. Tested on Mini-Circuits Characterization Test Board TB-GP2Y1AC+. See Figure 2. Board loss de-embedded to the device.

2. Typical values displayed are the worst case among Port 1 and Port 2.



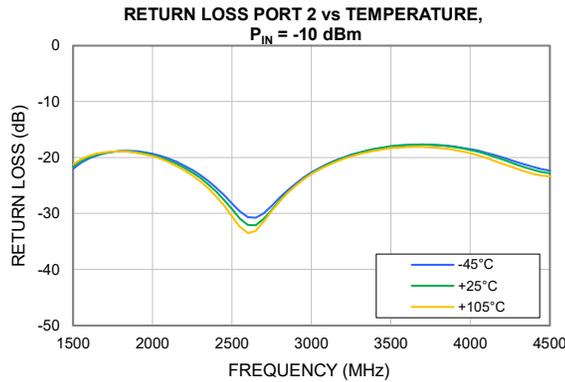
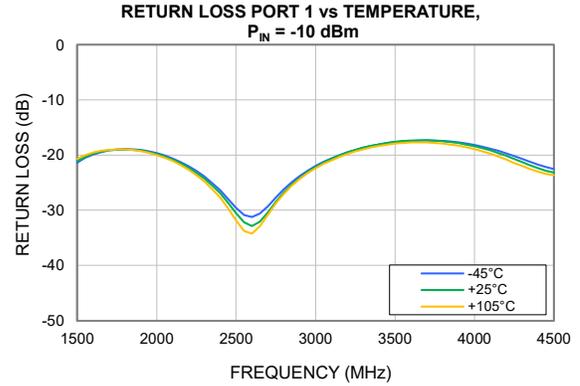
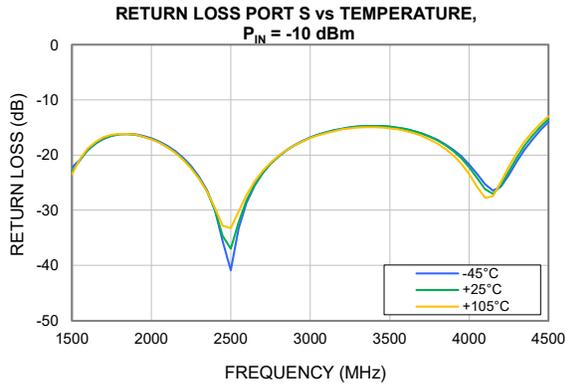
### TYPICAL PERFORMANCE GRAPHS



3. Total Loss = Single Path (S-1 or S-2) Insertion Loss + 3 dB Splitter Loss  
 4. Average of both paths' Total Loss



### TYPICAL PERFORMANCE GRAPHS





### ABSOLUTE MAXIMUM RATINGS<sup>5</sup>

Parameter	Ratings
Operating Temperature	-45 °C to +105 °C
Storage Temperature	-65 °C to +150 °C
Junction Temperature <sup>6</sup>	+150 °C
Power Input (CW)	
<ul style="list-style-type: none"> <li>As a Splitter<sup>7,8</sup></li> <li>As a Combiner<sup>9,10</sup></li> </ul>	+40 dBm +38 dBm

- Permanent damage may occur if any of these limits are exceeded. Maximum ratings are not intended for continuous normal operation.
- Peak temperature on top of Die.
- Tested by applying input power to Port S, measuring output power at Port 1, and presenting both an Open and 50Ω load at Port 2 to determine worst case conditions.
- Max power rating at +25°C and +105°C; no derating required over temperature.
- Tested by applying input power to Port 2, measuring output power at Port 1, and presenting both an Open and 50Ω load at Port S to determine worst case conditions.
- Derates linearly to +31 dBm at +105°C.

### ESD RATING

	Class	Voltage Range	Reference Standard
HBM	1A	250 V to < 500 V	ANSI/ESDA/JEDEC JS-001-2023
CDM	C3	≥ 1000 V	ANSI/ESDA/JEDEC JS-002-2022



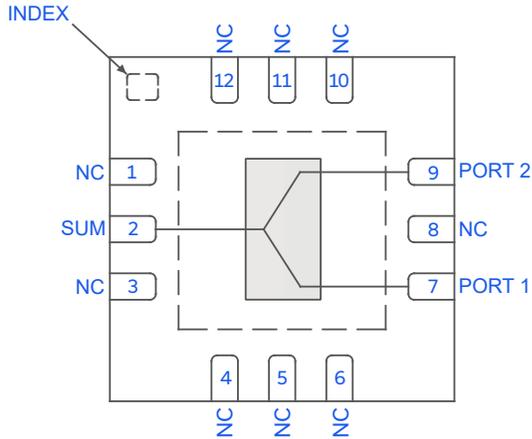
ESD HANDLING PRECAUTION: This device is designed to be Class 1A for HBM. Static charges may easily produce potentials higher than this with improper handling and can discharge into DUT and damage it. As a preventive measure Industry standard ESD handling precautions should be used at all times to protect the device from ESD damage.

### MSL RATING

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020E /JEDEC J-STD-033C



### FUNCTIONAL DIAGRAM



### PAD DESCRIPTION

Function	Pad Number	Description (Refer to Figure 2)
SUM	2	SUM Pad connects to Input Sum Port.
PORT 1	7	PORT 1 Pad connects to Output Port 1.
PORT 2	9	PORT 2 Pad connects to Output Port 2.
NC	1, 3-6, 8, 10-12	Connects to ground on the test board.
GND	PADDLE & INDEX	Connects to ground.

Figure 1. GP2Y1A+ Functional Diagram

### CHARACTERIZATION TEST BOARD

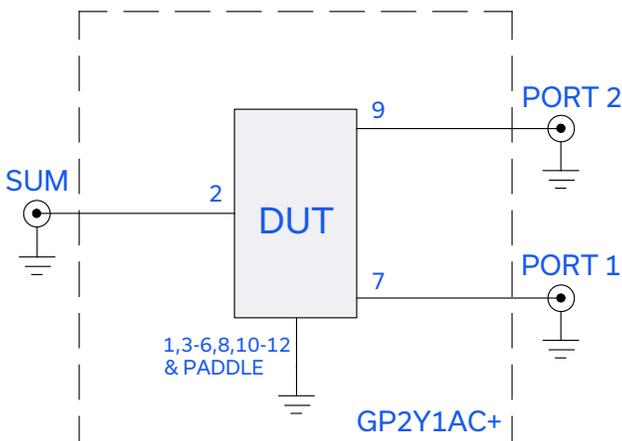


Figure 2. GP2Y1A+ Characterization and Application Circuit.

#### Electrical Parameters and Conditions

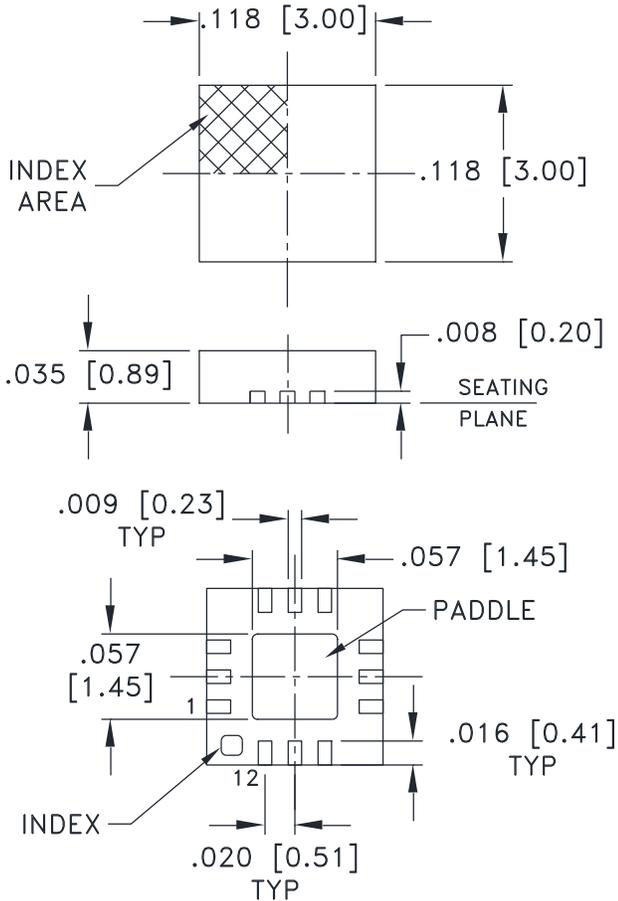
Insertion Loss, Isolation, Return Loss, Phase Unbalance, and Amplitude Unbalance measured using N5242A PNA-X microwave network analyzer.

#### Condition:

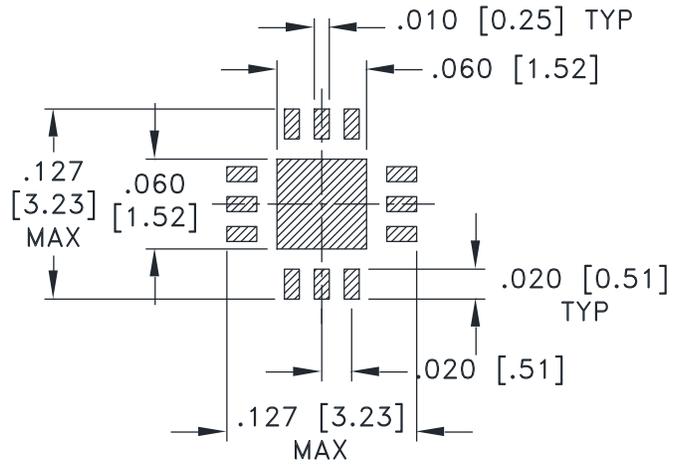
1) Insertion Loss and Return Loss:  $P_{IN} = -10$  dBm



### CASE STYLE DRAWING



### PCB Land Pattern

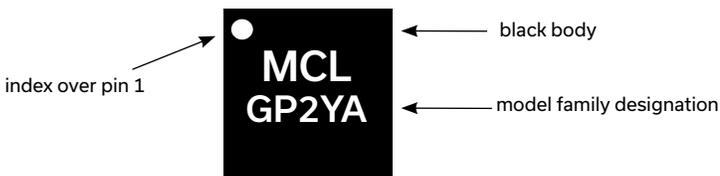


SUGGESTED LAYOUT,  
TOLERANCE TO BE WITHIN ±.002

Weight: .02 Grams

Dimensions are in inches [mm]. Tolerances in inches: 2 Pl. ±.01; 3 Pl.±.004 inches

### PRODUCT MARKING



Marking may contain other features or characters for internal lot control



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ADDITIONAL DETAILED INFORMATION IS AVAILABLE ON OUR DASHBOARD [CLICK HERE](#)

<b>Performance Data &amp; Graphs</b>	Data Graphs S-Parameter (S3P Files) Data Set (.zip file)
<b>Case Style</b>	DQ1225 Plastic package, exposed paddle, Lead Finish: Matte-Tin
<b>RoHS Status</b>	Compliant
<b>Tape &amp; Reel</b> Standard quantities available on reel	F66 7" reels with 20, 50, 100, 200, 500, 1000, 2000, or 3000 devices
<b>Suggested Layout for PCB Design</b>	PL-817
<b>Evaluation Board</b>	TB-GP2Y1AC+ Gerber File
<b>Environmental Ratings</b>	ENV12

### NOTES

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at [www.minicircuits.com/terms/viewterm.html](http://www.minicircuits.com/terms/viewterm.html)

