

750V, 170A, N-channel SiC power MOSFET
General Description:

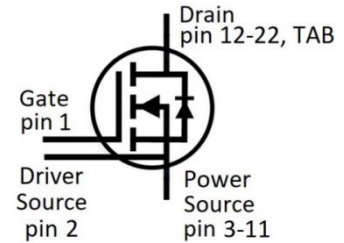
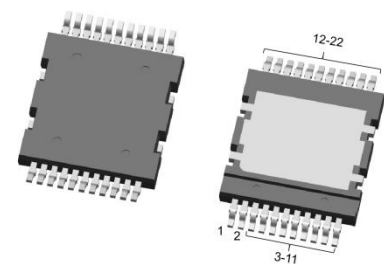
NCES075P013QD is a SiC MOSFET that contributes to miniaturization and low power consumption of applications. This product achieves industry-leading low on-resistance without sacrificing short-circuit withstand time. This is a QDPAK-22L package type with a driver source terminal that can maximize the high-speed switching performance that is a feature of SiC MOSFETs.

Features

- Low on-resistance
- Fast switching speed
- Fast reverse recovery
- Easy to parallel
- Simple to drive
- Pb-free lead plating ; RoHS compliant

Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives


Schematic diagram

QDPAK 22L
Package Marking and Ordering Information

Device	Device Package	Device Marking
NCES075P013QD	QDPAK 22L	NCES075P013QD

Absolute Maximum Ratings (T_c=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	750	V
Gate-Source Voltage,max static voltage	V _{GS}	-10 to +24	V
Gate-Source Voltage,max transient voltage (Note 1)	V _{GS}	-11 to +26	V
Drain Current-Continuous (Note 2)	I _D	170	A
Drain Current-Continuous(T _C =100°C)	I _D (100°C)	129	A
Pulsed Drain Current (Note 3)	I _{DM}	427	A
Maximum Power Dissipation	P _D	T _{vj} = 25°C	600
		T _{vj} = 100°C	300
Recommended turn-on gate - source drive voltage	V _{GS_on}	+15 to +18	V
Recommended turn-off gate - source drive voltage	V _{GS_off}	-4 to 0	V
Virtual junction temperature	T _{vj}	175	°C
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 To 175	°C

Thermal Characteristic

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction to case	0.25	°C/W

Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=100\mu A$	750			V	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=750V, V_{GS}=0V$	$T_{vj} = 25^\circ\text{C}$	1	80	μA	
			$T_{vj} = 175^\circ\text{C}$	2			
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=-10V / +24V, V_{DS}=0V$			± 100	nA	
On Characteristics (Note 4)							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=17mA$	$T_{vj} = 25^\circ\text{C}$	1.9	3.0	4.8	V
			$T_{vj} = 175^\circ\text{C}$		2.1		
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=18V, I_D=60A$	$T_{vj} = 25^\circ\text{C}$	13	17	$m\Omega$	
			$T_{vj} = 175^\circ\text{C}$	15			
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=15V, I_D=60A$	$T_{vj} = 25^\circ\text{C}$	18	24	$m\Omega$	
			$T_{vj} = 175^\circ\text{C}$	21			
Gate input resistance	R_G	$f=1MHz, \text{open drain}$		0.5		Ω	
Forward Transconductance	g_{FS}	$V_{DS}=20V, I_D=60A$		14		S	
Dynamic Characteristics (Note 5)							
Input Capacitance	C_{iss}	$V_{DS}=500V, V_{GS}=0V, f=1MHz$		4980		pF	
Output Capacitance	C_{oss}			445		pF	
Reverse Transfer Capacitance	C_{rss}			15.5		pF	
C_{oss} stored energy	E_{oss}			61.5		μJ	
Output charge	Q_{oss}	Calculated by $C_{oss}(f)V_{DS}@1MHz$		340		nC	
Effective output capacitance, energy related	$C_{o(er)}$	$V_{DS}=0-500V, V_{GS}=0V$		493		pF	
Effective output capacitance, time related	$C_{o(tr)}$	$I_C=\text{constant}, V_{DS}=0-500V, V_{GS}=0V$		680		pF	
Switching Characteristics (Note 5)							
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=500V, I_D=60A, V_{GS}=+18V / -3V, R_G=2.7\Omega, L=200\mu H$		20.8		ns	
Turn-on Rise Time	t_r			31.2		ns	
Turn-Off Delay Time	$t_{d(off)}$			41.4		ns	
Turn-Off Fall Time	t_f			10.8		ns	
Turn-on energy	E_{on}	$V_{DD}=500V, I_D=60A, V_{GS}=+18V / -3V, R_G=2.7\Omega, L=200\mu H$	$T_{vj} = 25^\circ\text{C}$	317		μJ	
			$T_{vj} = 175^\circ\text{C}$	276		μJ	
Turn-off energy	E_{off}	$R_G=2.7\Omega, L=200\mu H$	$T_{vj} = 25^\circ\text{C}$	206		μJ	
			$T_{vj} = 175^\circ\text{C}$	263		μJ	
Total Gate Charge	Q_g	$V_{DS}=500V, I_D=60A, V_{GS}=-3/18V$		186		nC	
Gate-Source Charge	Q_{gs}			54		nC	
Gate-Drain Charge	Q_{gd}			57		nC	

Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 4)	V _{SD}	V _{GS} =0V, I _{SD} =60A	T _{vj} = 25°C		3.4	V
			T _{vj} = 175°C		3.0	
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F =60A, V _R =500V, di/dt = 3930A/μs (Note3)		22.4		ns
Reverse Recovery Charge	Q _{rr}			597		nC
Peak reverse recovery current	I _{rrm}			46.5		A

Notes:

1. t_p < 500ns, D < 0.01
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. PW ≤ 10μs, Duty cycle ≤ 1%
4. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
5. Guaranteed by design, not subject to production

Test Circuit

Fig.1-1 Gate Charge Measurement Circuit

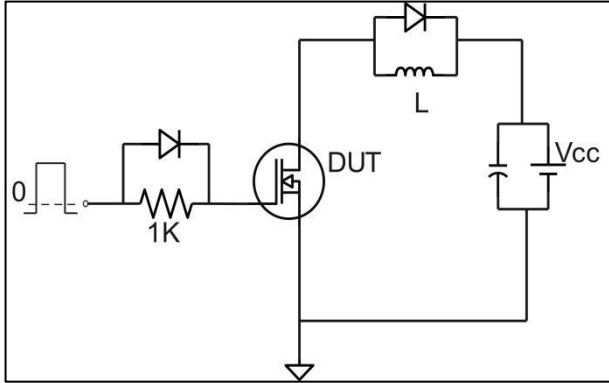


Fig.1-2 Gate Charge Waveform

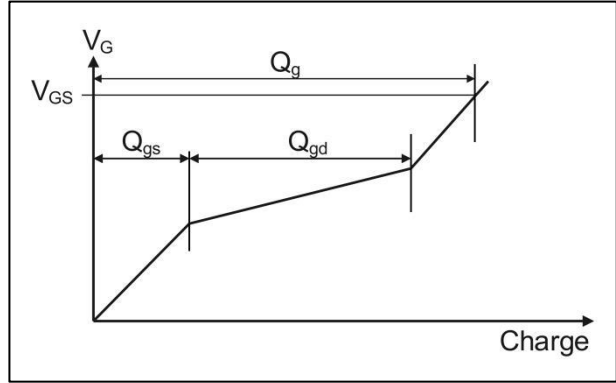


Fig.2-1 Switching Characteristics Measurement Circuit

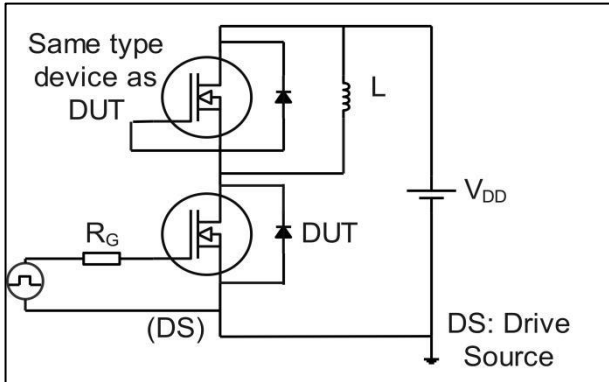
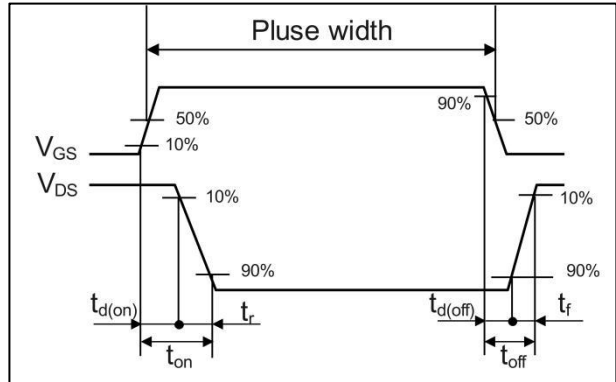


Fig.2-2 Waveforms for Switching Time



Typical Electrical and Thermal Characteristics

Fig.1 Power Dissipation Derating Curve

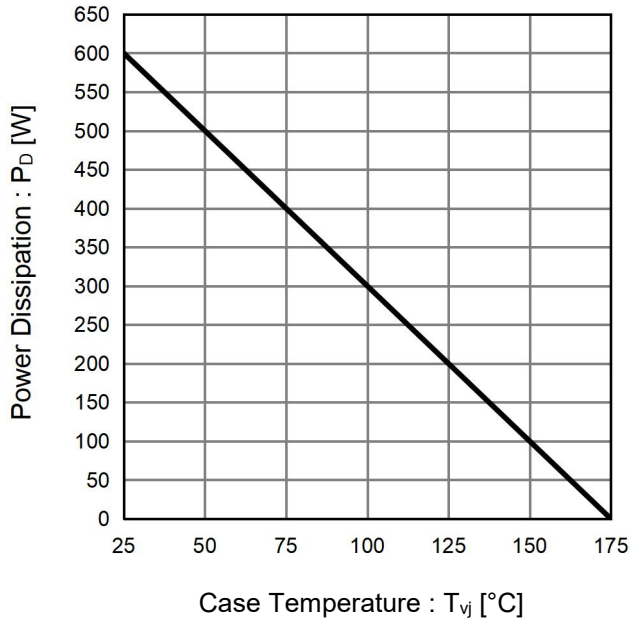


Fig.2 Maximum Safe Operating Area

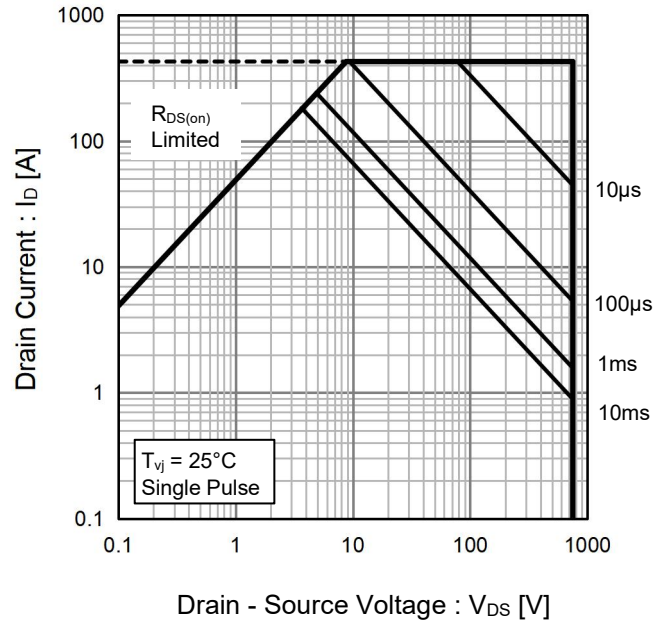


Fig.3 Typical Transient Thermal Impedance vs. Pulse Width

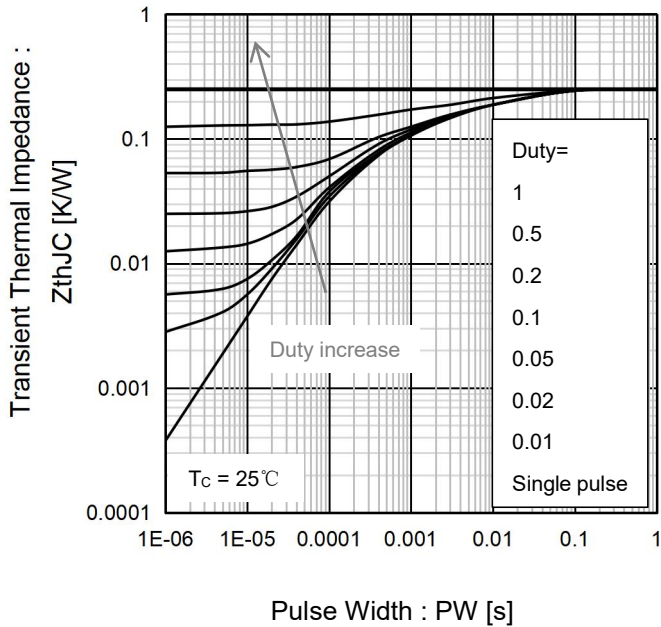


Fig.4 $T_{vj} = 25^{\circ}\text{C}$ Typical Output Characteristics

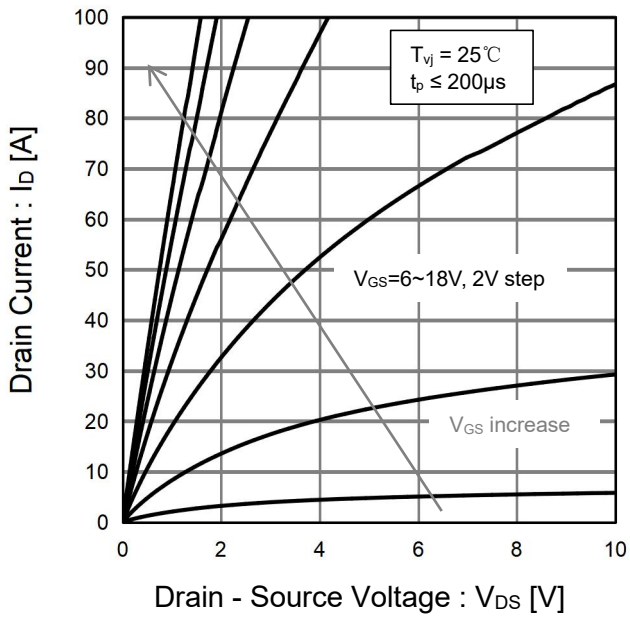


Fig.5 $T_{vj} = 25^{\circ}\text{C}$ 3rd Quadrant Characteristics

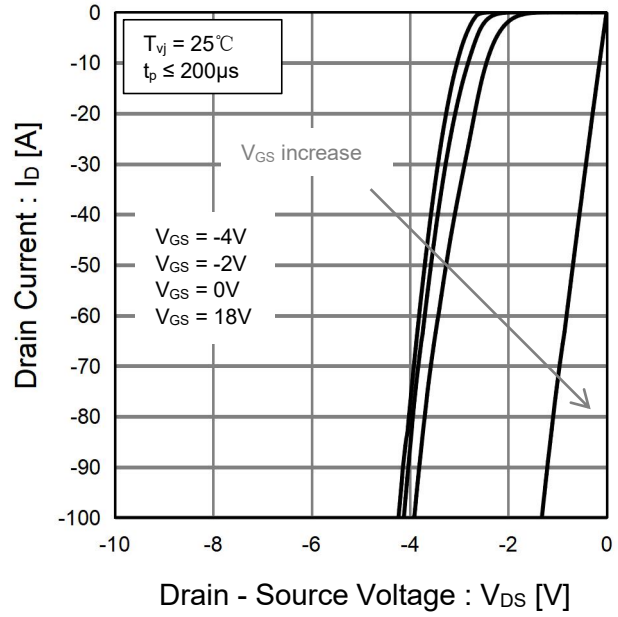


Fig.6 $T_{vj} = 175^{\circ}\text{C}$ Typical Output Characteristics

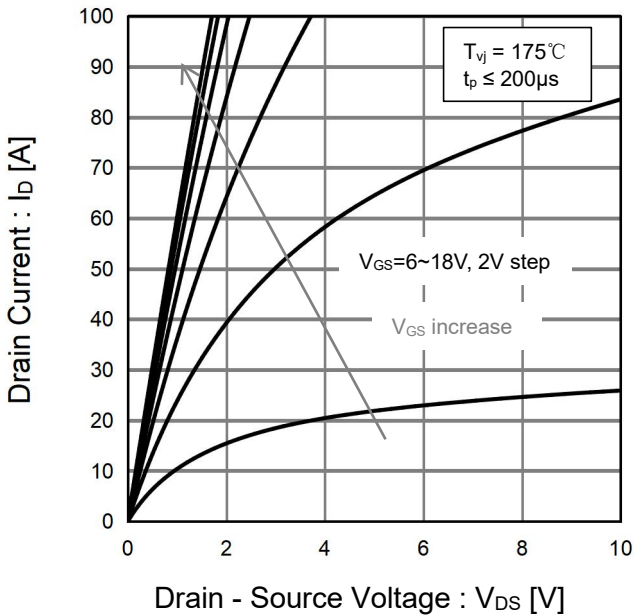


Fig.7 $T_{vj} = 175^{\circ}\text{C}$ 3rd Quadrant Characteristics

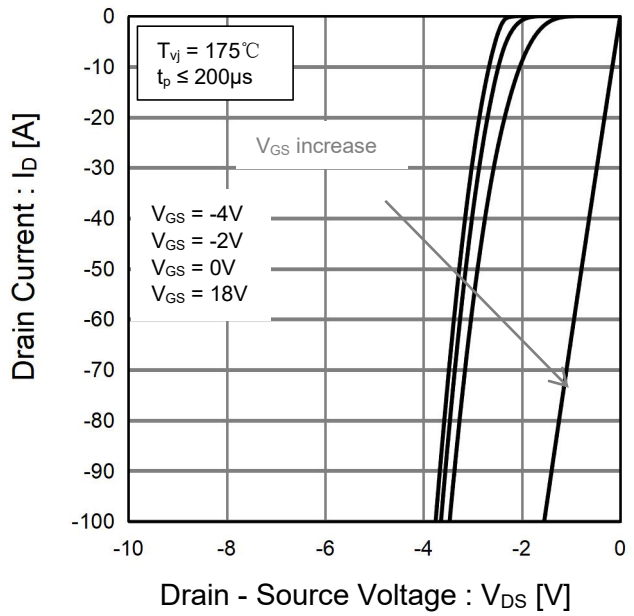


Fig.8 Typical Transfer Characteristics

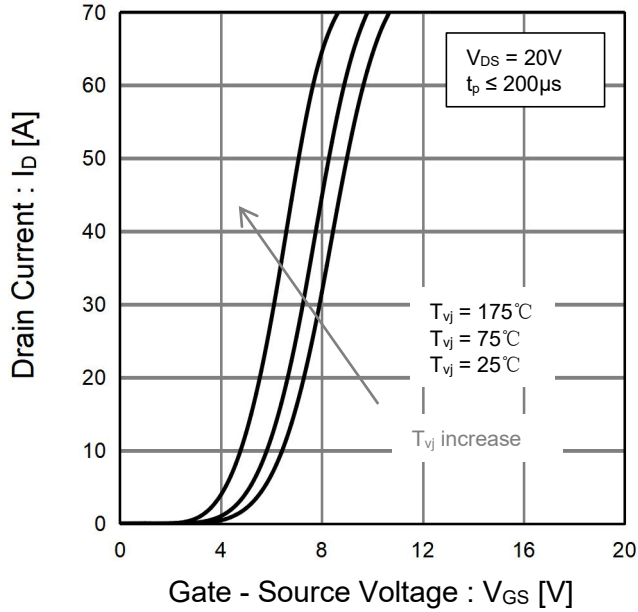


Fig.9 Static Drain - Source On - State Resistance vs. Gate - Source Voltage

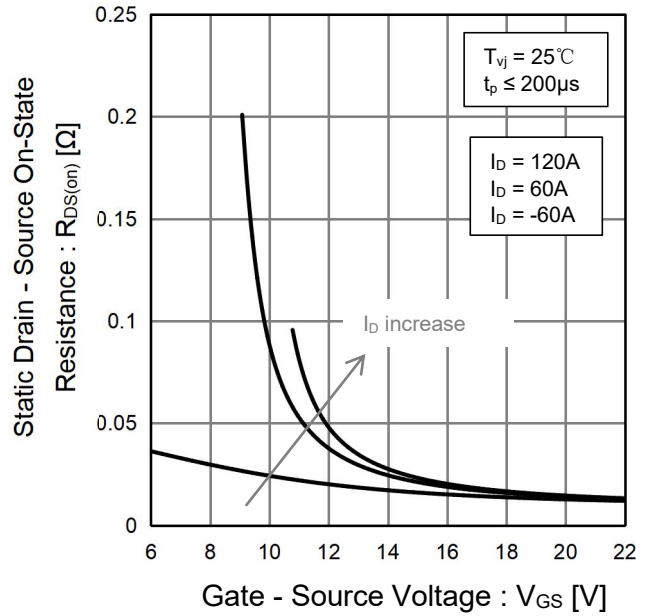


Fig.10 Static Drain - Source On - State Resistance vs. Drain Current

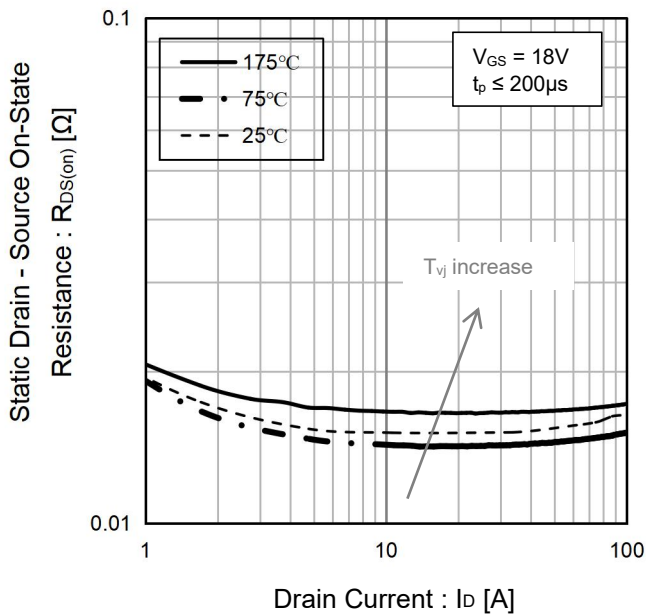


Fig.11 Static Drain - Source On - State Resistance vs. Virtual Junction Temperature

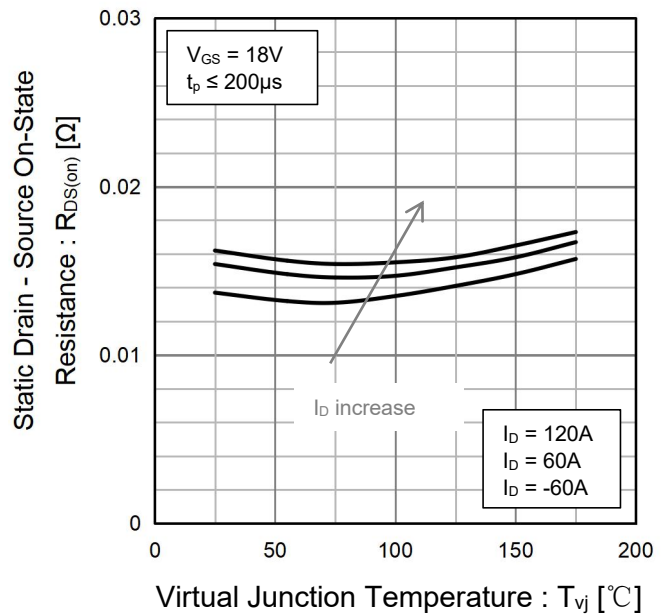


Fig.12 Body Diode Forward Voltage vs. Gate - Source Voltage

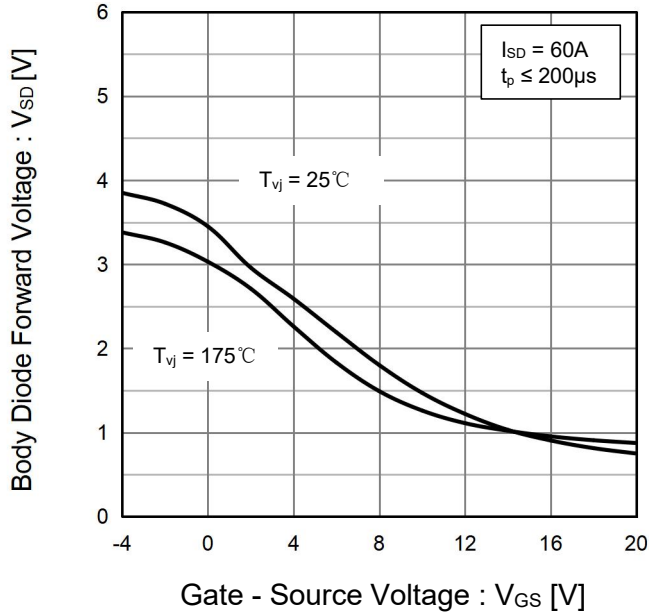


Fig.13 Gate Threshold Voltage vs. Virtual Junction Temperature

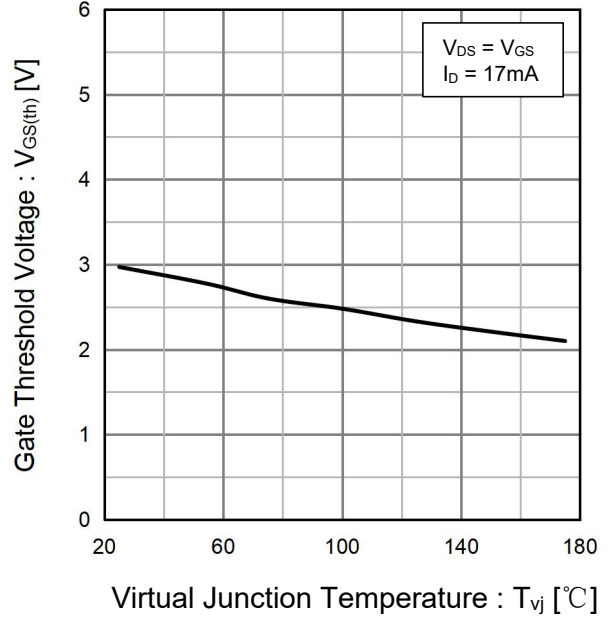


Fig.14 Typical Capacitance vs. Drain - Source Voltage

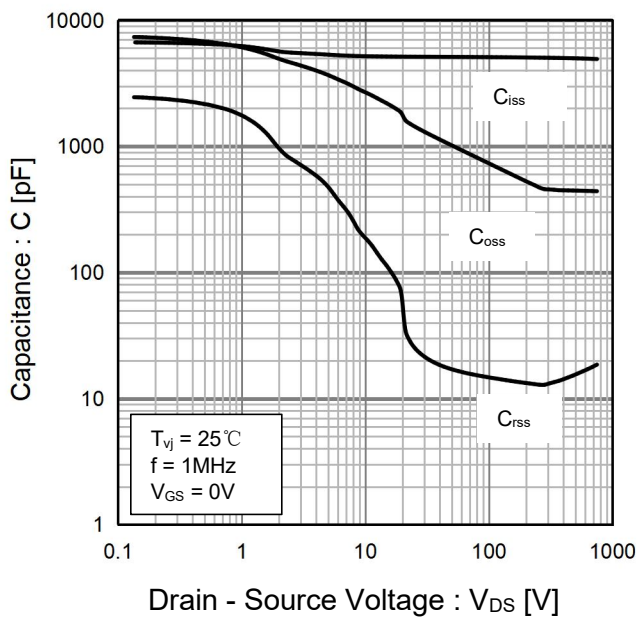


Fig.15 Coss Stored Energy

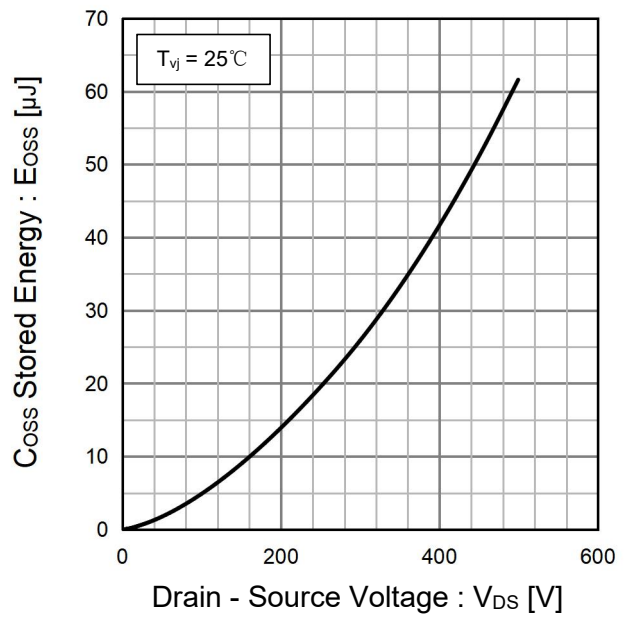


Fig.16 Dynamic Input Characteristics

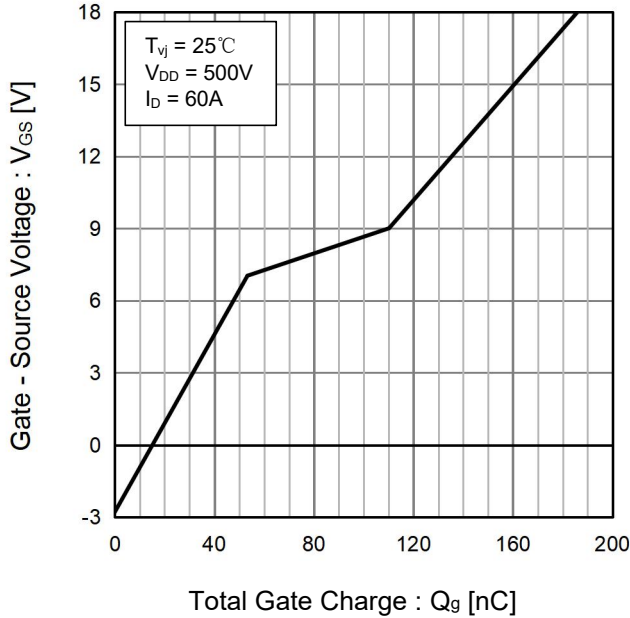


Fig.17 Typical Switching Time vs. External Gate Resistance

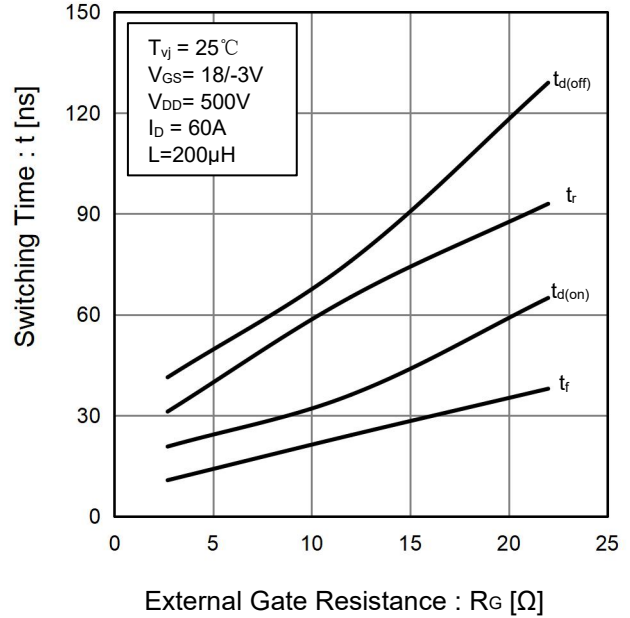


Fig.18 Typical Switching Energy vs. External Gate Resistance

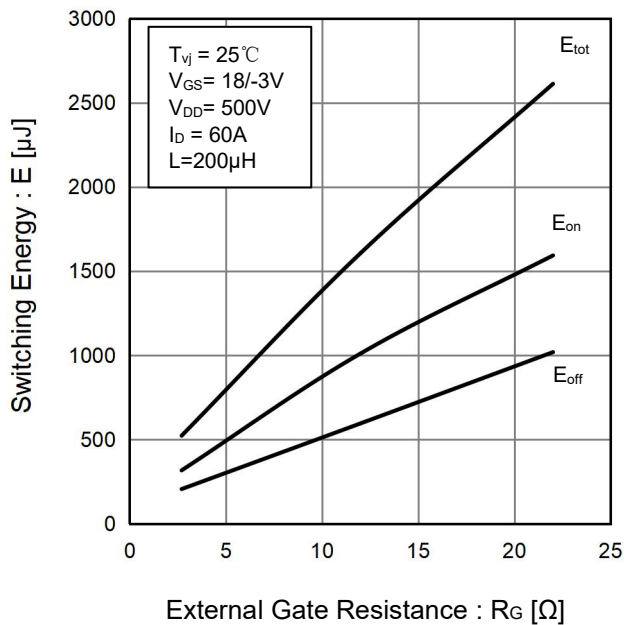


Fig.19 Typical Switching Energy vs. Drain Current

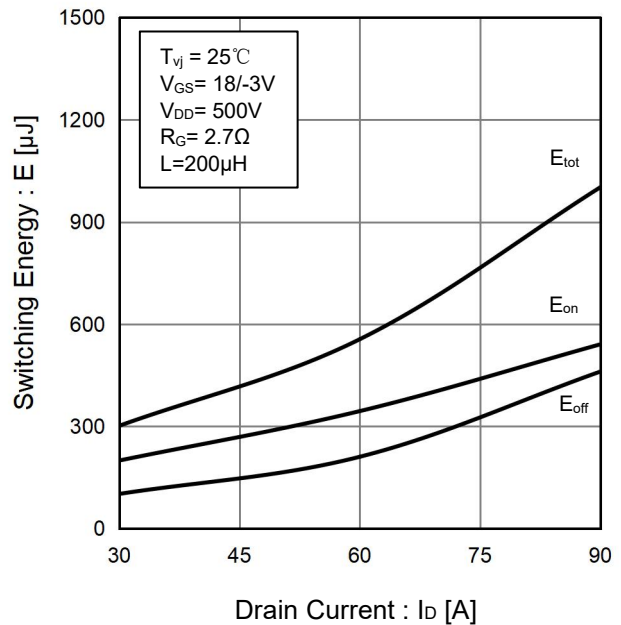
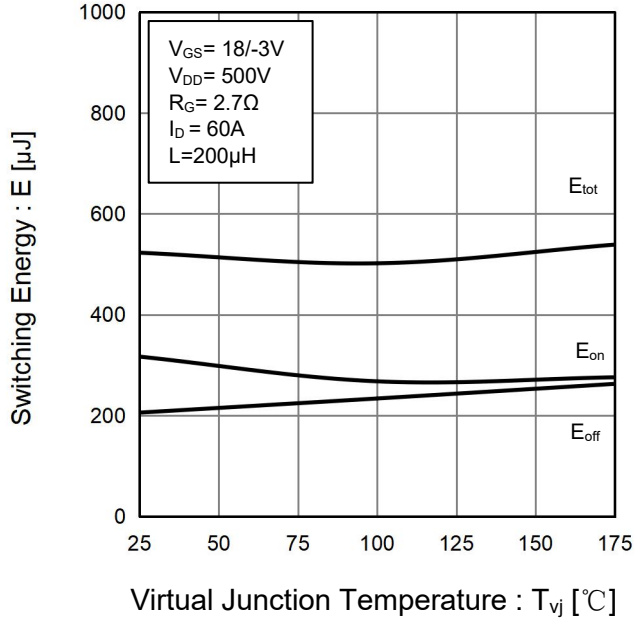
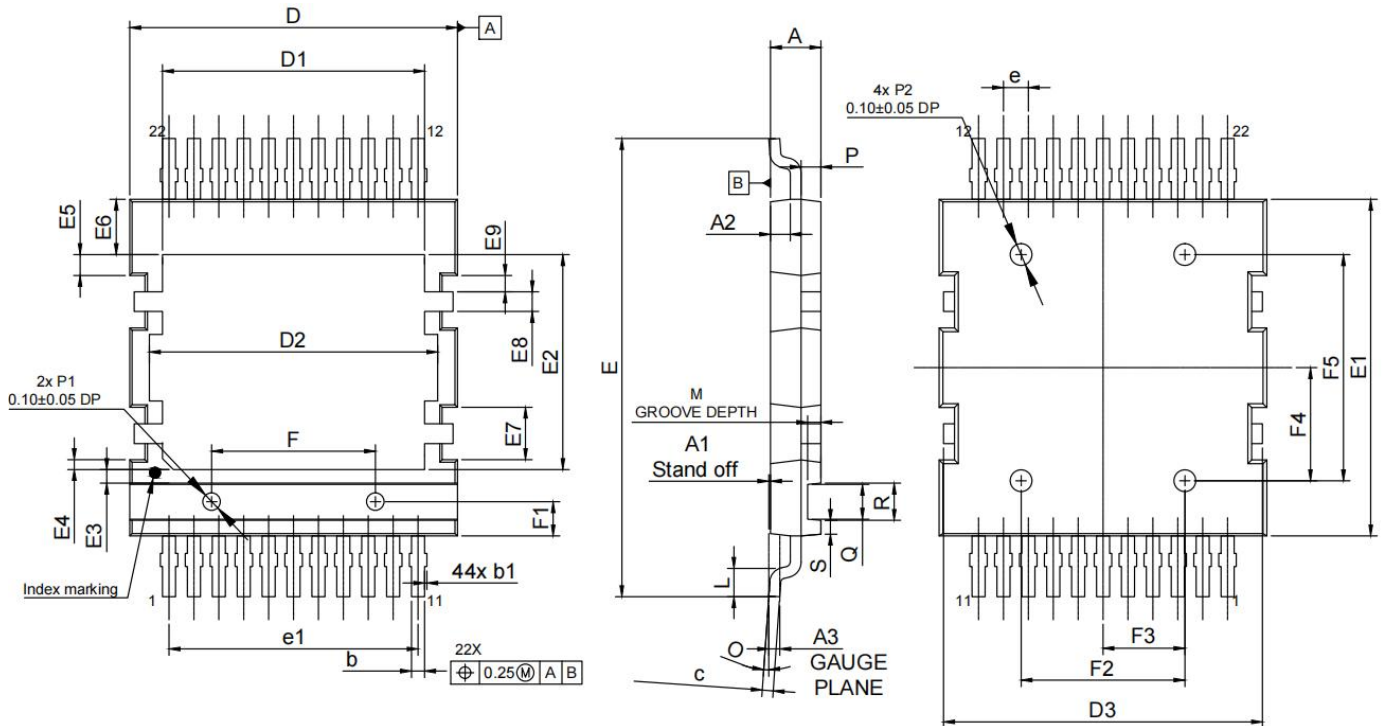


Fig.20 Typical Switching Energy vs. Virtual Junction Temperature



QDPAK 22L Package Information



SYMBOL	MIN	MAX	SYMBOL	MIN	MAX
A	2.25	2.35	E9		0.75
A1	0.00	0.15	e		1.14
A2		0.90	e1		11.4
A3		0.50	F	7.40	7.60
b	0.50	0.70	F1	1.47	1.67
b1	-	0.15	F2	7.40	7.60
c	0.46	0.58	F3	3.65	3.85
D	14.90	15.10	F4	5.07	5.27
D1		12.00	F5	10.24	10.44
D2		13.20	L		1.30
D3	14.50	14.70	M		0.60
E	20.81	21.11	N		22
E1	15.30	15.50	O	0°	8°
E2		9.83	P		0.90
E3		0.625	P1	0.70	0.90
E4		0.45	P2	0.90	1.10
E5		0.95	Q		1.60
E6		2.53	R		1.70
E7		2.40	S		0.631
E8		0.90			

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