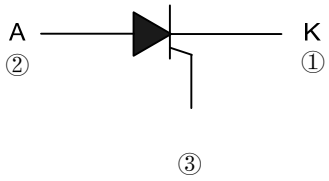
TO-220AB Silicon Controlled Rectifier

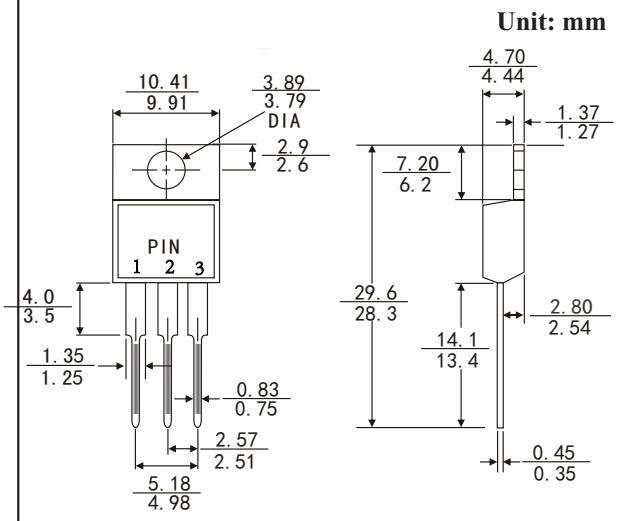
DESCRIPTION:

With high ability to withstand the shock loading of large current, BT152-800R of silicon controlled rectifiers provides high dV/dt rate with strong resistance to electromagnetic interference. It is especially recommended for use on solid state relay, motorcycle, power charger,T-tools etc. From all three terminals to external heatsink.

Package TO-220AB is RoHS compliant.

Symbol





MAXIMUM RATINGS AND CHARACTERISTICS

@ 25°C Ambient Temperature (unless otherwise noted)

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	℃
Operating junction temperature range	T_j	-40-125	℃
Repetitive peak off-state voltage (T_j=25℃)	V_{DRM}	800	V
Repetitive peak reverse voltage (T_j=25℃)	V_{RRM}	800	V
RMS on-state current (T_C≤80℃)	$I_{T(RMS)}$	12	A
Non repetitive surge peak on-state current (full cycle , t_p=20ms , T_j=25℃)	I_{TSM}	120	A
I^2t value for fusing (t_p=10ms , T_j=25℃)	I^2t	144	A^2s
Critical rate of rise of on-state current (T_j=125℃)	dI/dt	50	A/μs
Peak gate current (t_p=20μs , T_j=125℃)	I_{GM}	4	A
Average gate power dissipation (T_j=125℃)	$P_{G(AV)}$	1	W

ELECTRICAL CHARACTERISTICS (T_j=25℃ unless otherwise specified)

Symbol	Test Condition	Value		Unit
I_{GT}	V_D =12V R_L =100Ω	MAX.	15	mA
V_{GT}		MAX.	1.5	V
V_{GD}	V_D =V_{DRM} T_j =125℃ R_L =100Ω	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	MAX.	40	mA
			60	
I_H	I_T =500mA	MAX.	30	mA
dV/dt	V_D=2/3V_{DRM} T_j =125℃	MIN.	500	V/μs

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	I_{TM} =32A	T_j=25℃	1.75	V
V_{TO}	Threshold voltage	T_j=125℃	0.86	V
R_D	Dynamic resistance	T_j=125℃	36.6	mΩ
I_{DRM}	V_D =V_{DRM} V_R =V_{RRM}	T_j=25℃	5	μA
I_{RRM}		T_j=125℃	1	mA

TYPICAL CHARACTERISTICS

FIG.1 Maximum power dissipation versus RMS on-state current

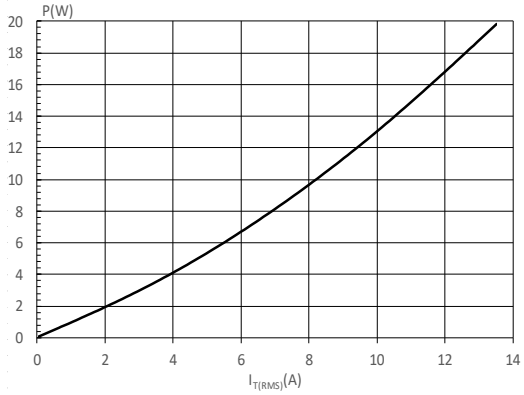


FIG.2: RMS on-state current versus case temperature

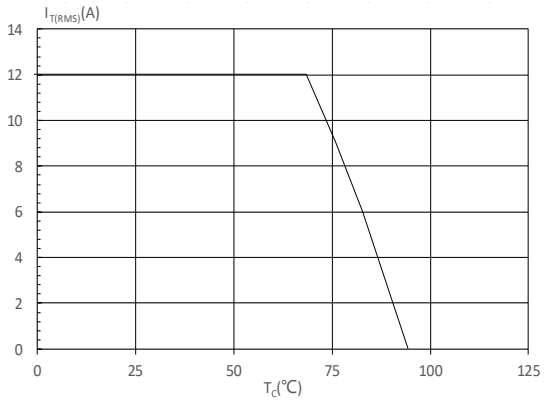


FIG.3: Surge peak on-state current versus number of cycles

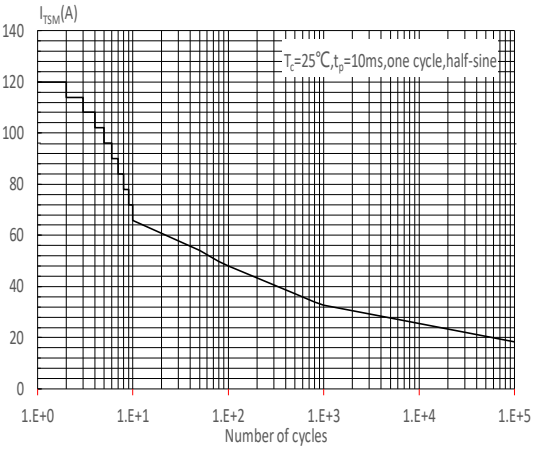


FIG.4: On-state characteristics

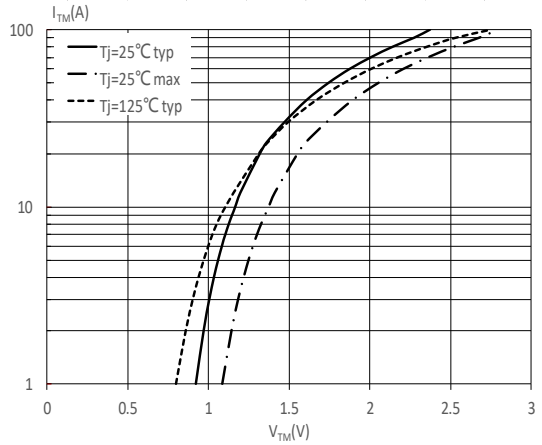


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width t_p<10ms, and corresponding value of I^2t (dI/dt<100A/μs)

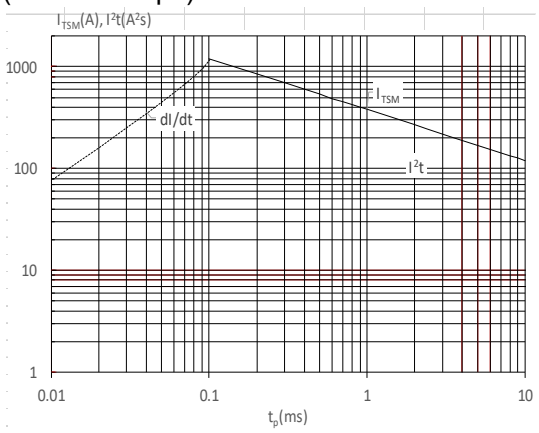


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature