

# CMH60N50

500V, 88mΩ typ., 60A N-Channel MOSFET

## General Description

These Power MOSFETs are produced using Cmos's proprietary, planar stripe, DMOS technology. These devices are well suited for high efficient switched mode power supplies and active power factor correction.

## Features

- 100% avalanche tested
- Improved dv/dt capability
- RoHS Compliant

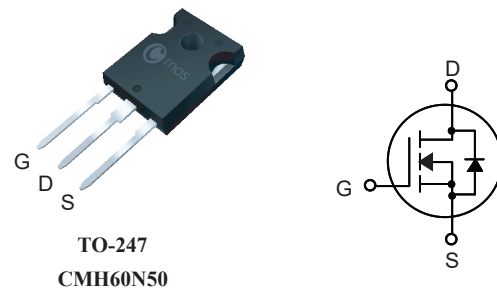
## Product Summary

BVDSS	R <sub>DS(on)</sub> max.	ID
500V	100mΩ	60A

## Applications

- Switch Mode Power Supply
- Uninterruptable Power Supply

## TO-247 Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	500	V
V <sub>GS</sub>	Gate-Source Voltage	±30	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current	60	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current	36	A
I <sub>DM</sub>	Pulsed Drain Current	240	A
EAS	Single Pulse Avalanche Energy <small>(Note 1)</small>	6125	mJ
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation	625	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	150	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient	---	40	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-case	---	0.2	°C/W

**Electrical Characteristics ( $T_J=25^{\circ}\text{C}$  , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	500	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=40A$	---	88	100	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	2	---	4	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=500V, V_{GS}=0V$	---	---	1	uA
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 30V, V_{DS}=0V$	---	---	±100	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=10V, I_D=20A$	---	51	---	S
$R_g$	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	0.9	---	Ω
$Q_g$	Total Gate Charge	$V_{DD}=400V, I_D=60A$ $V_{GS}=10V$	---	193	---	nC
$Q_{gs}$	Gate-Source Charge		---	43	---	
$Q_{gd}$	Gate-Drain Charge		---	63	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=250V, I_D=23A$ $R_G=25\Omega$	---	102	---	ns
$T_r$	Rise Time		---	56	---	
$T_{d(off)}$	Turn-Off Delay Time		---	476	---	
$T_f$	Fall Time		---	79	---	
$C_{iss}$	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, f=1\text{MHz}$	---	9900	---	pF
$C_{oss}$	Output Capacitance		---	750	---	
$C_{rss}$	Reverse Transfer Capacitance		---	50	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current	---	---	60	A
$I_{SM}$	Pulsed Source Current		---	---	240	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=80A$	---	0.94	1.5	V
$t_{rr}$	Reverse Recovery Time	$I_S=50A, V_{GS}=0V$	---	602	---	ns
$Q_{rr}$	Reverse Recovery Charge	$di/dt=100A/\mu s$ (note 2,3)	---	6.35	---	μC

Note :

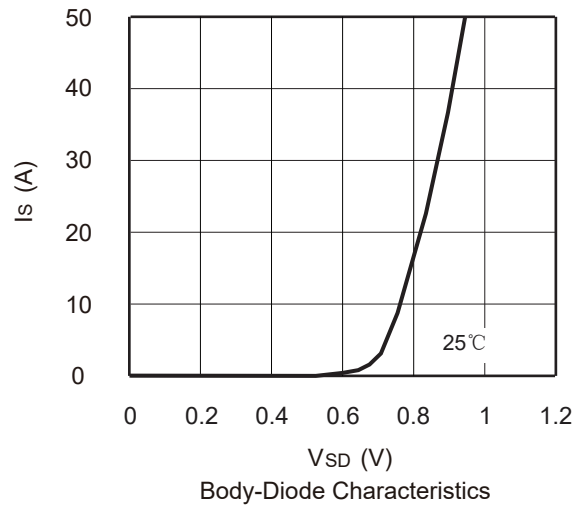
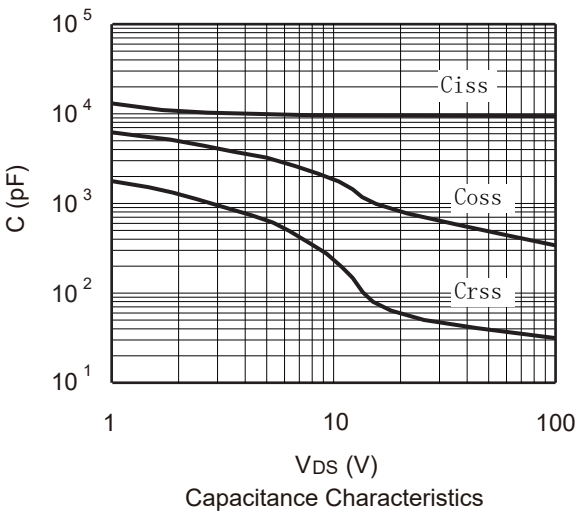
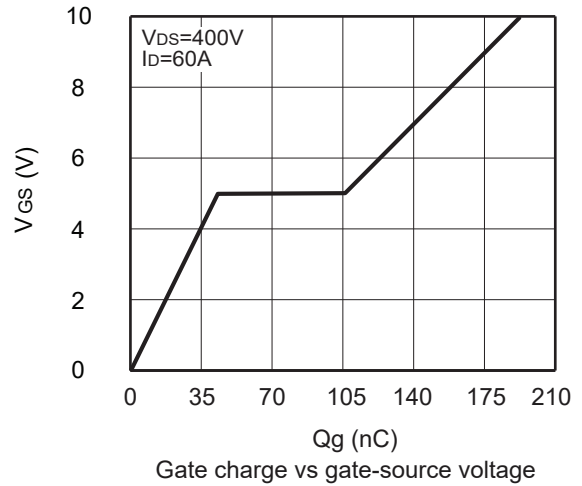
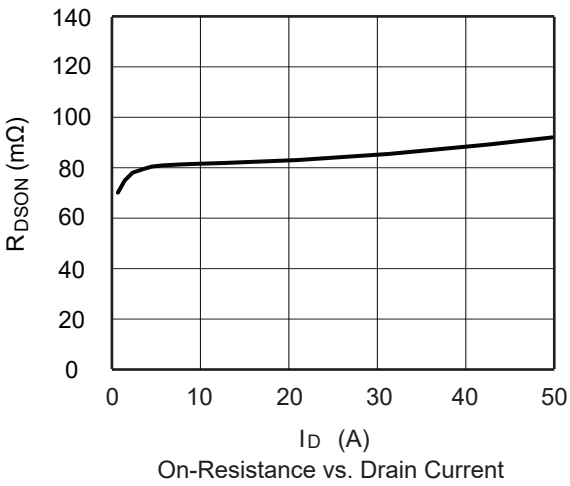
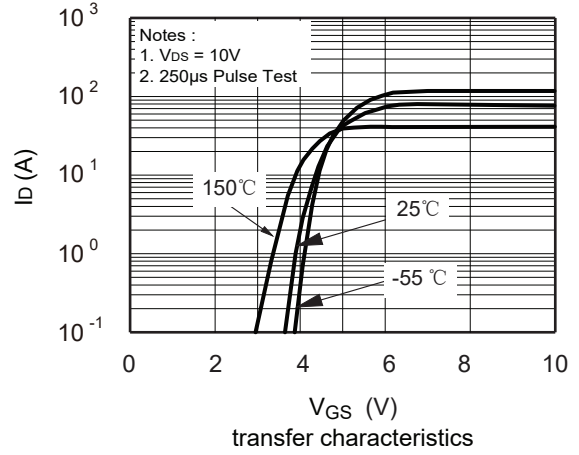
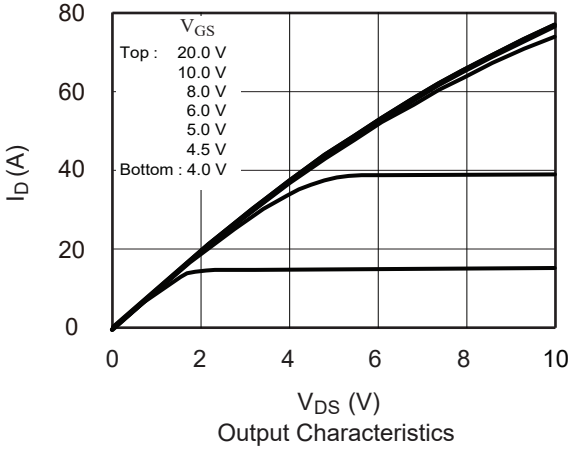
1. The EAS data shows Max. rating .The test condition is  $V_{DS}=150V, V_{GS}=10V, L=10mH, I_{AS}=35A$ .
2. Pulse test: Pulse width≤300us, Duty cycle≤2%.
3. Essentially independent of operating temperature typical characteristics.

This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.Please refer to the latest version of specification.

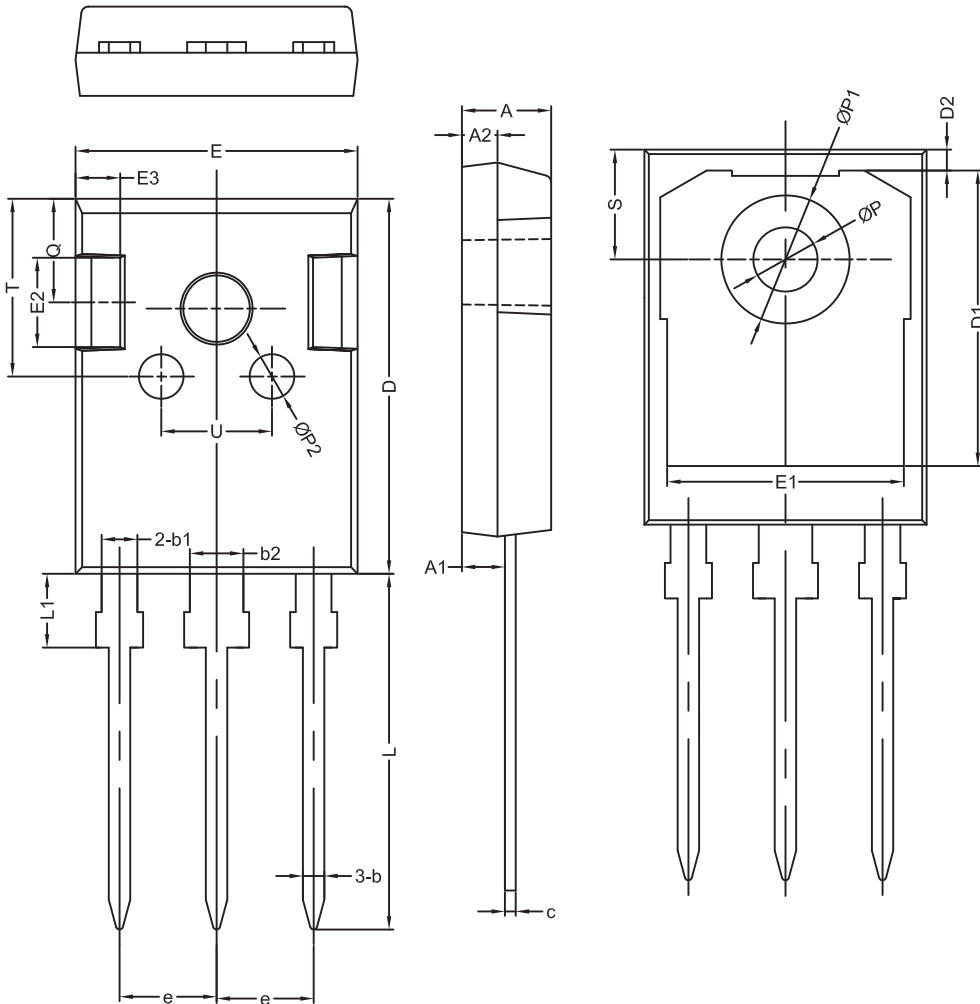
Typical Characteristics



Package Dimension

TO-247

Unit :mm



符号	机械尺寸/mm			符号	机械尺寸/mm		
	最小值	典型值	最大值		最小值	典型值	最大值
A	4.80	5.00	5.20	E2		5.00	
A1	2.21	2.41	2.61	E3		2.50	
A2	1.90	2.00	2.10	e		5.44	
b	1.10	1.20	1.35	L	19.42	19.92	20.42
b1		2.00		L1		4.13	
b2		3.00		P	3.50	3.60	3.70
c	0.55	0.60	0.75	P1		7.19	
D	20.80	21.00	21.20	P2		2.50	
D1		16.55		Q		5.80	
D2		1.20		S	6.05	6.15	6.25
E	15.60	15.80	16.0	T		10.00	
E1		13.30		U		6.20	