

**750V, 160A, N-channel SiC power MOSFET**
**General Description:**

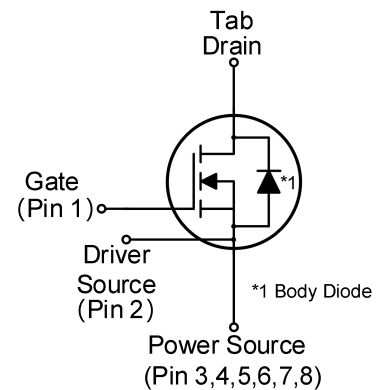
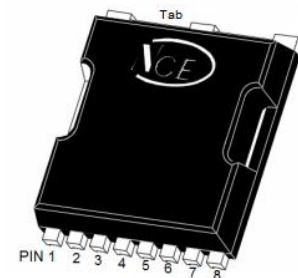
NCES075P013LL is a SiC MOSFET that contributes to miniaturization and low power consumption of applications. This product achieves industry-leading low on-resistance without sacrificing short-circuit withstand time. This is a TOLL package type with a driver source terminal that can maximize the high-speed switching performance that is a feature of SiC MOSFETs.

**Features**

- Low on-resistance
- Fast switching speed
- Fast reverse recovery
- Easy to parallel
- Simple to drive
- Pb-free lead plating ; RoHS compliant

**Application**

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives


**Schematic diagram**

**TOLL**
**Package Marking and Ordering Information**

Device	Device Package	Device Marking
NCES075P013LL	TOLL	NCES075P013LL

**Absolute Maximum Ratings (T<sub>c</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	750	V
Gate-Source Voltage,max static voltage	V <sub>GS</sub>	-10 to +24	V
Gate-Source Voltage,max transient voltage (Note 1)	V <sub>GS</sub>	-11 to +26	V
Drain Current-Continuous (Note 2)	I <sub>D</sub>	160	A
Drain Current-Continuous(T <sub>C</sub> =100°C)	I <sub>D</sub> (100°C)	114	A
Pulsed Drain Current (Note 3)	I <sub>DM</sub>	403	A
Maximum Power Dissipation	P <sub>D</sub>	T <sub>vj</sub> = 25°C	535
		T <sub>vj</sub> = 100°C	267
Recommended turn-on gate - source drive voltage	V <sub>GS_on</sub>	+15 to +18	V
Recommended turn-off gate - source drive voltage	V <sub>GS_off</sub>	-4 to 0	V
Virtual junction temperature	T <sub>vj</sub>	175	°C
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 To 175	°C

**Thermal Characteristic**

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction to case	0.28	°C/W

**Electrical Characteristics ( $T_c=25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit	
<b>Off Characteristics</b>							
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=100\mu A$	750			V	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=750V, V_{GS}=0V$	$T_{vj} = 25^\circ\text{C}$	1	80	$\mu A$	
			$T_{vj} = 175^\circ\text{C}$	2			
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=-10V / +24V, V_{DS}=0V$			$\pm 100$	nA	
<b>On Characteristics (Note 4)</b>							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=17mA$	$T_{vj} = 25^\circ\text{C}$	1.9	3.0	4.8	V
			$T_{vj} = 175^\circ\text{C}$		2.1		
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=18V, I_D=60A$	$T_{vj} = 25^\circ\text{C}$		13	17	m $\Omega$
			$T_{vj} = 175^\circ\text{C}$		15		
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=15V, I_D=60A$	$T_{vj} = 25^\circ\text{C}$		18	24	
			$T_{vj} = 175^\circ\text{C}$		21		
Gate input resistance	$R_G$	$f=1MHz, \text{open drain}$		0.5		$\Omega$	
Forward Transconductance	$g_{FS}$	$V_{DS}=20V, I_D=60A$		14		S	
<b>Dynamic Characteristics (Note 5)</b>							
Input Capacitance	$C_{iss}$	$V_{DS}=500V, V_{GS}=0V, f=1MHz$		4980		pF	
Output Capacitance	$C_{oss}$			445		pF	
Reverse Transfer Capacitance	$C_{rss}$			15.5		pF	
$C_{oss}$ stored energy	$E_{oss}$			61.5		$\mu J$	
Output charge	$Q_{oss}$	Calculated by $C_{oss}(f)V_{DS}@1MHz$		340		nC	
Effective output capacitance, energy related	$C_{o(er)}$	$V_{DS}=0-500V, V_{GS}=0V$		493		pF	
Effective output capacitance, time related	$C_{o(tr)}$	$I_C=\text{constant}, V_{DS}=0-500V, V_{GS}=0V$		680		pF	
<b>Switching Characteristics (Note 5)</b>							
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=500V, I_D=60A, V_{GS}=+18V / -3V, R_G=2.7\Omega, L=200\mu H$		20.8		ns	
Turn-on Rise Time	$t_r$			31.2		ns	
Turn-Off Delay Time	$t_{d(off)}$			41.4		ns	
Turn-Off Fall Time	$t_f$			10.8		ns	
Turn-on energy	$E_{on}$	$V_{DD}=500V, I_D=60A, V_{GS}=+18V / -3V, R_G=2.7\Omega, L=200\mu H$	$T_{vj} = 25^\circ\text{C}$		317	$\mu J$	
			$T_{vj} = 175^\circ\text{C}$		276	$\mu J$	
Turn-off energy	$E_{off}$	$R_G=2.7\Omega, L=200\mu H$	$T_{vj} = 25^\circ\text{C}$		206	$\mu J$	
			$T_{vj} = 175^\circ\text{C}$		263	$\mu J$	
Total Gate Charge	$Q_g$	$V_{DS}=500V, I_D=60A, V_{GS}=-3/18V$		186		nC	
Gate-Source Charge	$Q_{gs}$			54		nC	
Gate-Drain Charge	$Q_{gd}$			57		nC	

**Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)**

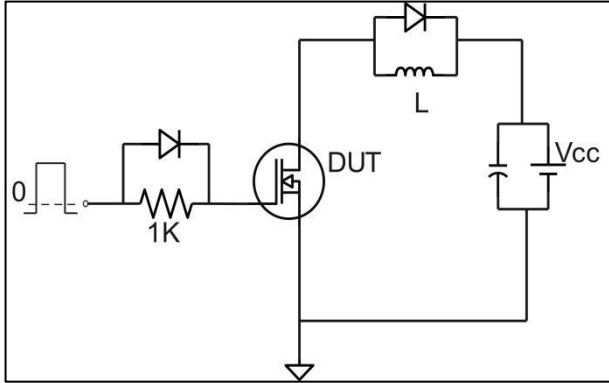
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 4)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>SD</sub> =60A	T <sub>vj</sub> = 25°C		3.4	V
			T <sub>vj</sub> = 175°C		3.0	
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> =60A, V <sub>R</sub> =500V, di/dt = 3930A/μs (Note3)		22.4		ns
Reverse Recovery Charge	Q <sub>rr</sub>			597		nC
Peak reverse recovery current	I <sub>rrm</sub>			46.5		A

**Notes:**

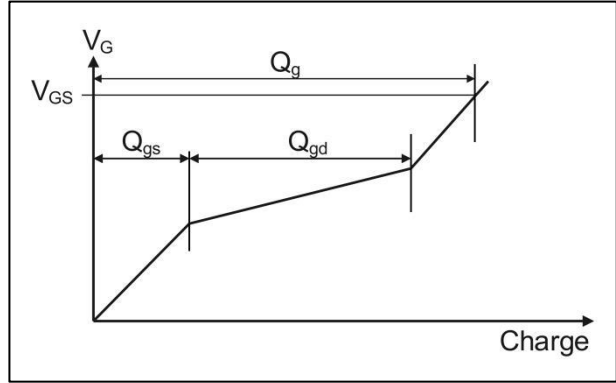
1. t<sub>p</sub> < 500ns, D < 0.01
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. PW ≤ 10μs, Duty cycle ≤ 1%
4. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
5. Guaranteed by design, not subject to production

## Test Circuit

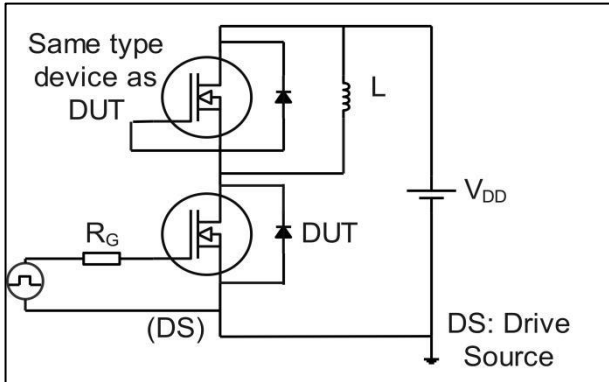
**Fig.1-1 Gate Charge Measurement Circuit**



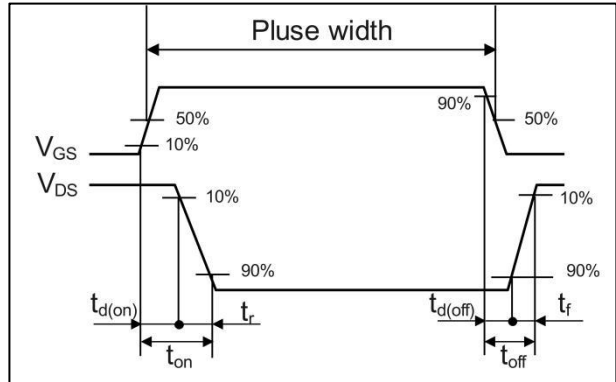
**Fig.1-2 Gate Charge Waveform**



**Fig.2-1 Switching Characteristics Measurement Circuit**



**Fig.2-2 Waveforms for Switching Time**



Typical Electrical and Thermal Characteristics

Fig.1 Power Dissipation Derating Curve

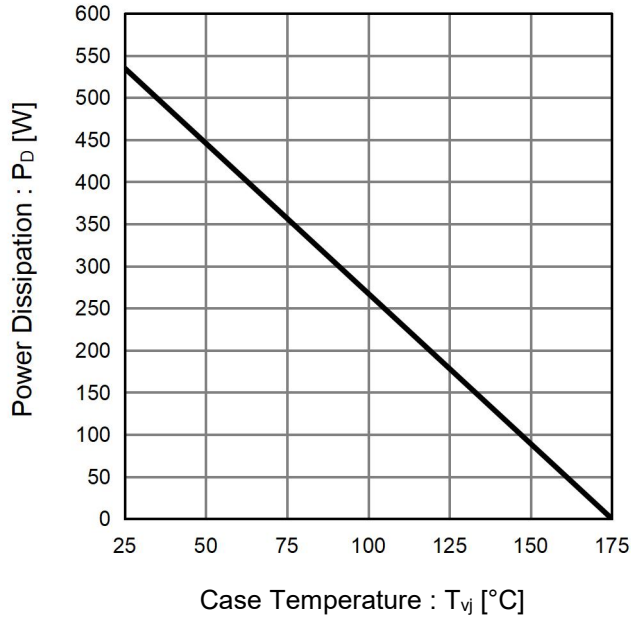


Fig.2 Maximum Safe Operating Area

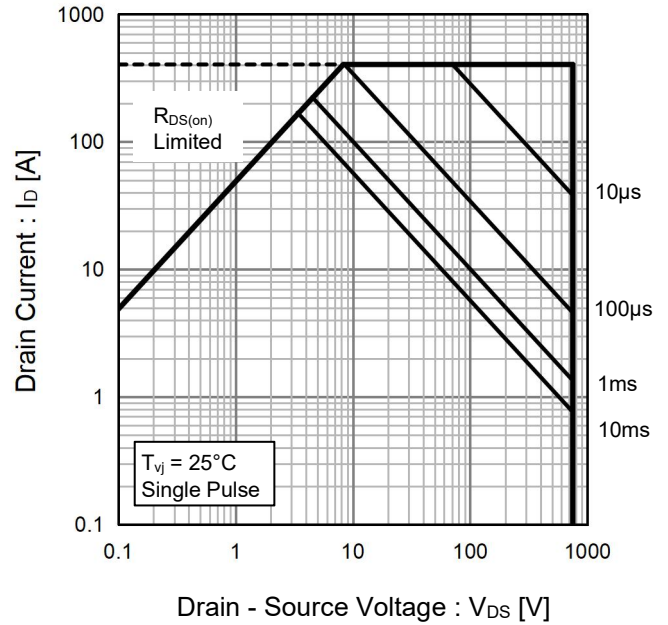


Fig.3 Typical Transient Thermal Impedance vs. Pulse Width

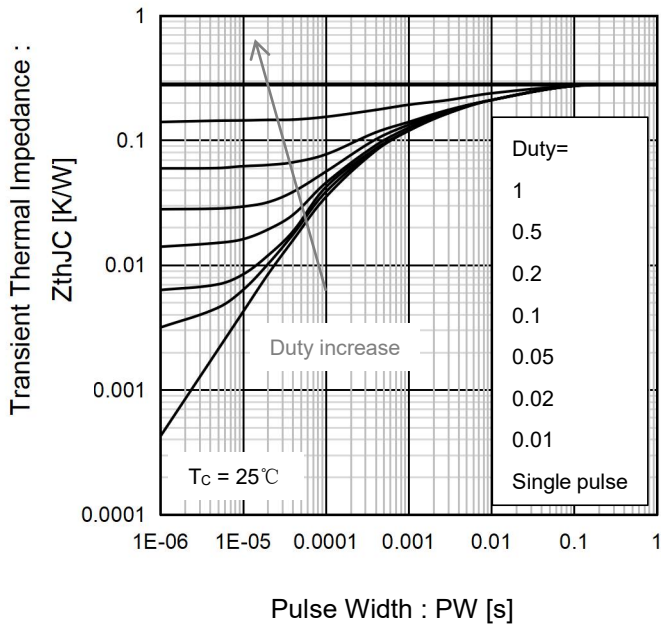


Fig.4  $T_{vj} = 25^{\circ}\text{C}$  Typical Output Characteristics

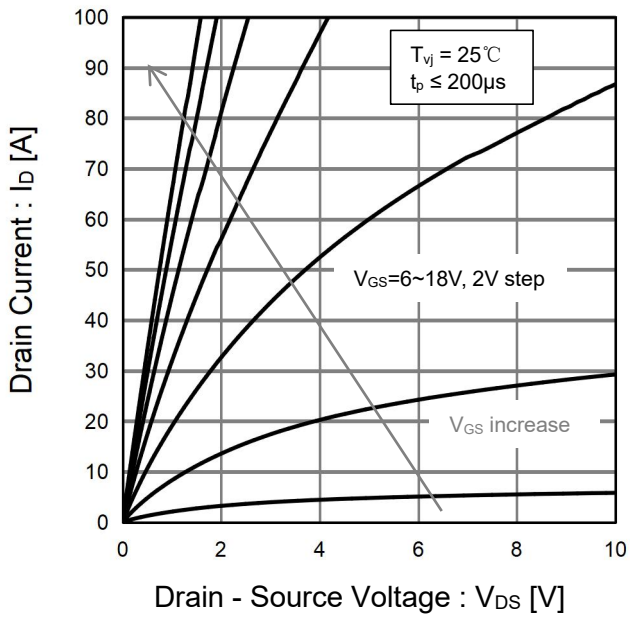


Fig.5  $T_{vj} = 25^{\circ}\text{C}$  3rd Quadrant Characteristics

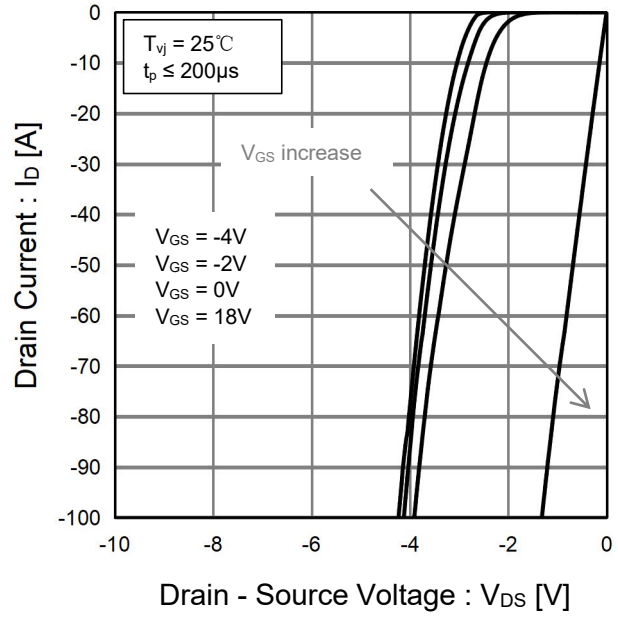


Fig.6  $T_{vj} = 175^{\circ}\text{C}$  Typical Output Characteristics

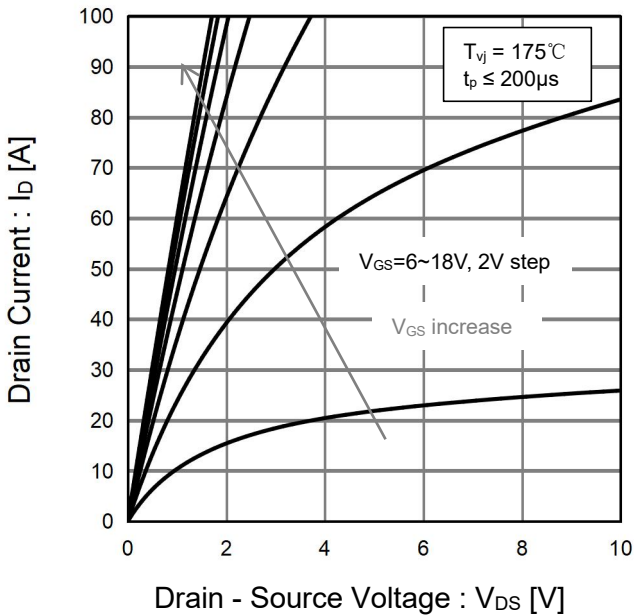


Fig.7  $T_{vj} = 175^{\circ}\text{C}$  3rd Quadrant Characteristics

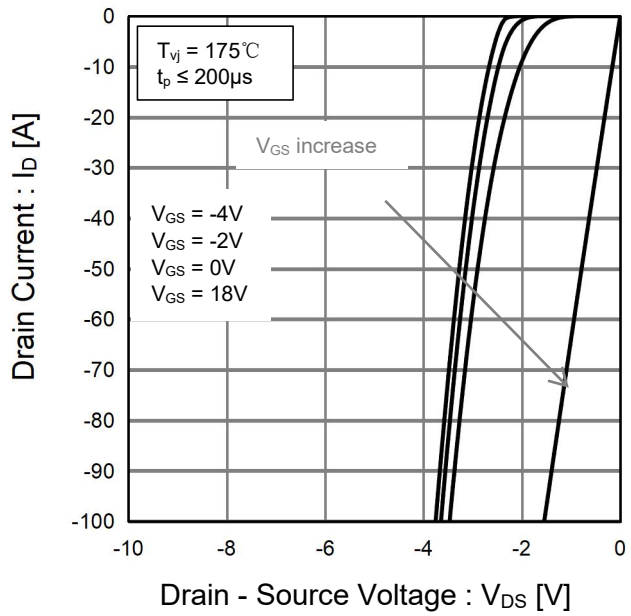


Fig.8 Typical Transfer Characteristics

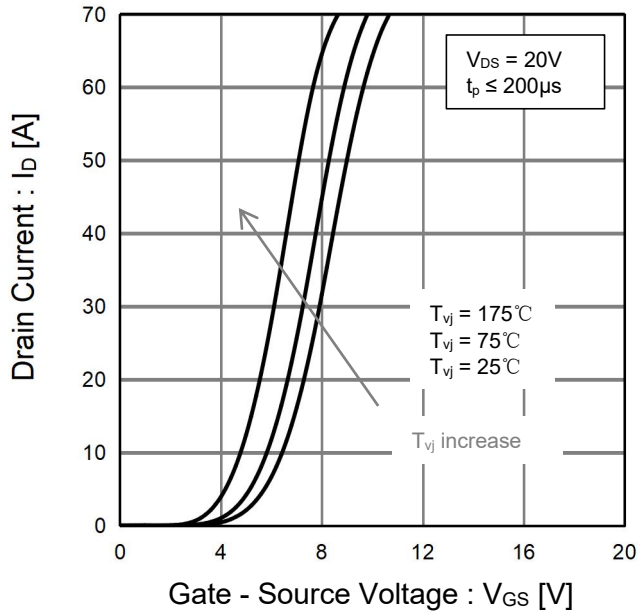


Fig.9 Static Drain - Source On - State Resistance vs. Gate - Source Voltage

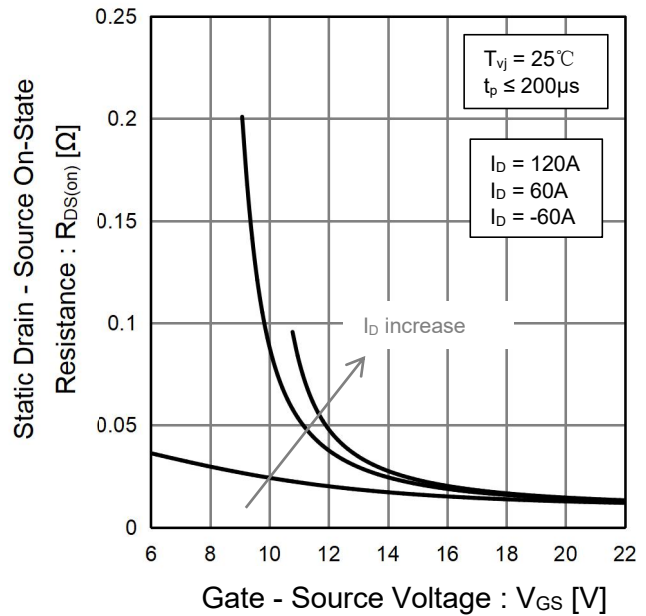


Fig.10 Static Drain - Source On - State Resistance vs. Drain Current

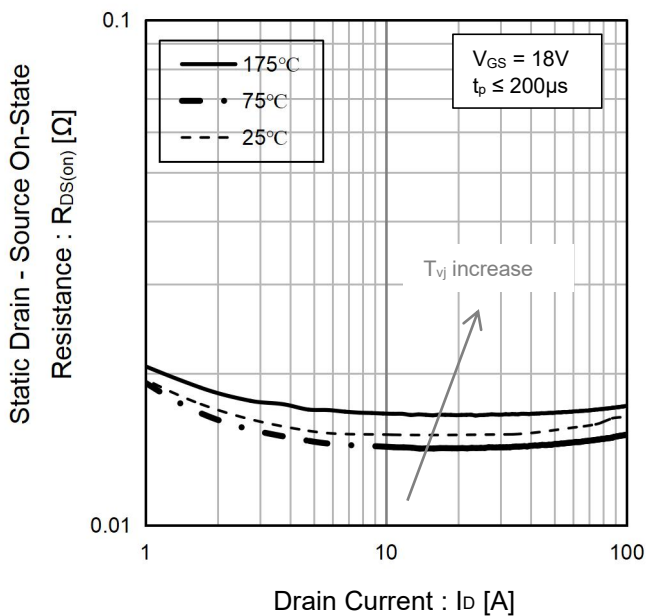


Fig.11 Static Drain - Source On - State Resistance vs. Virtual Junction Temperature

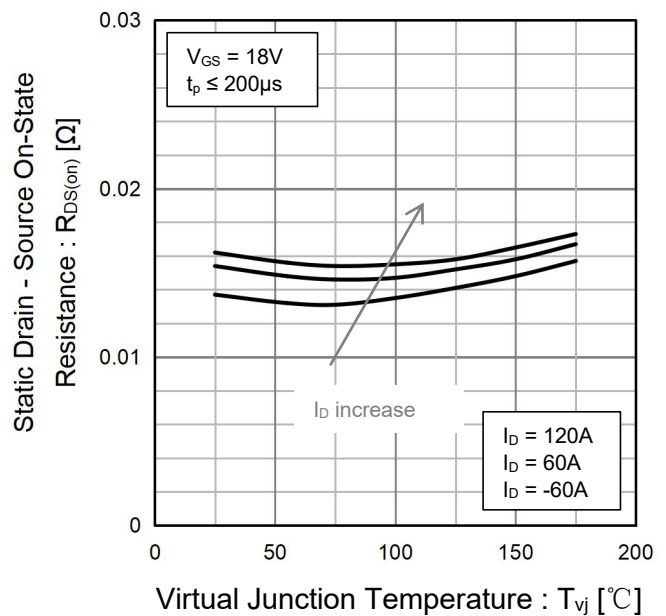


Fig.12 Body Diode Forward Voltage vs. Gate - Source Voltage

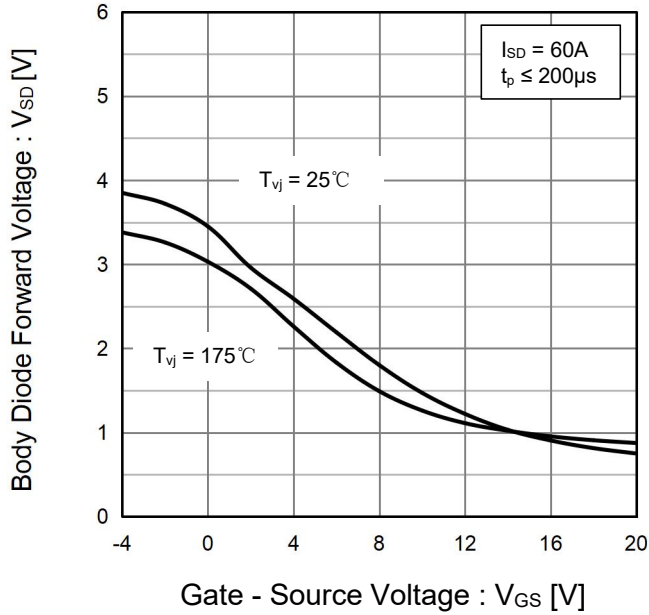


Fig.13 Gate Threshold Voltage vs. Virtual Junction Temperature

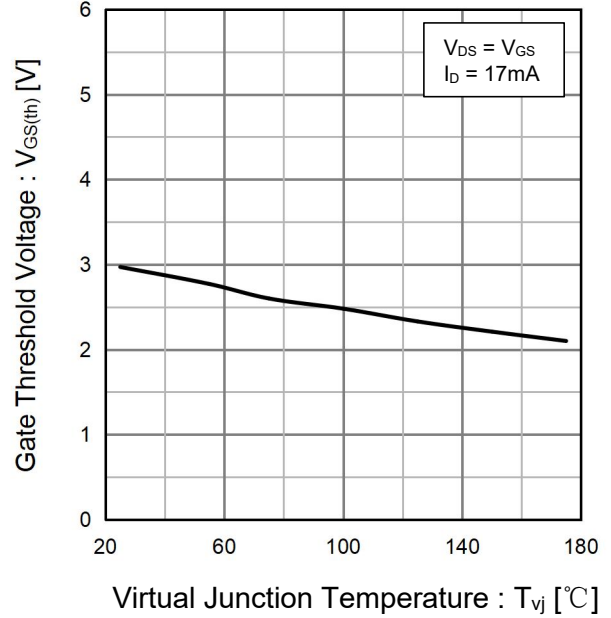


Fig.14 Typical Capacitance vs. Drain - Source Voltage

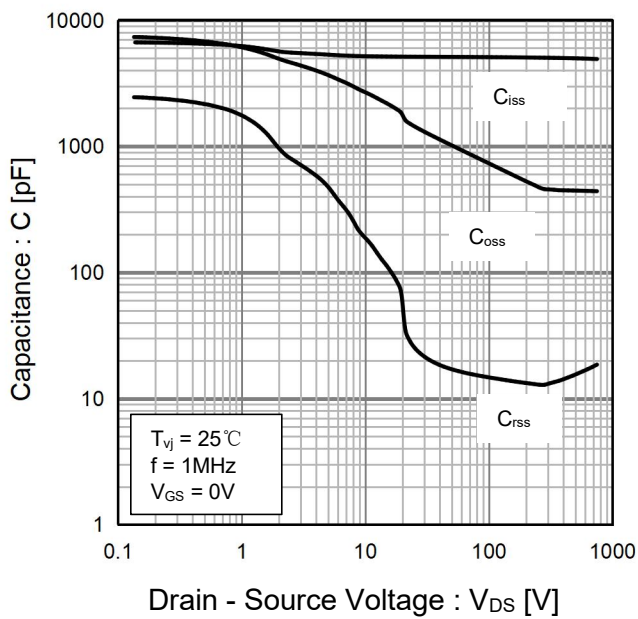


Fig.15 Coss Stored Energy

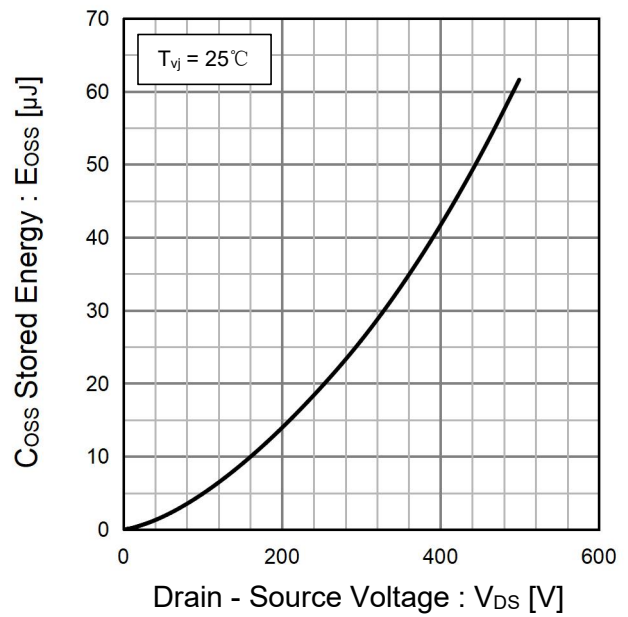


Fig.16 Dynamic Input Characteristics

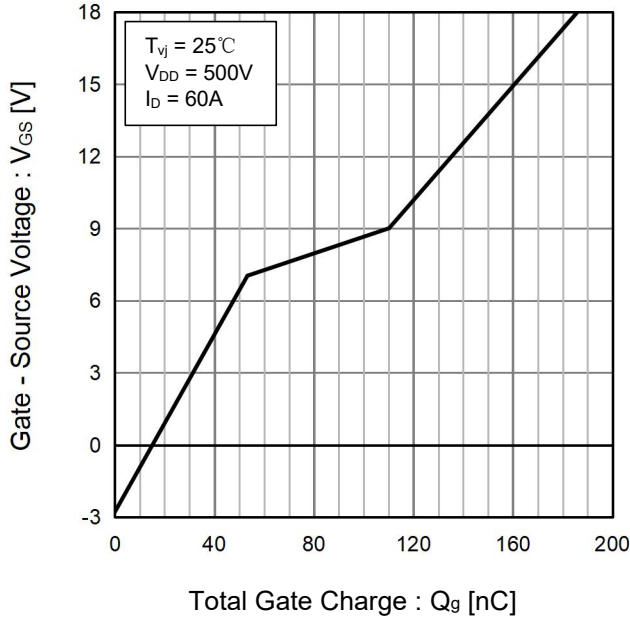


Fig.17 Typical Switching Time vs. External Gate Resistance

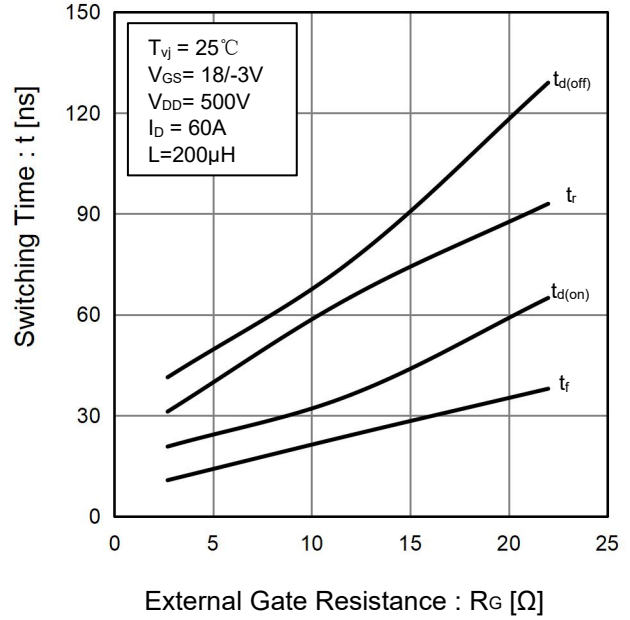


Fig.18 Typical Switching Energy vs. External Gate Resistance

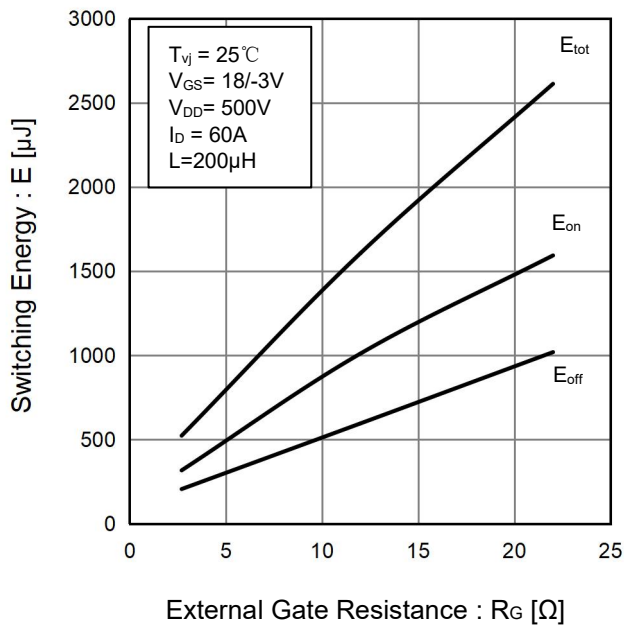


Fig.19 Typical Switching Energy vs. Drain Current

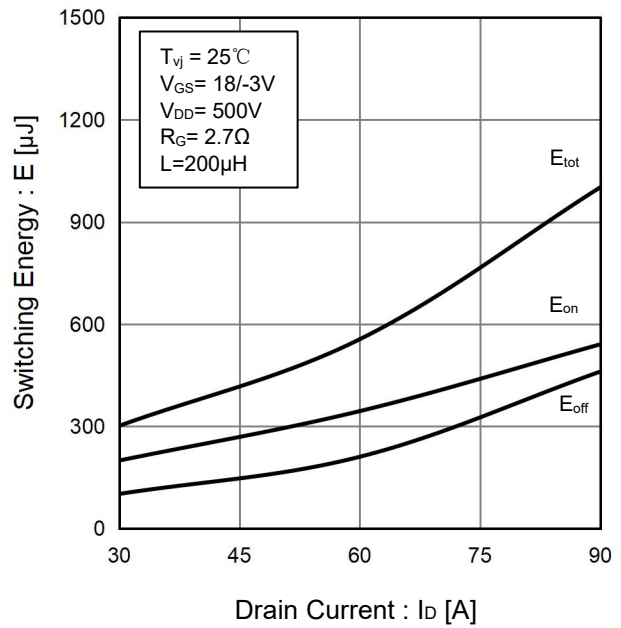
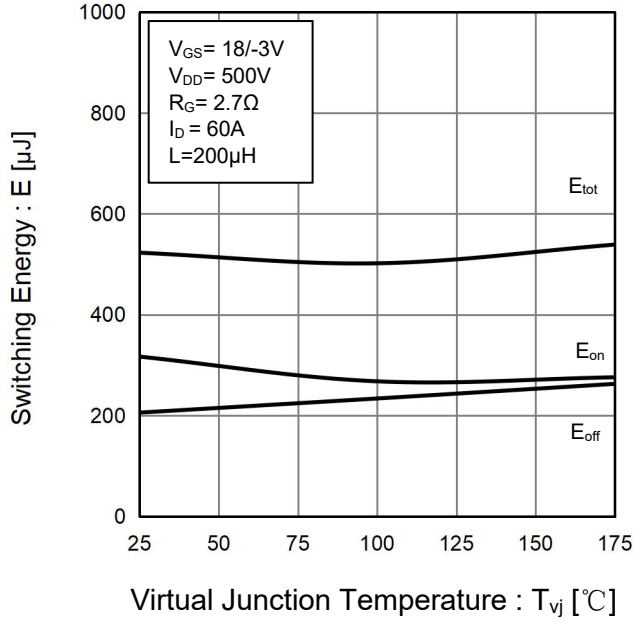
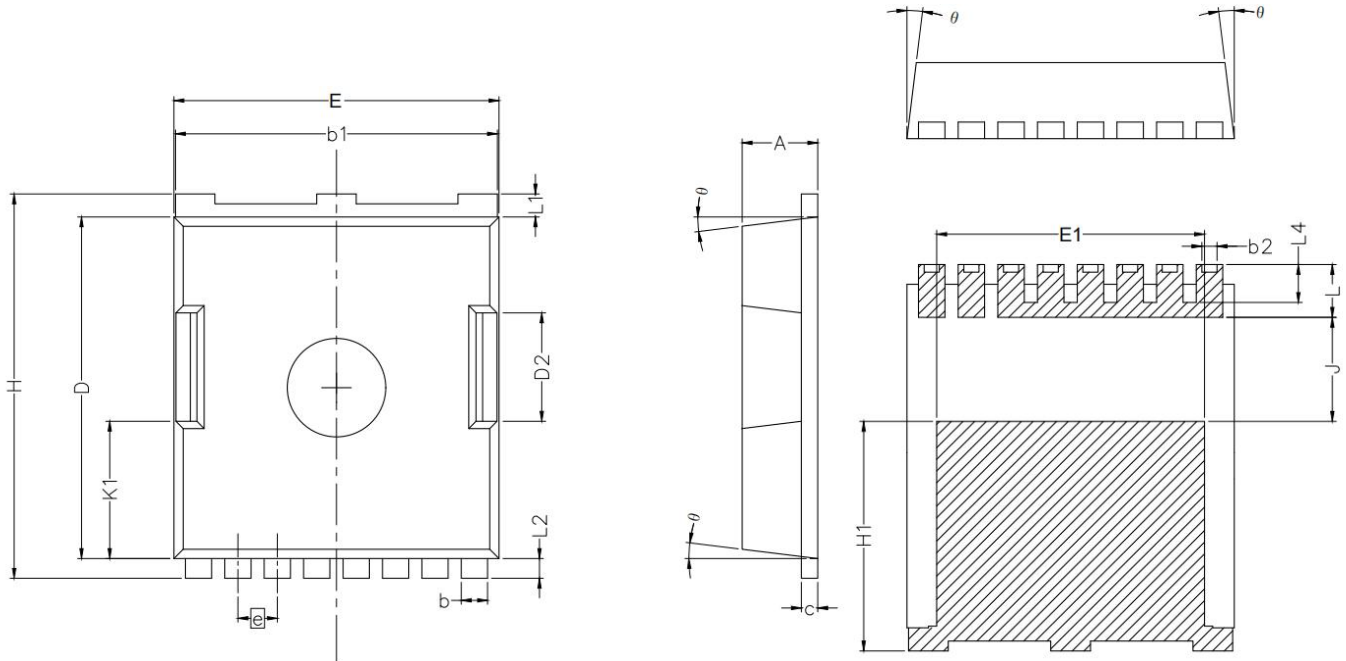


Fig.20 Typical Switching Energy vs. Virtual Junction Temperature



# TOLL Package Information



SYMBOL	MIN	MAX
A	2.20	2.40
b	0.70	0.90
b1	9.70	9.90
b2	0.42	0.50
c	0.40	0.60
D	10.28	10.58
D2	3.10	3.50
E	9.70	10.10
E1	7.90	8.30
e	1.20 BSC	
H	11.48	11.88
H1	6.75	7.15
N	8	
J	3.00	3.30
K1	3.98	4.38
L	1.40	1.80
L1	0.60	0.80
L2	0.50	0.70
L4	1.00	1.30
θ	4°	10°

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