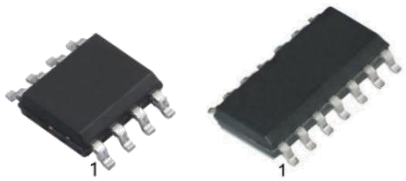
48V, Low Noise, Precision Operational Amplifiers

Features and Benefits

- Wide Supply: ±2.25 V to ±24 V, 4.5 V to 48 V
- Low Offset Voltage: ±100 μV Maximum
- Low Offset Voltage Drift: ±1 μV/°C
- Bandwidth: 1.1 MHz GBW
- Slew Rate: 0.8 V/μs
- Low Noise: 22 nV/√Hz at 10 kHz
- High Common-Mode Rejection: 110 dB
- Low Bias Current: ±20 pA
- EMI/RFI Filtered Inputs


SOP-8

SOP-14


MSOP-8


TSSOP-14


SOT-23-5


DFN-8 3*3

Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
OPA130NAM5/TR-HG	SOT-23-5	A130	REEL	3000pcs/reel
OPA130UAM/TR-HG	SOP-8	OPA130	REEL	2500pcs/reel
OPA2130UAM/TR-HG	SOP-8	OPA2130	REEL	2500pcs/reel
OPA2130EAM/TR-HG	MSOP-8	A2130	REEL	3000pcs/reel
OPA2130DQ3/TR-HG	DFN-8 3*3	A2130	REEL	5000pcs/reel
OPA4130UAM/TR-HG	SOP-14	OPA4130	REEL	2500pcs/reel
OPA4130EAM/TR-HG	TSSOP-14	OPA4130	REEL	2500pcs/reel

General Description

The OPAx130 family (OPA130, OPA2130, and OPA4130) is a new generation of high voltage (48 V), low noise, precision operational amplifiers. These devices offer outstanding dc precision and ac performance, include low offset (±100 μV maximum), low drift (±1 μV/°C), 1.1-MHz bandwidth, and 22 nV/√Hz Input voltage noise density at 10 kHz. Unique features such as differential input-voltage range to the negative supply rail, high output current (± 40 mA), and high capacitive load drive of up to 1 nF make the OPAx130 high-performance op-amps for high-voltage industrial applications.

The robust design of the OPAx130 family provides ease-of-use to the circuit designer: integrated RF/EMI rejection filter, no phase reversal in overdrive conditions, and high electro-static discharge (ESD) protection. The OPAx130 are optimized for operation at voltages from +4.5 V (± 2.25 V) to +48 V (± 24 V) over the extended temperature range of −40°C to +125°C.

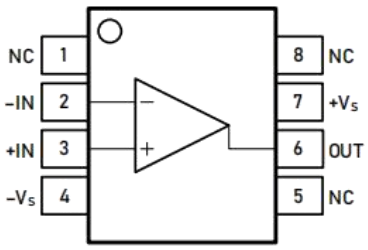
The OPA130 (single) is available in both SOT-23-5 and SOP-8 packages. The OPA2130 (dual) is offered in SOP-8, DFN-8 and MSOP-8 packages. The quad-channel OPA4130 is offered in both SOP-14 and TSSOP-14 packages.

Applications

- High-side and low-side current sensing
- Analog input and output modules
- ADC driver and reference buffer amplifier
- High Precision Comparator
- Power delivery: UPS, server, and merchant network power
- Multiplexed Data-Acquisition Systems
- Test and Measurement Equipment
- Programmable Logic Controllers

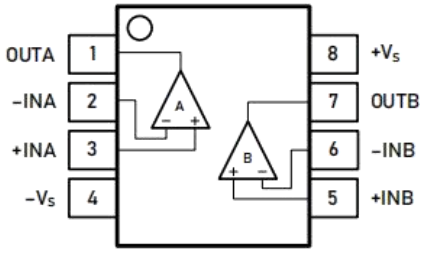
Pin Configuration (Top View)

OPA130



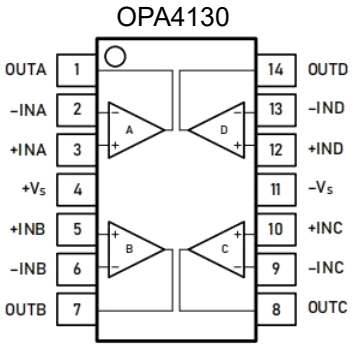
SOP-8

OPA2130



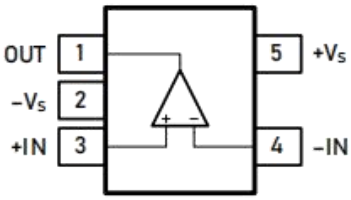
SOP-8/MSOP-8

OPA4130



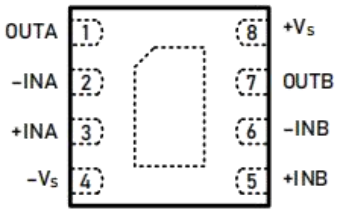
SOP-14/TSSOP-14

OPA130



SOT-23-5

OPA2130



DFN-8 3*3

Pin Description

Symbol	Description
–IN	Inverting input of the amplifier. The voltage range is from $V_{S–}$ to V_{S+} – 2V.
+IN	Non-inverting input of the amplifier. This pin has the same voltage range as –IN.
$+V_S$	Positive power supply. The voltage is from 4.5V to 48V. Split supplies are possible as long as the voltage between V_{S+} and $V_{S–}$ is from 4.5V to 48V.
$–V_S$	Negative power supply. It is normally tied to ground. It can also be tied to a voltage other than ground as long as the voltage between V_{S+} and $V_{S–}$ is from 4.5V to 48V.
OUT	Amplifier output.
NC	No connection

Limiting Value

In accordance with the Absolute Maximum Rating System (IEC 60134).

Parameter	Absolute Maximum Rating
Supply Voltage, VS+ to VS–	60 V
Signal Input Terminals: Voltage, Current	$-V_S$ – 0.3V to $+V_S$ + 0.3V, ±10 mA
Output Short-Circuit	Continuous
Storage Temperature Range, Tstg	–65 °C to +150 °C
Operating Temperature Range, T_A (1)	−40 °C to +125 °C
Junction Temperature, TJ	150 °C
Lead Temperature Range (Soldering 10 sec)	260 °C

Note: Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions outside those indicated in the operationalsections of this specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Note(1) :Operating temperature range: -40°C to +125°C. This product is designed for industrial grade applications. For automotive grade versions compliant with AEC-Q100, please conduct internal screening per the standard or contact our sales team for availability.

ESD Rating

Parameter	Item	Value	Unit
Electrostatic Discharge Voltage	Human body model (HBM), per MIL-STD-883J / Method 3015.9 (1)	±1 500	V
	Charged device model (CDM), per ESDA/JEDEC JS-002-2014 (2)	±1 000	
	Machine model (MM), per JESD22-A115C	±400	

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process. Manufacturing with less than 500-V HBM is possible if necessary precautions are taken.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process. Manufacturing with less than 250-V CDM is possible if necessary precautions are taken.

Electrical Characteristics

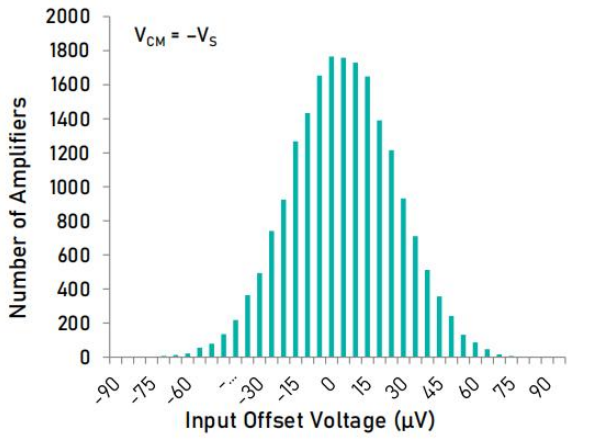
V_S = 4.5 V to 48 V, T_A = +25 °C, V_{CM} = V_S /2, V_O = V_S /2, and R_L = 10 kΩ connected to V_S /2, unless otherwise noted. Boldface limits apply over the specified temperature range, T_A = −40 °C to +125 °C.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
OFFSET VOLTAGE						
V_{OS}	Input offset voltage				±100	μV
$V_{OS}TC$	Offset voltage drift	T_A = −40 to +125 °C		±1		μV/°C
PSRR	Power supply rejection ratio	V_S = 4.5 to 44 V, V_{CM} = 0.1 V		2		μV/V
		T_A = −40 to +125 °C		6		
INPUT BIAS CURRENT						
I_B	Input bias current			20		pA
		T_A = +85 °C		150		
		T_A = +125 °C		600		
I_{OS}	Input offset current			10		pA
NOISE						
V_n	Input voltage noise	f = 0.1 to 10 Hz		4		$μV_{P-P}$
e_n	Input voltage noise density	f = 1 kHz		25		nV/√Hz
		f = 10 kHz		22		
I_n	Input current noise density	f = 1 kHz		5		fA/√Hz
INPUT VOLTAGE						
V_{CM}	Common-mode voltage range		$-V_S$		$+V_S-2$	V
CMRR	Common-mode rejection ratio	V_S = 40 V, V_{CM} = 0 to 38 V		110		dB
		V_{CM} = 0.1 to 38 V, T_A = −40 to +125 °C		98		
		V_S = 5.0 V, V_{CM} = 0 to 3 V		85		
		V_{CM} = 0.1 to 3 V, T_A = −40 to +125 °C		72		
INPUT IMPEDANCE						
CIN	Input capacitance	Differential		2.0		pF
		Common mode		3.5		
OPEN-LOOP GAIN						
A_{VOL}	Open-loop voltage gain	V_S = 40 V, V_O = 0.1 to 39.9 V		116		dB
		T_A = −40 to +125 °C		112		
		V_S = 5 V, V_O = 0.1 to 4.9 V		101		
		T_A = −40 to +125 °C		97		
FREQUENCY RESPONSE						
GBW	Gain bandwidth product			1.1		MHz
SR	Slew rate	V_S = 40 V, G = +1, 10 V step		0.8		V/μs

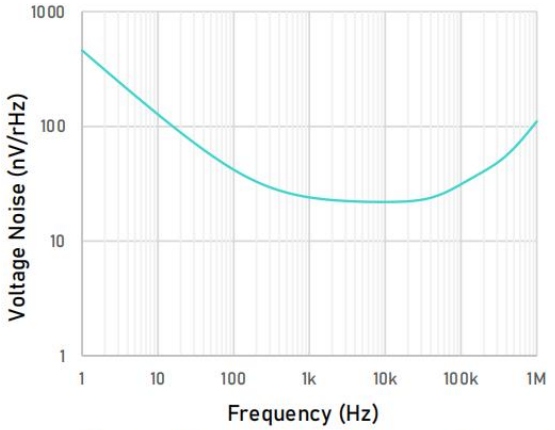
THD+N	Total harmonic distortion + noise	G = +1, f = 1 kHz, V_O = 3 VRMS		0.02		%
t_S	Settling time	To 0.1%, VS = 40 V, G = +1, 5 V step		6		µs
		To 0.01%, V_S = 40 V, G = +1, 5 V step		12		
t_{OR}	Overload recovery time	V_{IN} × Gain > VS		3		µs
OUTPUT						
V_{OH}	High output voltage swing	V_S = ±20 V, R_L = 10 kΩ		+V_S−97		mV
		V_S = ±20 V, R_L = 2 kΩ		+V_S−257		
V_{OL}	Low output voltage swing	V_S = ±20 V, R_L = 10 kΩ		−V_S+52		mV
		V_S = ±20 V, R_L = 2 kΩ		−V_S+232		
I_{SC}	Short-circuit current			±40		mA
POWER SUPPLY						
V_S	Operating supply voltage	T_A = −40 to +125 °C	4.5		48	V
I_Q	Quiescent current (per amplifier)	V_S = 5 V		150		µA
		V_S = 40 V		220		
THERMAL CHARACTERISTICS						
θ_{JA}	Package Thermal Resistance	SOT-23-5		190		°C/W
		MSOP-8		201		
		SOP-8		125		
		TSSOP-14		112		
		DFN-8		65		
		SOP-14		115		

Typical Performance Characteristics

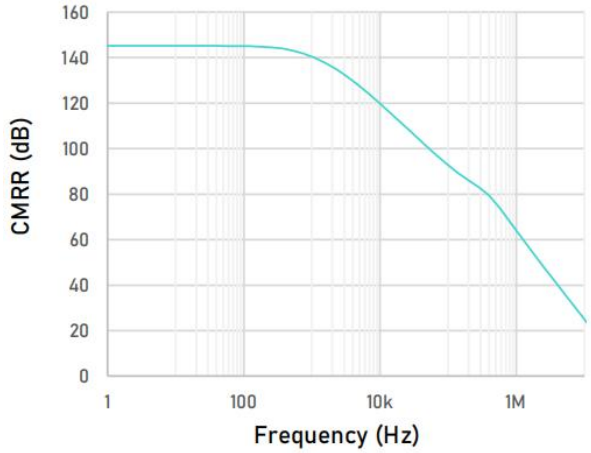
At T_A = +25 °C, V_{CM} = V_S /2, and R_L = 10 kΩ connected to V_S /2, unless otherwise noted.



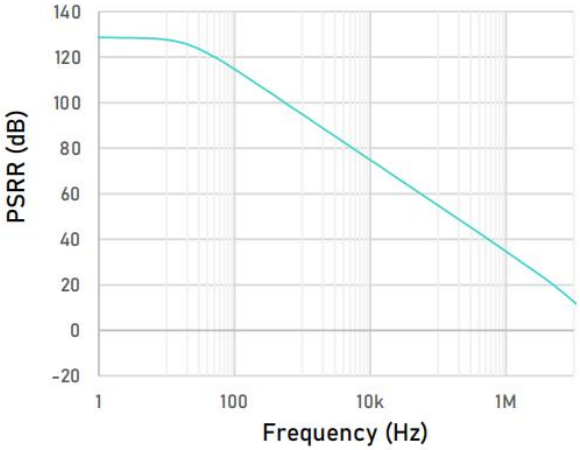
Offset Voltage Production Distribution



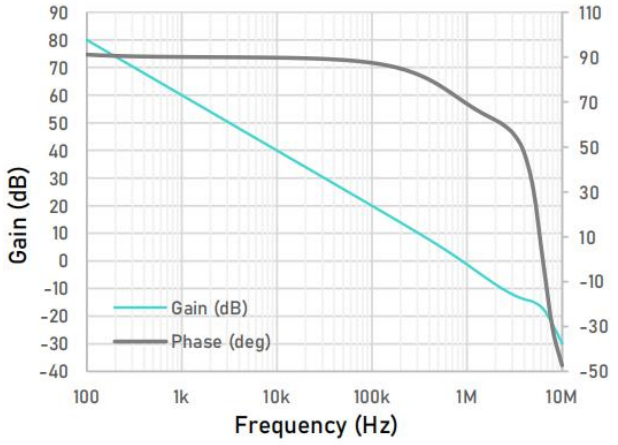
Input Voltage Noise Spectral Density as a function of Frequency



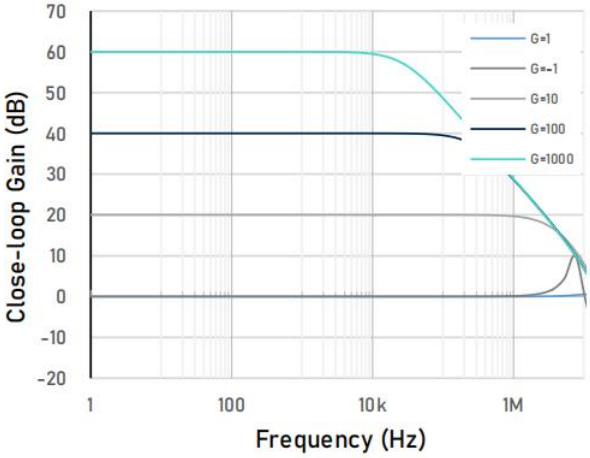
CMRR as a function of Frequency



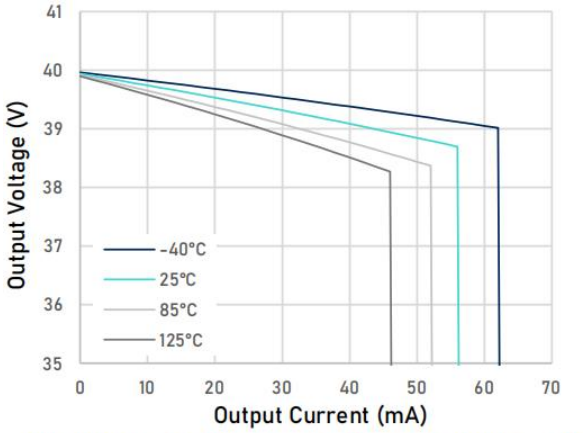
PSRR as a function of Frequency



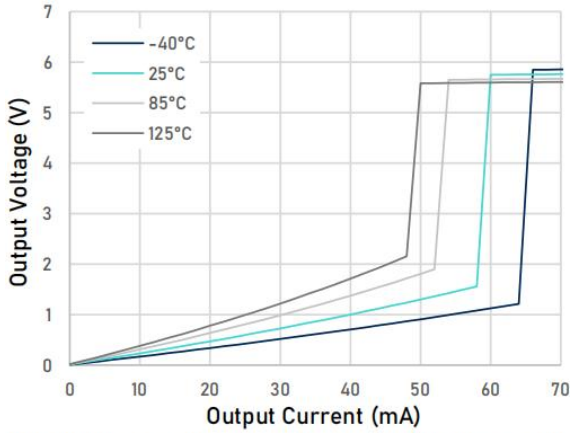
Open-loop Gain and Phase as a function of Frequency



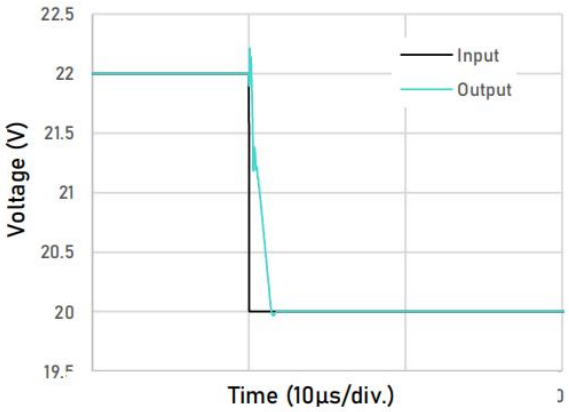
Close-loop Gain as a function of Frequency



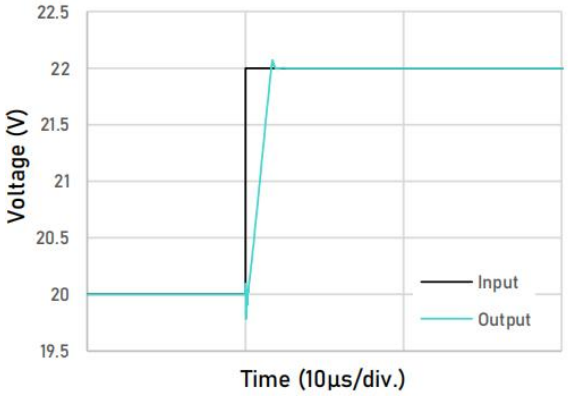
Output Voltage Swing as a function of Output Current (Sourcing, V_S = 40 V)



Output Voltage Swing as a function of Output Current (Sinking, V_S = 40 V)



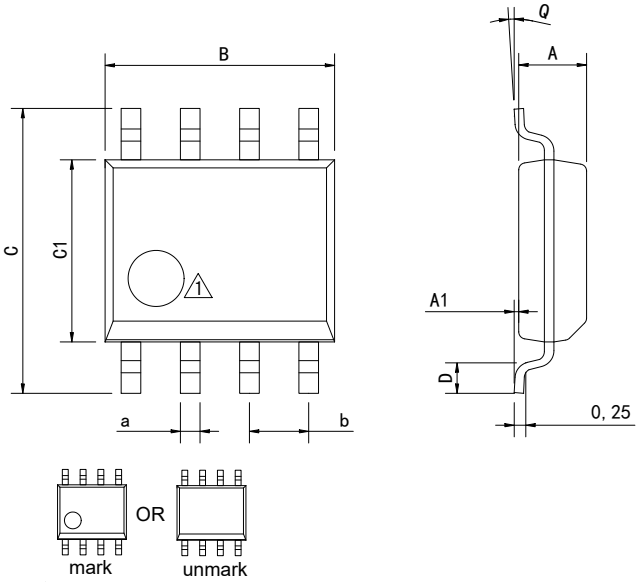
Large-Signal Step Response(Failing)



Large-Signal Step Response(Rising)

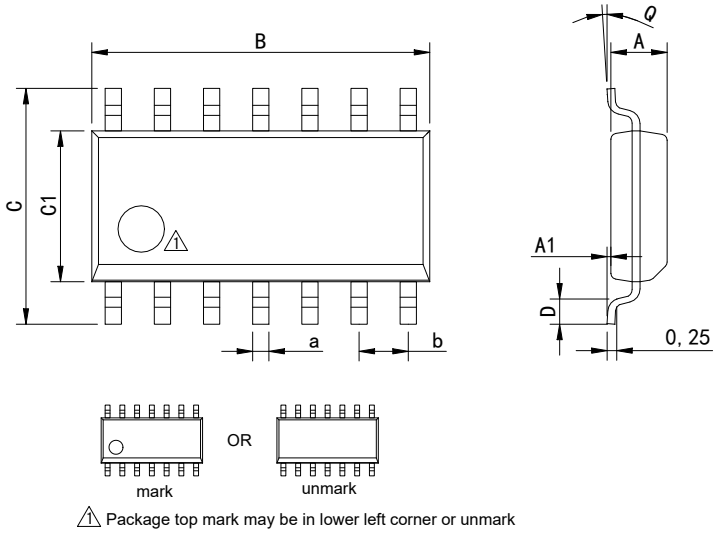
Physical Dimensions

SOP-8



Dimensions In Millimeters(SOP-8)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	

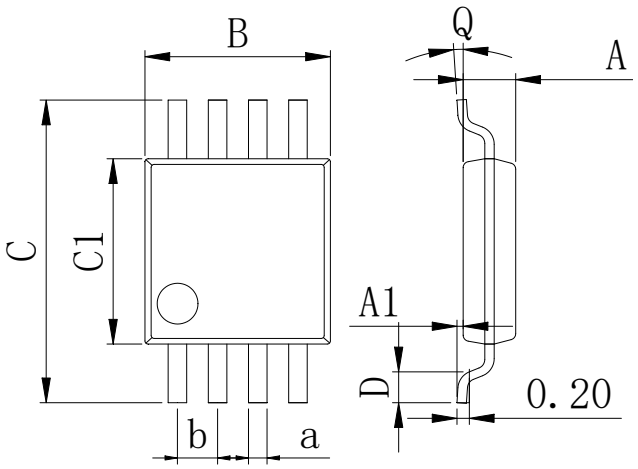
SOP-14



Dimensions In Millimeters(SOP-14)									
Symbol:	**A**	**A1**	**B**	**C**	**C1**	**D**	**Q**	**a**	**b**
Min:	1.35	0.05	8.55	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	8.75	6.20	4.00	0.80	8°	0.45	

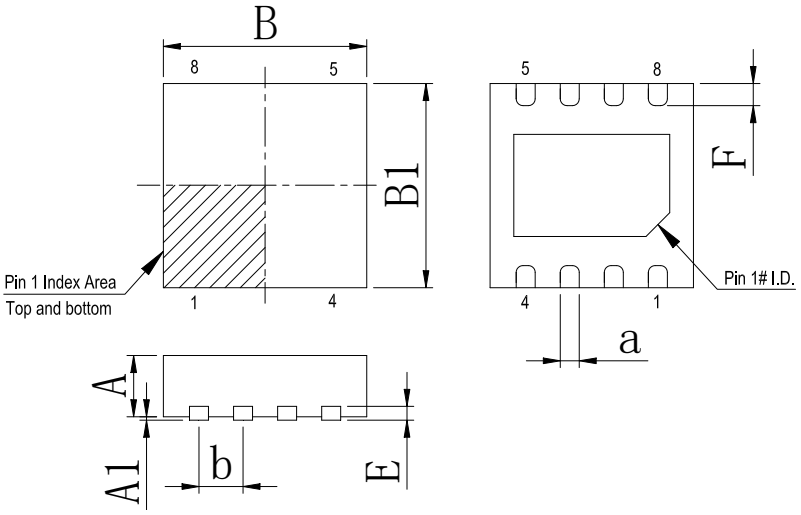
Physical Dimensions

MSOP-8



Dimensions In Millimeters(MSOP-8)									
Symbol:	**A**	**A1**	**B**	**C**	**C1**	**D**	**Q**	**a**	**b**
Min:	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65 BSC
Max:	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	

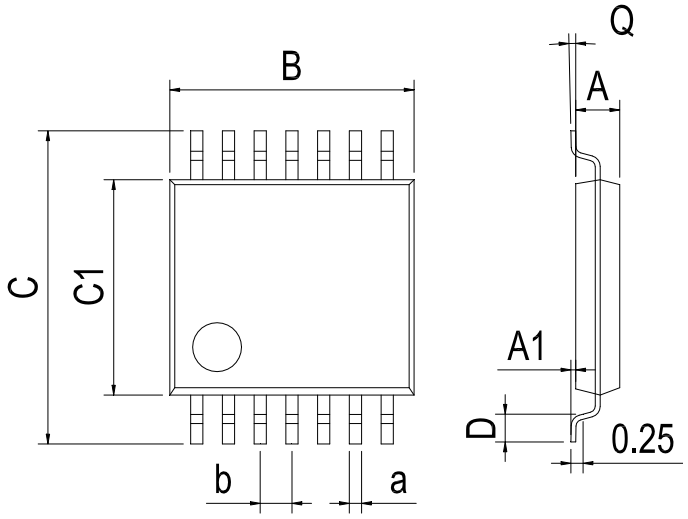
DFN-8 3*3



Dimensions In Millimeters(DFN-8 3*3)								
Symbol:	**A**	**A1**	**B**	**B1**	**E**	**F**	**a**	**b**
Min:	0.85	0.00	2.90	2.90	0.20	0.30	0.20	0.65 BSC
Max:	0.95	0.05	3.10	3.10	0.25	0.50	0.34	

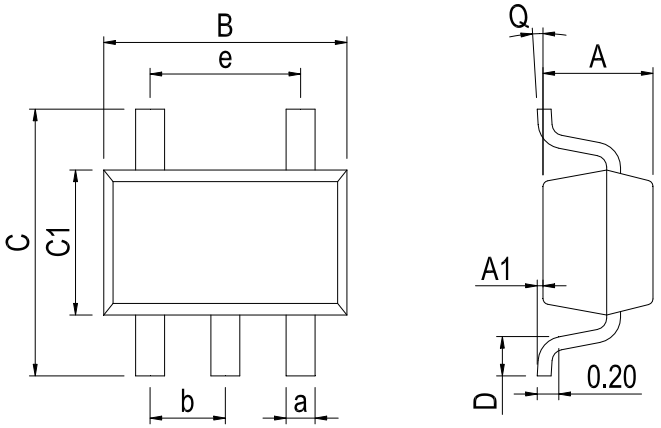
Physical Dimensions

TSSOP-14



Dimensions In Millimeters(TSSOP-14)									
Symbol:	**A**	**A1**	**B**	**C**	**C1**	**D**	**Q**	**a**	**b**
Min:	0.85	0.05	4.90	6.20	4.30	0.40	0°	0.20	0.65 BSC
Max:	0.95	0.20	5.10	6.60	4.50	0.80	8°	0.25	

SOT-23-5



Dimensions In Millimeters(SOT-23-5)										
Symbol:	**A**	**A1**	**B**	**C**	**C1**	**D**	**Q**	**a**	**b**	**e**
Min:	1.00	0.00	2.82	2.65	1.50	0.30	0°	0.30	0.95 BSC	1.90 BSC
Max:	1.15	0.15	3.02	2.95	1.70	0.60	8°	0.50		

Revision History

REVISION NUMBER	DATE	REVISION	PAGE
V1.0	2018-11	New	1-10
V1.1	2025-6	Document Reformatting	1-13
V1.2	2025-12	Update important statements	13

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