

General Description

The 65R580Q use advanced super junction technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

Features

- Multi-layer Epitaxial Chip Technology
- Low On-Resistance
- 100% avalanche tested
- RoHS Compliant

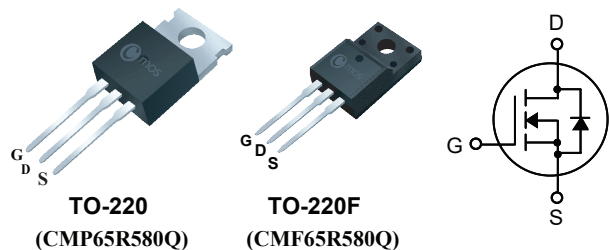
Product Summary

BVDSS	$R_{DS(on)}$ max.	ID
650V	580mΩ	8A

Applications

- Switching applications
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply(UPS)

TO-220/220F Pin Configuration



Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	220/263/262	220F	Units
V_{DS}	Drain-Source Voltage	650		V
V_{GS}	Gate-Source Voltage	±30		V
$I_D@T_C=25^\circ C$	Continuous Drain Current	8	8*	A
$I_D@T_C=100^\circ C$	Continuous Drain Current	4.8	4.8*	A
I_{DM}	Pulsed Drain Current	24	24*	A
EAS	Single Pulse Avalanche Energy (Note 1)	120		mJ
$P_D@T_C=25^\circ C$	Total Power Dissipation	70	30	W
T_{STG}	Storage Temperature Range	-55 to 150		°C
T_J	Operating Junction Temperature Range	-55 to 150		°C

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	220/263/262	220F	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient Max.	62	80	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case Max.	1.78	4.17	°C/W

Electrical Characteristics (T_J=25°C , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	650	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =2.5A	---	540	580	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	2.0	---	4.0	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =600V , V _{GS} =0V	---	---	1	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±30V , V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =15V , I _D =2A	---	4.5	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	27	---	Ω
Q _g	Total Gate Charge	I _D = 8A	---	13	---	nC
Q _{gs}	Gate-Source Charge	V _{DD} = 480V	---	4	---	
Q _{gd}	Gate-Drain Charge	V _{GS} = 10V	---	5	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} = 300V V _{GS} = 10V R _G = 25Ω , I _D = 8A	---	15	---	ns
T _r	Rise Time		---	35	---	
T _{d(off)}	Turn-Off Delay Time		---	76	---	
T _f	Fall Time		---	33	---	
C _{iss}	Input Capacitance	V _{DS} =100V , V _{GS} =0V , f=1MHz	---	480	---	pF
C _{oss}	Output Capacitance		---	35	---	
C _{rss}	Reverse Transfer Capacitance		---	8.2	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V , Force Current	---	---	8	A
I _{SM}	Pulsed Source Current		---	---	24	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =8A , T _J =25°C	---	0.89	1.4	V
t _{rr}	Reverse Recovery Time	di/dt = 100A/μs	---	260	---	ns
Q _{rr}	Reverse Recovery Charge	V _{DD} =100V , I _{SD} =8A	---	2	---	μC

Note :

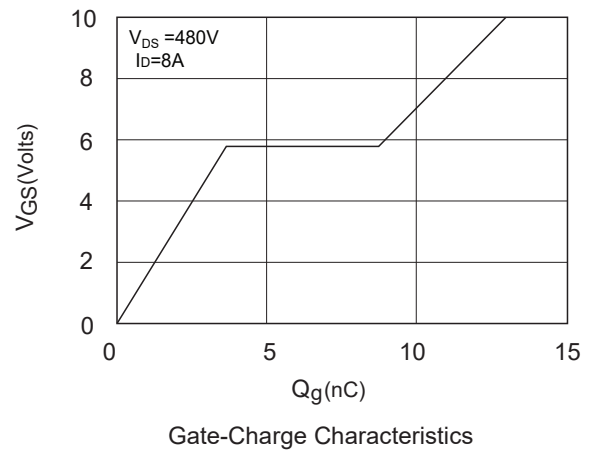
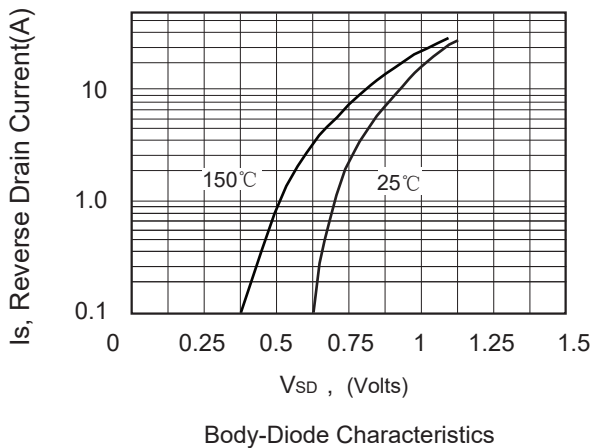
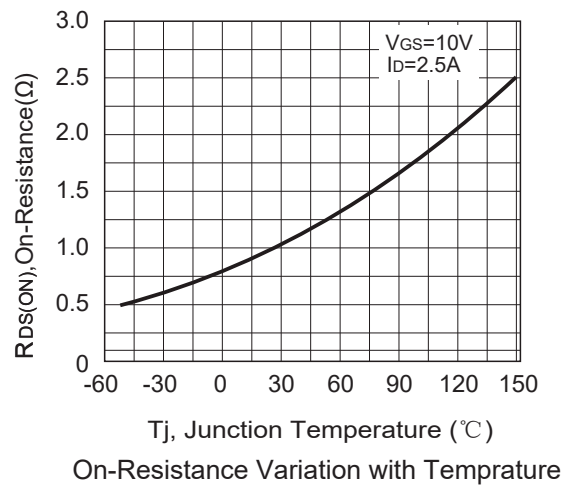
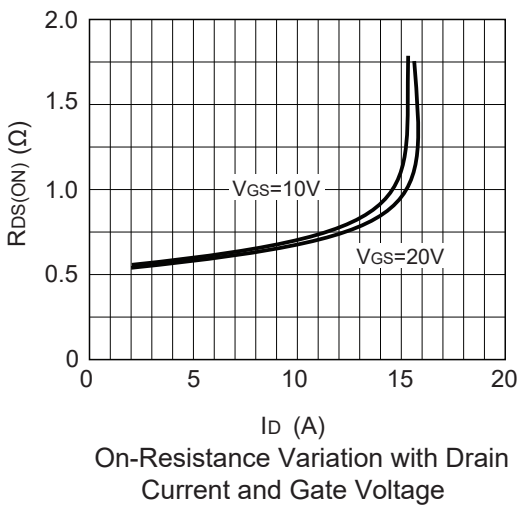
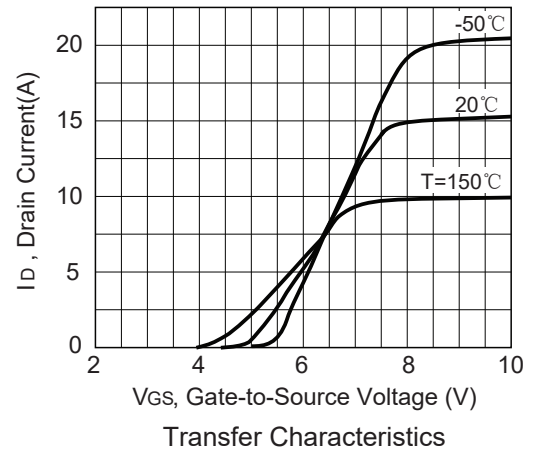
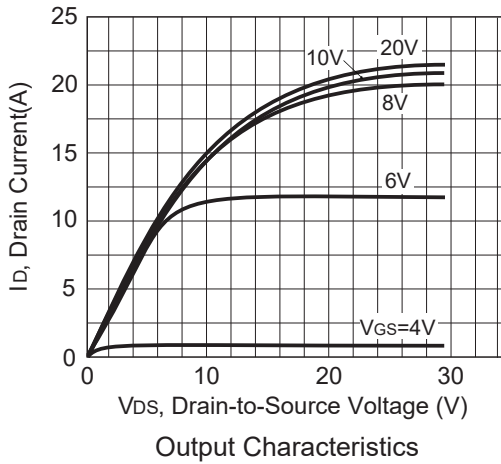
1.The EAS data shows Max. rating . The test condition is V_{DD}=50V , V_{GS}=10V , L=15mH , I_{AS} =4A.

This product has been designed and qualified for the consumer market.

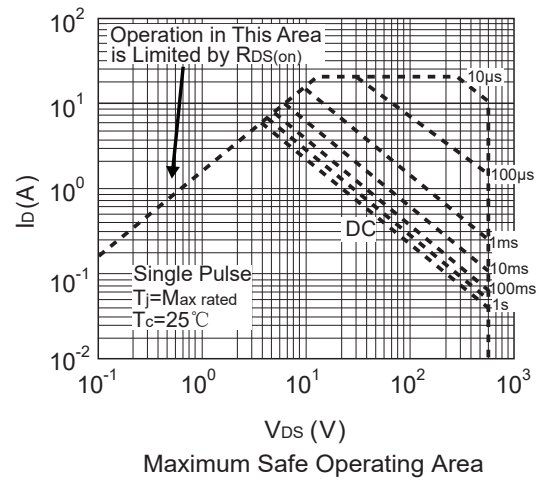
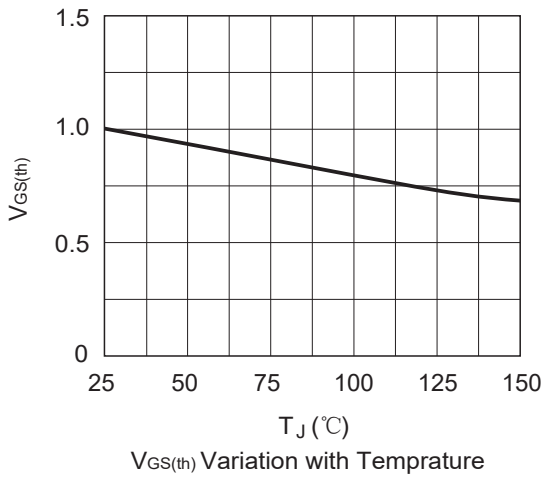
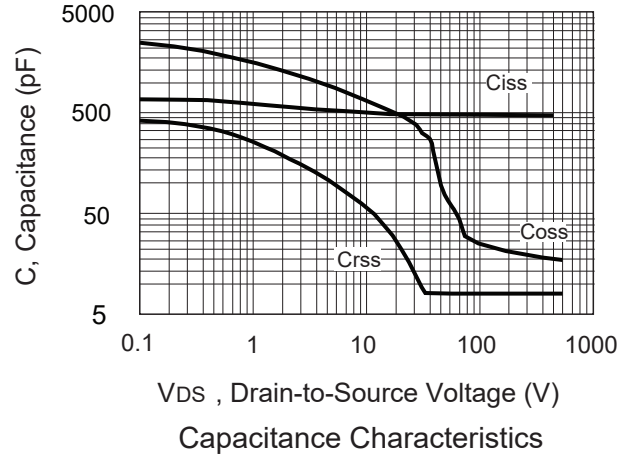
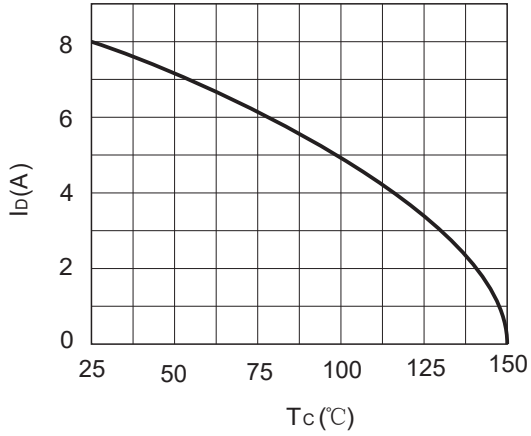
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Cmos reserves the right to improve product design ,functions and reliability without notice.Please refer to the latest version of specification.

Typical Characteristics



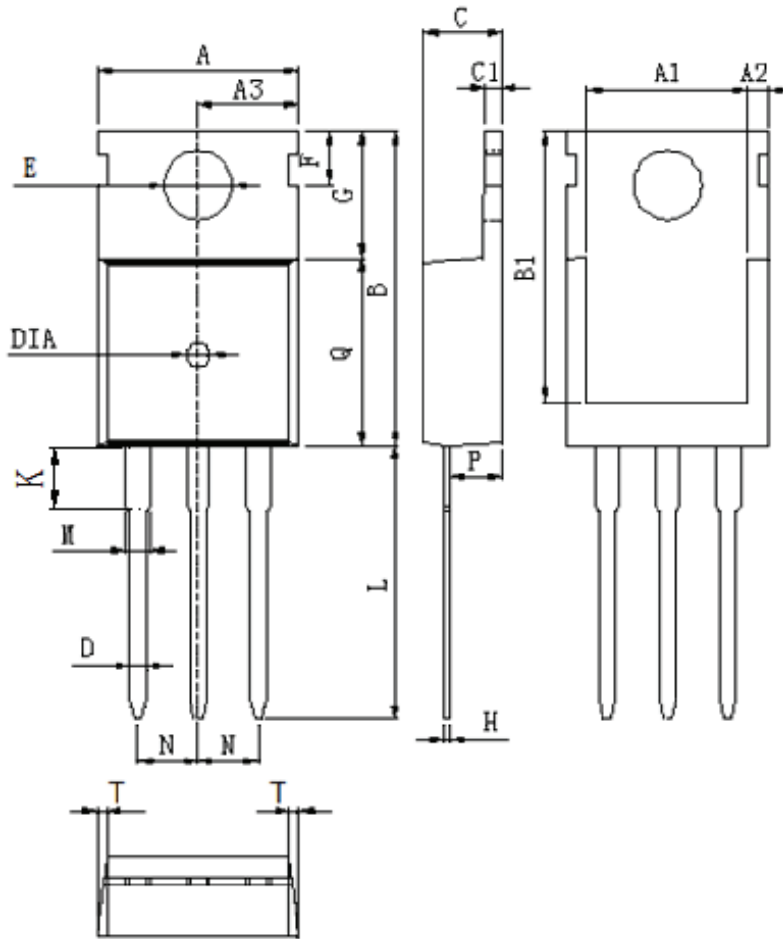
Typical Characteristics



Package Dimension

TO-220

Unit :mm

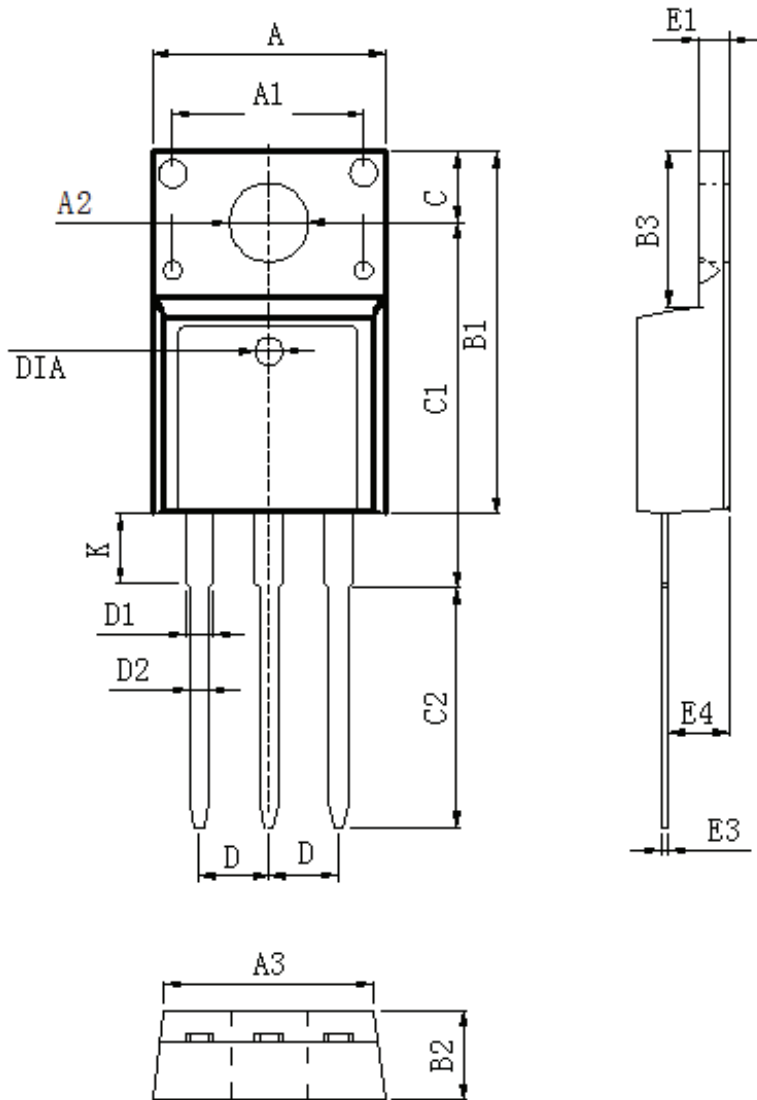


DIM	MILLIMETERS
A	10.0±0.3
A1	8.64±0.2
A2	1.15±0.1
A3	5.0±0.2
B	15.8±0.4
B1	13.2±0.3
C	4.56±0.1
C1	1.3±0.2
D	0.8±0.2
E	3.6±0.2
F	2.95±0.3
G	6.5±0.3
H	0.5±0.1
K	3.1±0.2
L	13.2±0.4
M	1.25±0.1
N	2.54±0.1
P	2.4±0.3
Q	9.0±0.3
T	W:0.35
DIA	⊙1.5(deep 0.2)

Package Dimension

TO-220F

Unit :mm



DIM	MILLIMETERS
A	10.16±0.3
A1	7.00±0.1
A2	3.3±0.2
A3	9.5±0.2
B1	15.87±0.3
B2	4.7±0.2
B3	6.68±0.4
C	3.3±0.2
C1	12.57±0.3
C2	10.02±0.5
D	2.54±0.05
D1	1.28±0.2
D2	0.8±0.1
K	3.1±0.3
E1	2.54±0.1
E3	0.5±0.1
E4	2.76±0.2
DIA	⊙1.5 (deep 0.2)