



Description

The NTD15N06LT4G uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = 60V$ $I_D = 8A$

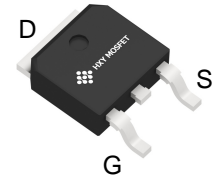
$R_{DS(ON)} < 100m\Omega$ @ $V_{GS}=10V$

Application

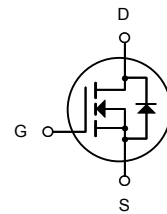
Battery protection

Load switch

Uninterruptible power supply



TO-252-2L
(DPAK)



N-Channel MOSFET

Ordering Information

Product ID	Pack	Brand	Qty(PCS)
NTD15N06LT4G	TO-252-2L(DPAK)	HXY MOSFET	2500

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, V_{GS} @ 10V	8	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, V_{GS} @ 10V	4	A
I_{DM}	Pulsed Drain Current	24	A
EAS	Single Pulse Avalanche Energy	6	mJ
$P_D@T_C=25^\circ C$	Total Power Dissipation	16	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$



Electrical Characteristics (T_J = 25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	60	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =60V	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0A	---	---	±100	nA
V_{GS(th)}	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250μA	1	1.5	2.2	V
R_{DS(on)}	Drain-Source On Resistance ²	V _{GS} =10V, I _D =3A	---	88	100	mΩ
		V _{GS} =4.5V, I _D =2A	---	95	125	mΩ
C_{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	---	349	---	pF
C_{oss}	Output Capacitance		---	28	--	
C_{rss}	Reverse Transfer Capacitance		---	22	---	
t_{d(on)}	Turn-On Delay Time	V _{DS} =40V, I _D =40A, R _{ENG} =6Ω, V _{GS} =1	---	27	---	ns
t_r	Rise Time		---	17	---	ns
t_{d(off)}	Turn-Off Delay Time		---	43	---	ns
t_f	Fall Time		---	54	---	ns
Q_{gs}	Total Gate Charge	0V	---	1	---	nc
Q_{gd}	Gate-Source Charge	V _{GS} =10V,	---	1.4	---	nc
Q_g	Gate-Drain "Miller" Charge	V _{DS} =30V, I _D =3A	---	8	---	nc
V_{SD}	Diode Forward Voltage ¹	V _{GS} =0V, I _{SD} =5A	---	---	1.2	V
I_S	Continuous Drain Curren ³	VD=VG=0V	---	---	8	A
I_{SM}	Pulsed Drain Current		---	---	24	A

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition : T_J=25°C, V_{DD}=30V, V_G=10V, L=0.5mH, R_g=25Ω, I_{AS}=4A
3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%



Typical Characteristics

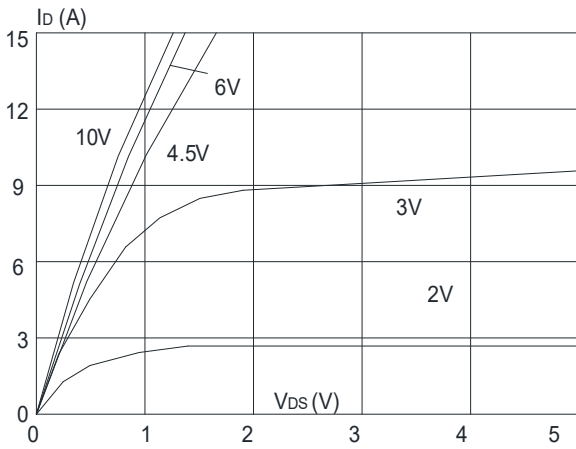


Figure1: Output Characteristics

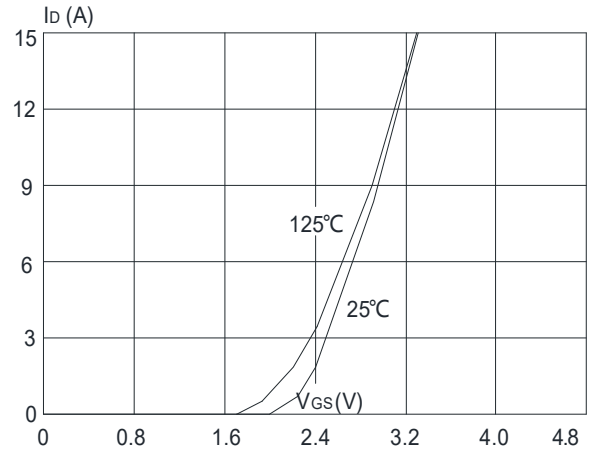


Figure 2: Typical Transfer Characteristics

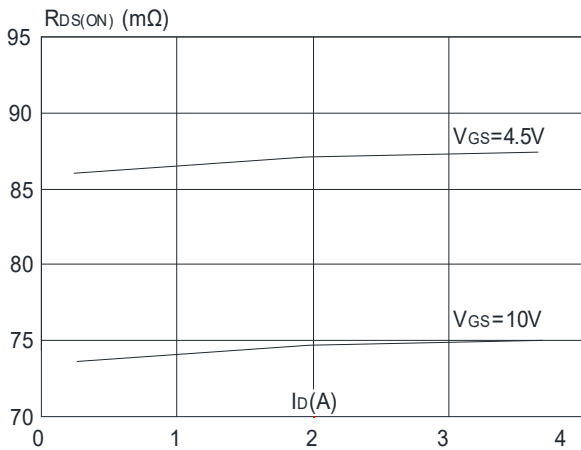


Figure 3: On-resistance vs. Drain Current

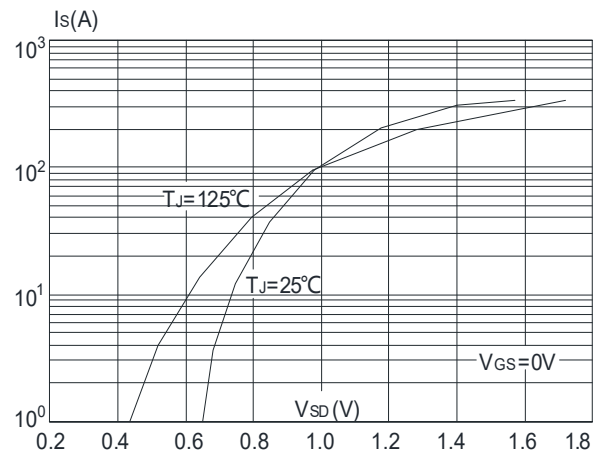


Figure 4: Body Diode Characteristics

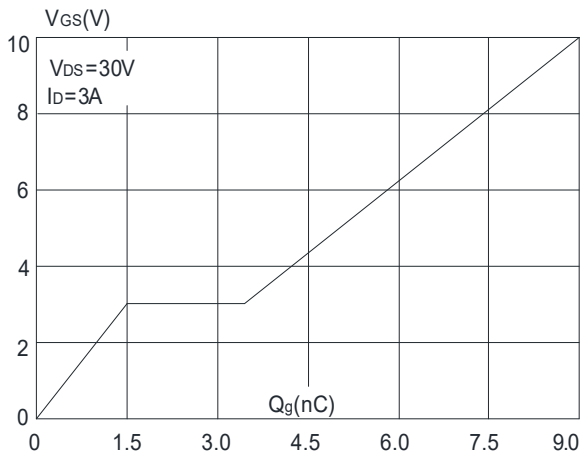


Figure 5: Gate Charge Characteristics

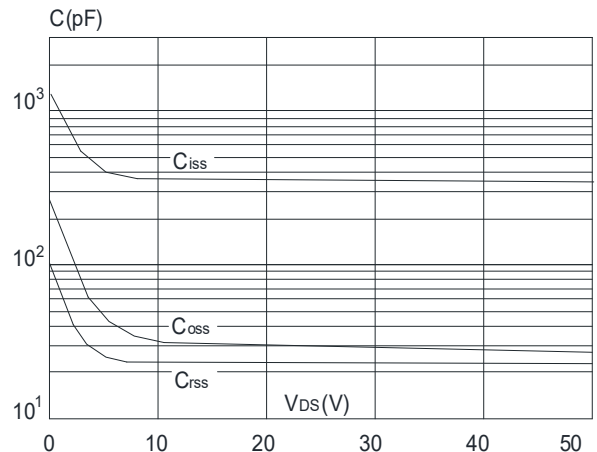


Figure 6: Capacitance Characteristics

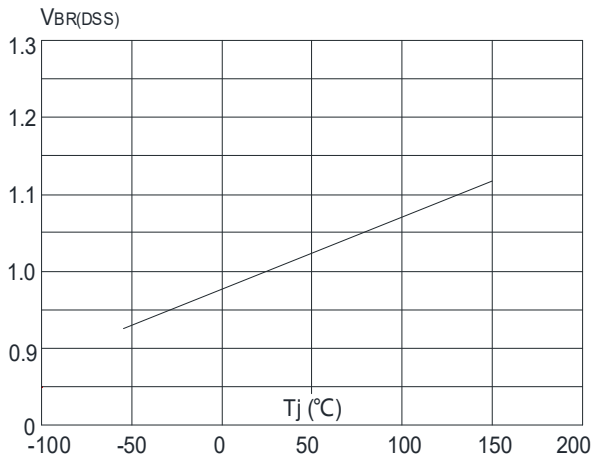


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

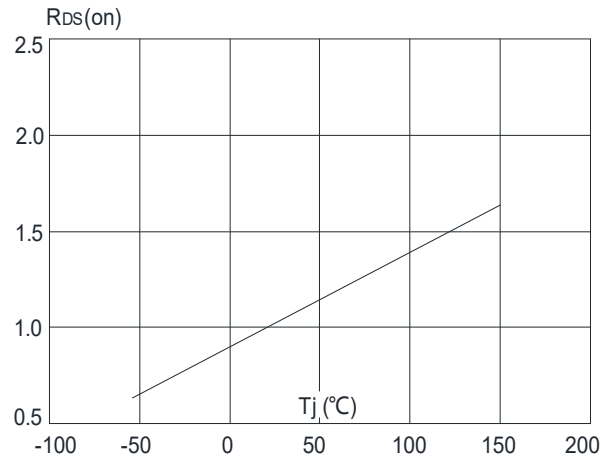


Figure 8: Normalized on Resistance vs. Junction Temperature

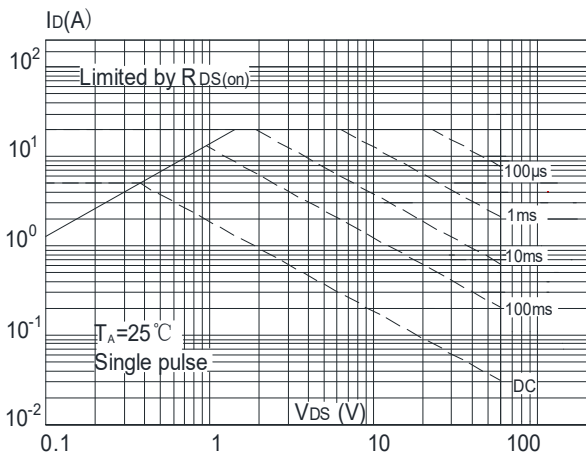


Figure 9: Maximum Safe Operating Area

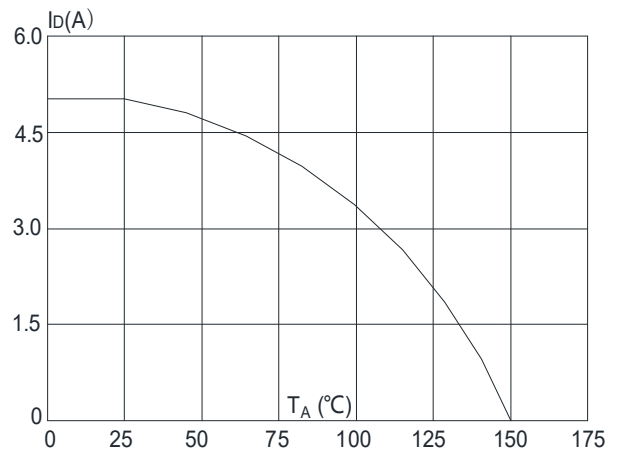


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

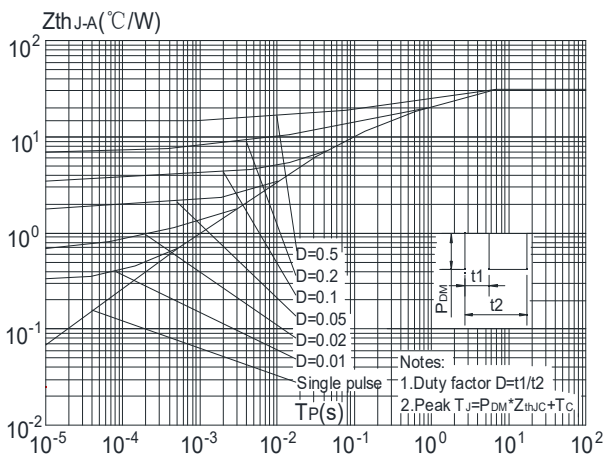


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



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