

General Description:

The LWP4080AM uses trench technology and designs to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications. The package form is SOT23, which accords with the ROHS standard and Halogen Free standard.

Features:

- Fast Switching
- Low Gate Charge and $R_{DS(ON)}$
- Low Reverse transfer capacitances

Applications:

- Battery switching application
- Hard switched and high frequency circuits
- Power Management

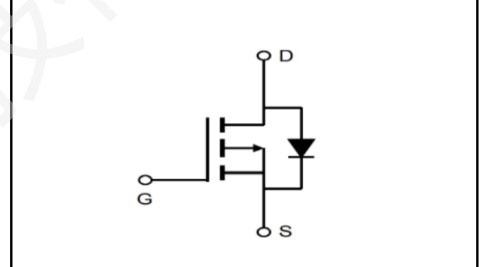


V_{DSS}	-40	V
I_D	-3.0	A
P_D	1.2	W
$R_{DS(ON)}$ TYPE	63	mΩ

Marking and Pin Assignment



Inner Equivalent Principium Chart



Package Marking and Ordering Information:

Marking	Part Number	Package	Packing	Qty.
4080A	LWP4080AM	SOT23	Reel	3000 Pcs

Absolute Maximum Ratings:

Symbol	Parameter		Value	Units
V_{DSS}	Drain-to-Source Voltage		-40	V
I_D	Continuous Drain Current	$T_A=25^{\circ}\text{C}$	-3.0	A
	Continuous Drain Current	$T_A=100^{\circ}\text{C}$	-1.9	A
I_{DM}^{a1}	Pulsed Drain Current		-12	A
V_{GS}	Gate-to-Source Voltage		± 20	V
P_D	Power Dissipation		1.2	W
T_J, T_{STG}	Operating Junction and Storage Temperature Range		150, -55 to 150	$^{\circ}\text{C}$
T_L	Maximum Temperature for Soldering		260	$^{\circ}\text{C}$

Thermal Characteristics:

Symbol	Parameter	Value	Units
$R_{\theta JA}^{a2}$	Thermal Resistance, Junction-to-Ambient	104	$^{\circ}\text{C/W}$

Electrical Characteristic ($T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise specified):

Static Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-40	--	--	V
I_{DSS}	Drain to Source Leakage Current	$V_{DS}=-40V, V_{GS}=0V$	--	--	-1.0	μA
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS}=-20V, V_{DS}=0V$	--	--	-100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS}=+20V, V_{DS}=0V$	--	--	100	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.1	-1.5	-2.0	V
$R_{DS(ON)1}$	Drain-to-Source On-Resistance	$V_{GS}=-10V, I_D=-3.0A$	--	63	80	$m\Omega$
$R_{DS(ON)2}$	Drain-to-Source On-Resistance	$V_{GS}=-4.5V, I_D=-2.0A$	--	83	100	$m\Omega$

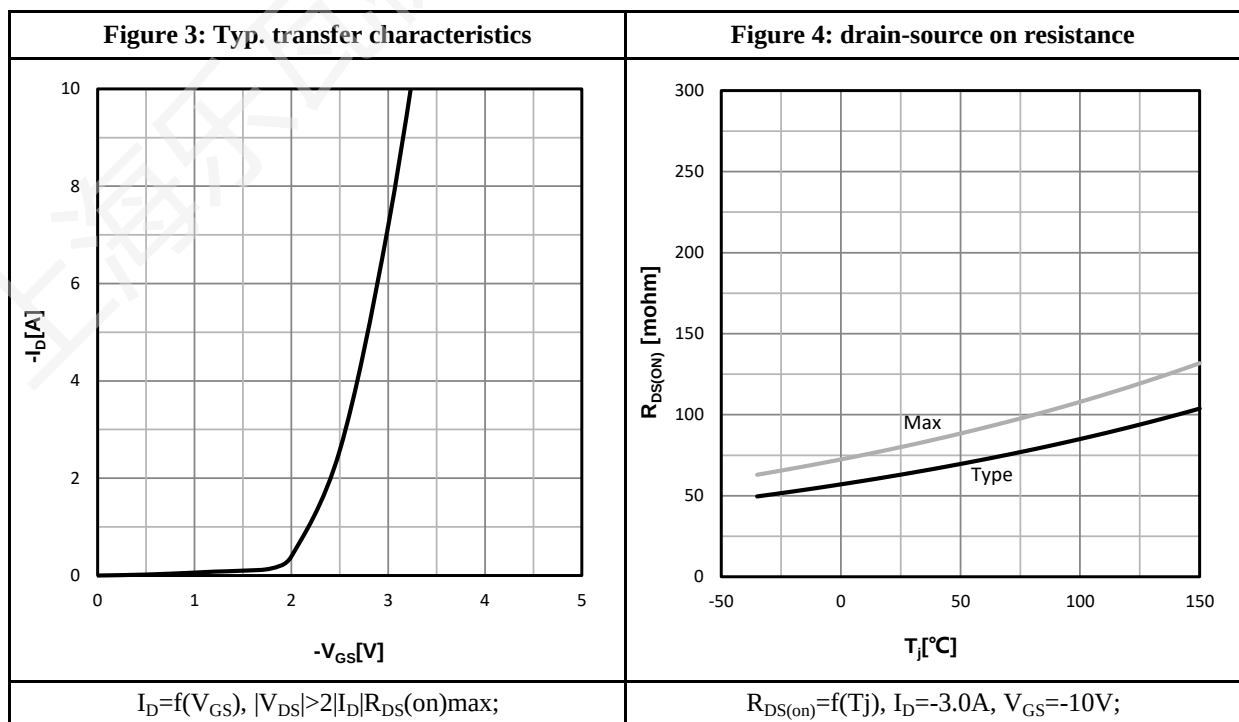
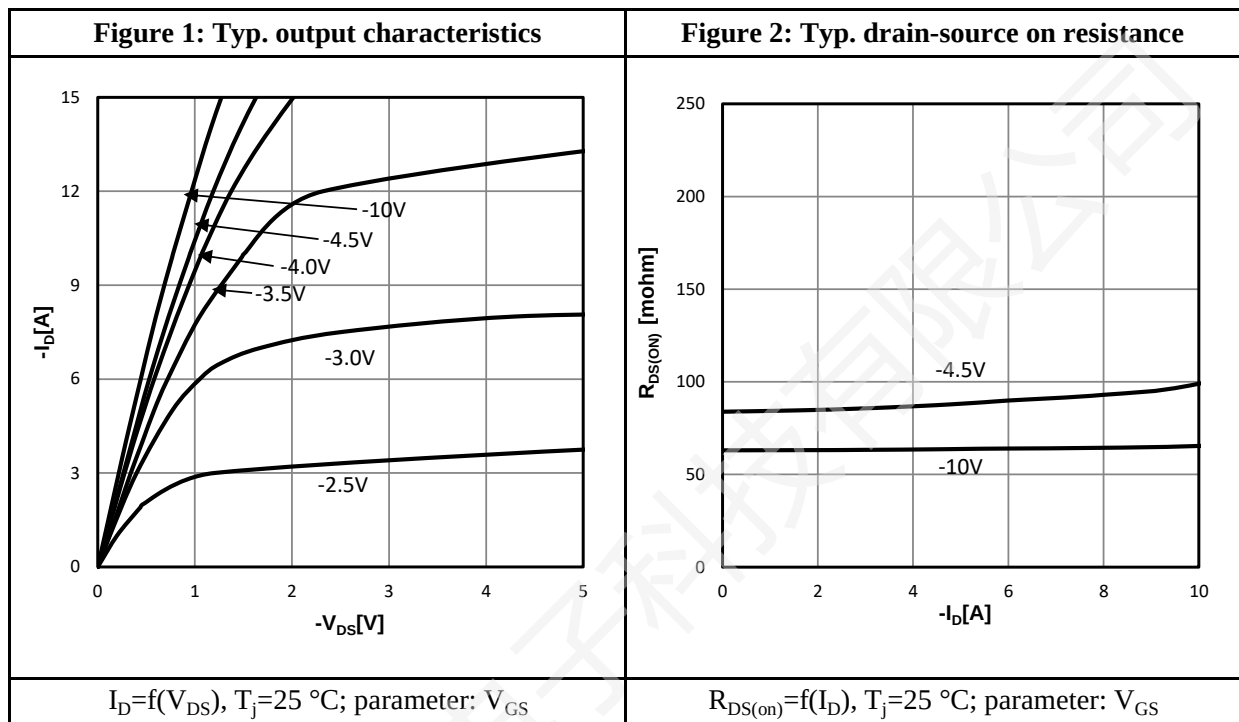
Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
C_{iss}	Input Capacitance	$V_{GS}=0V$	--	489	--	pF
C_{oss}	Output Capacitance	$V_{DS}=-20V$	--	46	--	
C_{rss}	Reverse Transfer Capacitance	$f=1.0MHz$	--	41	--	
R_G	Gate resistance	$V_{GS}=0V, V_{DS}=0V, f=1MHz$	--	5.7	--	Ω

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$I_D=-3.0A$	--	7.8	--	ns
t_r	Rise Time	$V_{DS}=-20V$	--	13	--	
$t_{d(OFF)}$	Turn-Off Delay Time	$V_{GS}=-10V$	--	16.4	--	
t_f	Fall Time	$R_G=3.0\Omega$	--	6.6	--	
Q_g	Total Gate Charge	$V_{GS}=-10V$	--	11	--	nC
Q_{gs}	Gate to Source Charge	$V_{DS}=-20V$	--	1.7	--	
Q_{gd}	Gate to Drain Charge	$I_D=-3.5A$	--	2.0	--	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
I_S	Diode Forward Current	$T_A=25\text{ }^{\circ}\text{C}$	--	--	-3.0	A
V_{SD}	Diode Forward Voltage	$I_S=-3.0A, V_{GS}=0V$	--	--	-1.2	V

a1: Repetitive rating; pulse width limited by maximum junction temperature

a2: Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm² (one layer, 70 μm thick) copper area for drain connection.

Characteristics Curve:


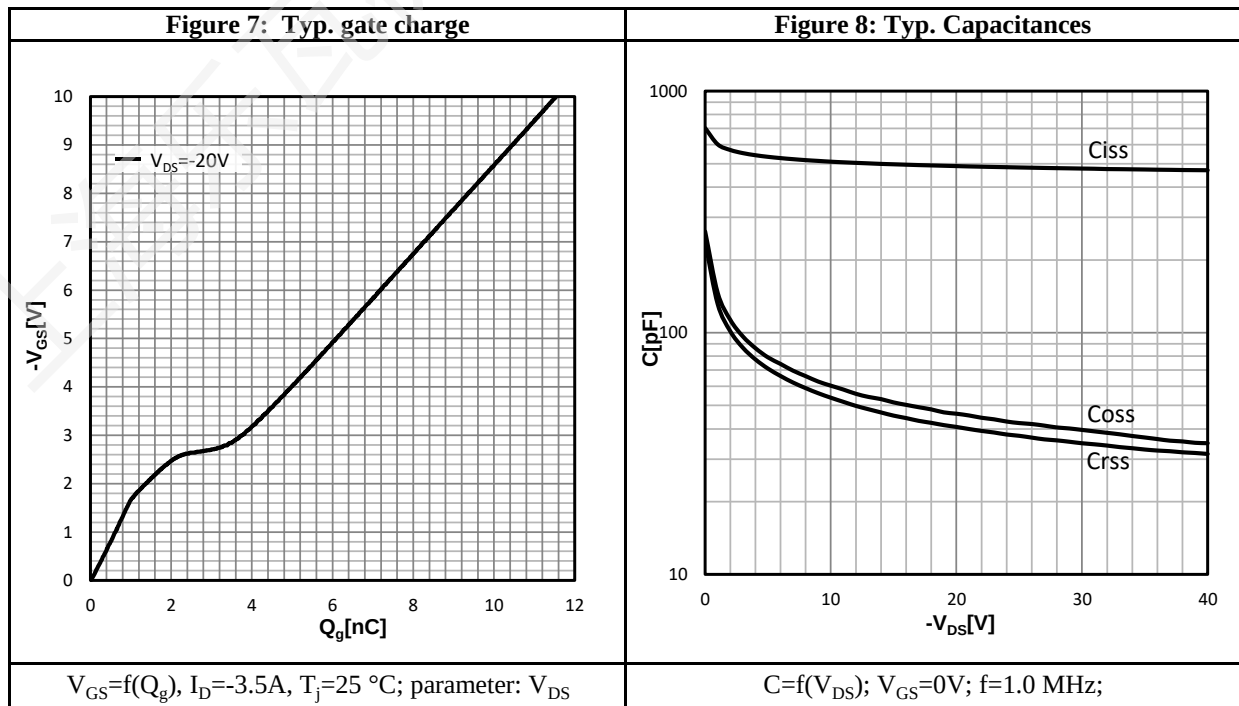
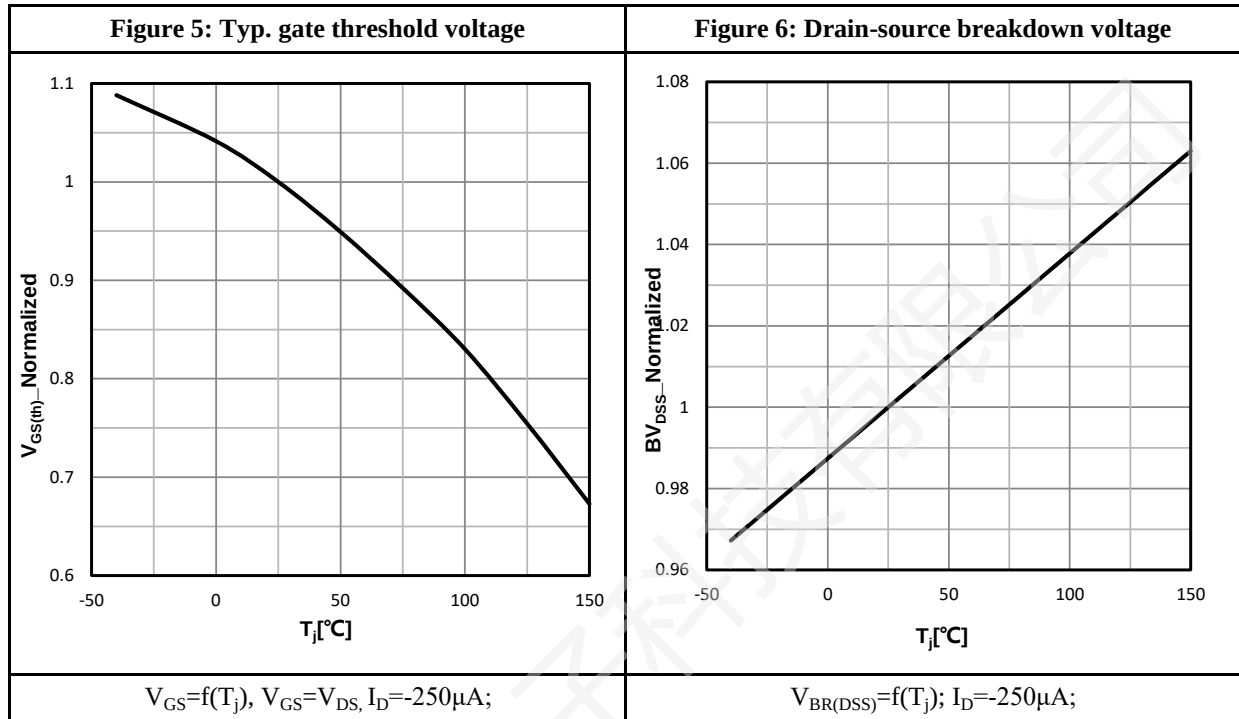
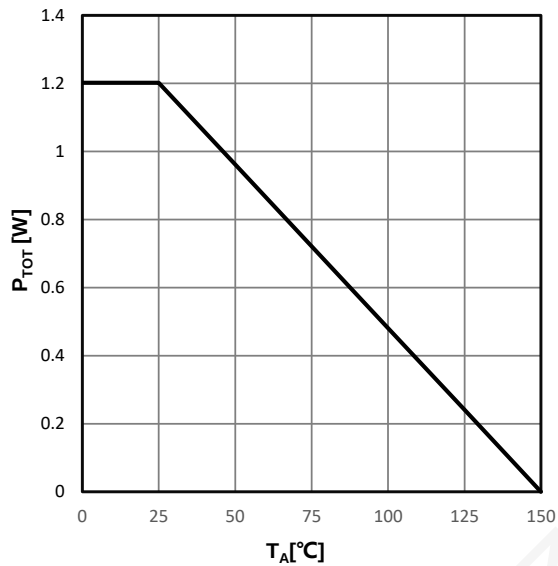
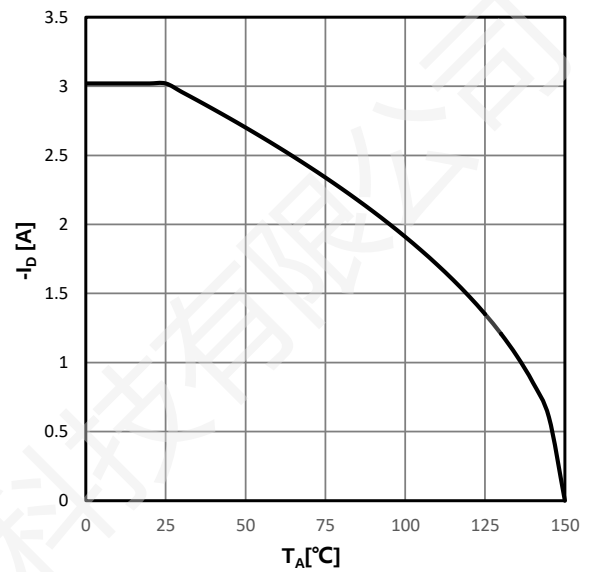
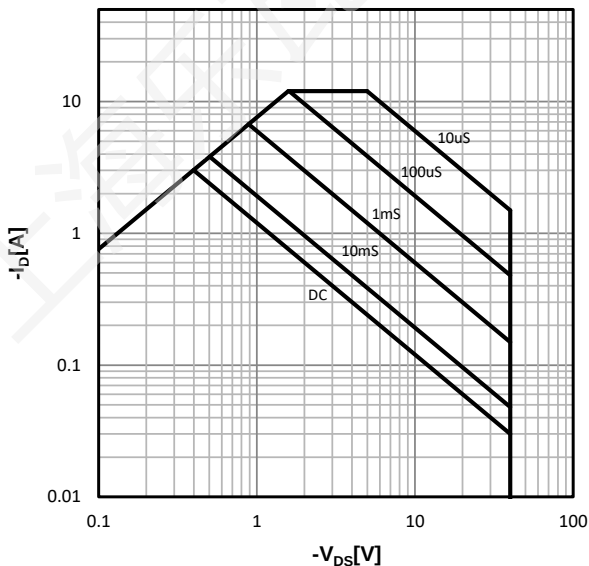


Figure 9: Power dissipation


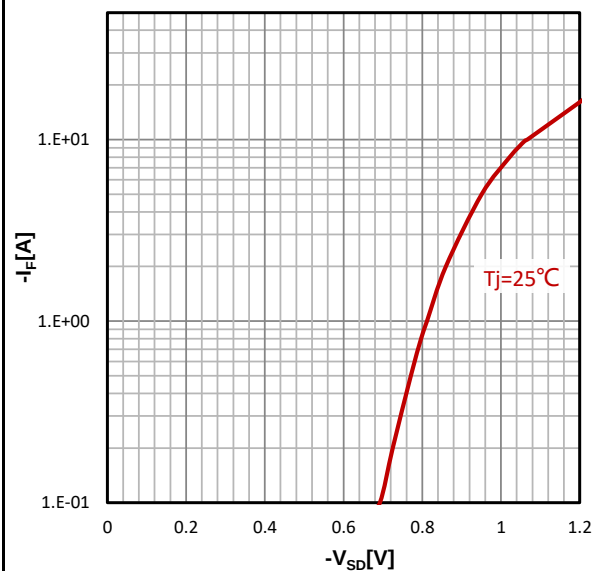
$$P_{tot}=f(T_A);$$

Figure 10: Drain current


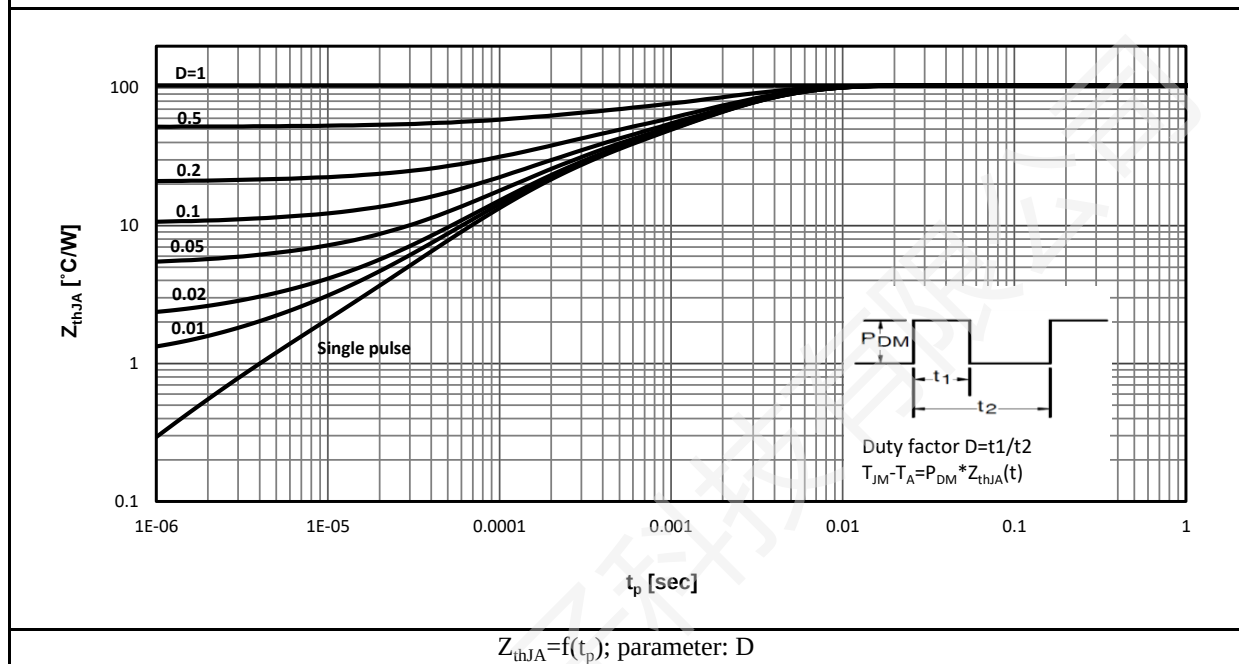
$$I_D=f(T_A);$$

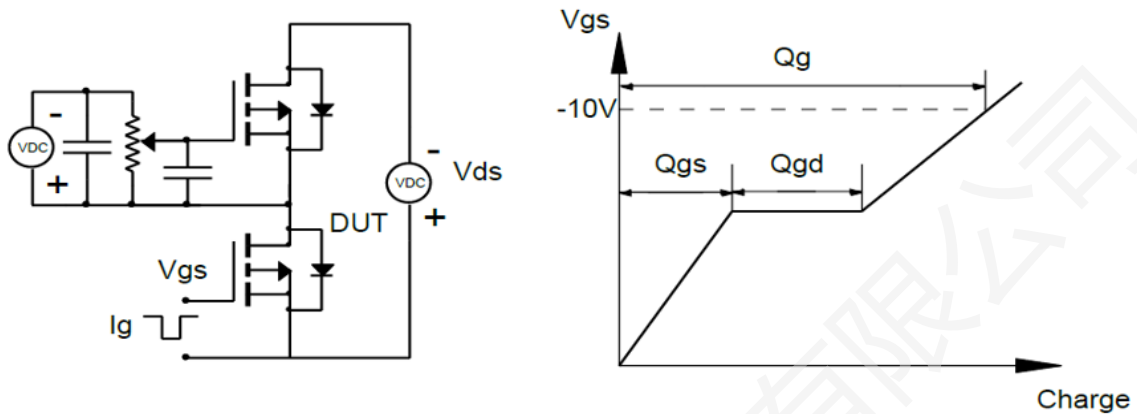
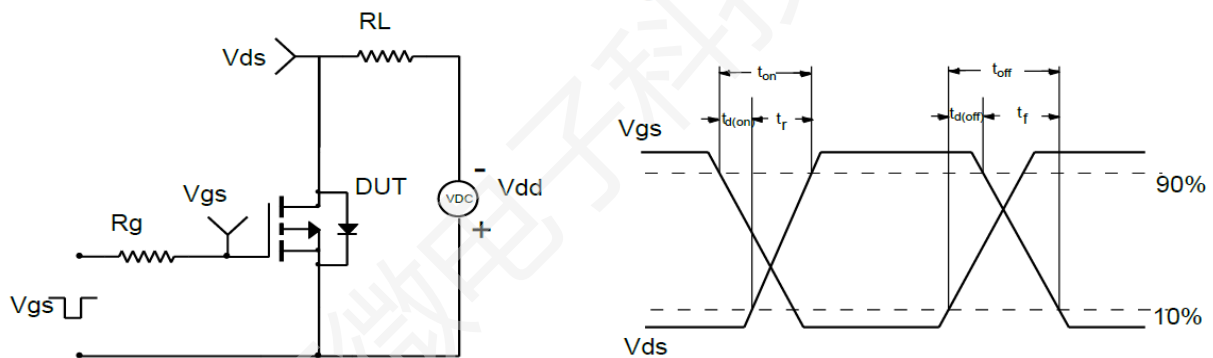
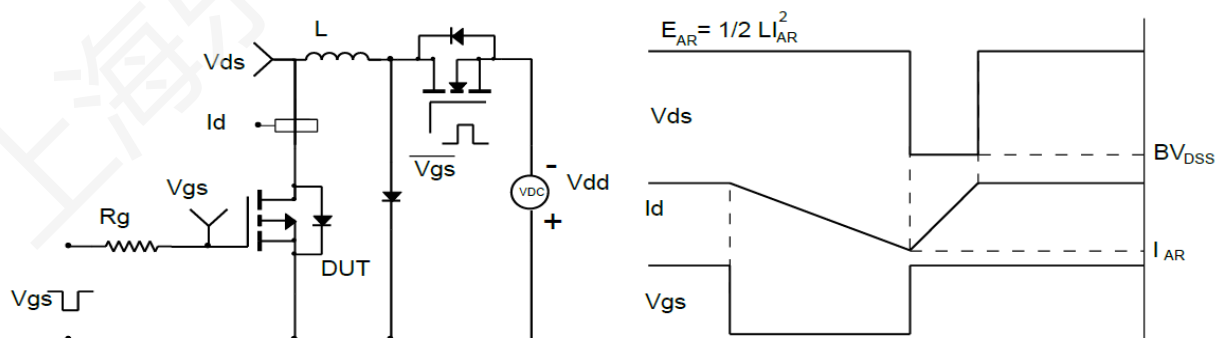
Figure 11: Safe operating area


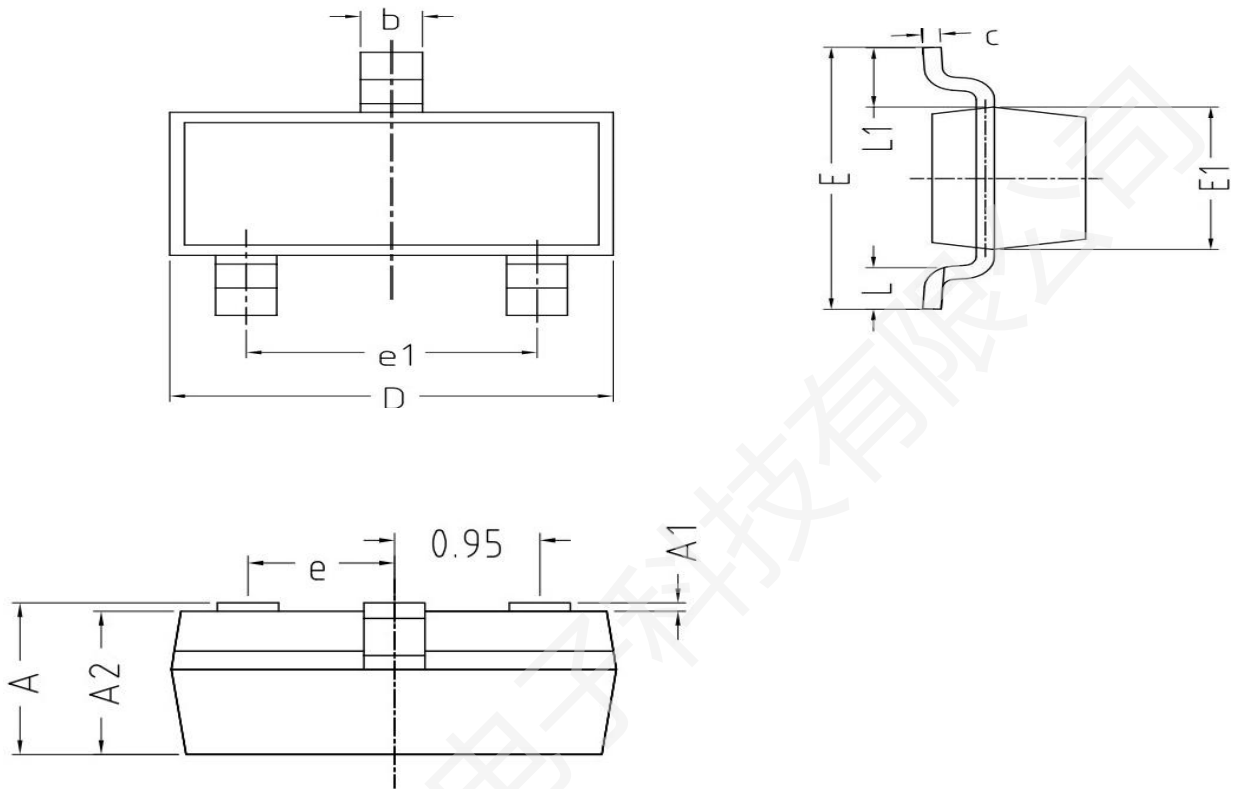
$$I_D=f(V_{DS}); T_A=25\text{ }^{\circ}\text{C}; D=0; \text{ parameter: tp}$$

Figure 12: Typ. forward characteristics


$$I_F=f(V_{SD});$$

Figure 13: Max. Transient Thermal Impedance


Test Circuit & Waveform:

Figure 14: Gate Charge Test Circuit & Waveform

Figure 15: Resistive Switching Test Circuit & Waveforms

Figure 16: Unclamped Inductive Switching Test Circuit & Waveforms

Package Outline:


COMMON IN DIMENSION (MM)			
Symbol	Min.	Nom.	Max.
A	0.900	1.050	1.150
A1	0.000	0.050	0.100
A2	0.900	1.000	1.050
b	0.300	0.400	0.500
C	0.100	0.175	0.250
D	2.800	2.900	3.000
E	2.250	2.400	2.550
E1	1.200	1.300	1.400
e	0.950 TYP		
e1	1.800	1.900	2.000
L	0.290	0.395	0.500
L1	0.550REF		

Revision History:

Revision	Date	Descriptions
Rev 1.2	Feb.2026	“Electrical Characteristic ” Update
Rev 1.1	Dec.2025	Formal Version

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