

## Features

- Low operating voltage: 15V
- Ultra low capacitance: 60pF (Max)
- Ultra low leakage: nA level
- Low clamping voltage
- -IEC 61000-4-2 (ESD) immunity test
  - Air discharge:  $\pm 30\text{kV}$
  - Contact discharge:  $\pm 30\text{kV}$
- -IEC61000-4-4 (EFT) 40A (5/50ns)
- -IEC61000-4-5 (Lightning) 6A (8/20 $\mu\text{s}$ )
- 2-pin leadless package
- These are Pb-Free Devices
- Response Time is Typically < 1 ns

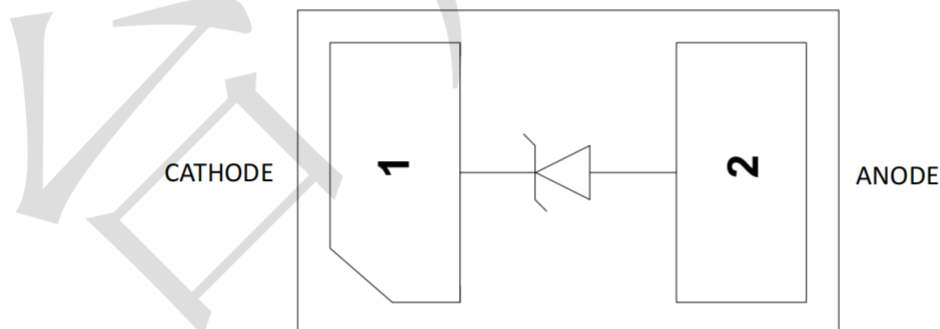
## Mechanical Characteristics

- Package: DFN1006-2(0402)
- Lead Finish: Matte Tin
- Case Material: "Green" Molding Compound
- Terminal Connections: See Diagram Below
- -IEC 61000-4-2 (ESD) immunity test

## Applications

- Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- Portable Instrumentation
- Peripherals

## Dimensions and Pin Configuration



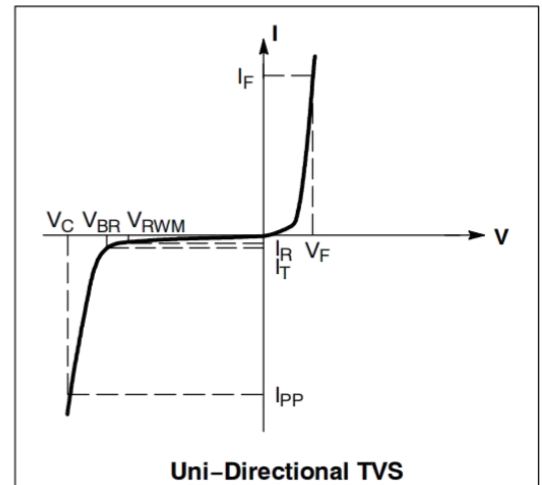
**Absolute Maximum Ratings**( $T_{amb}=25^{\circ}C$  unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 $\mu$ s)	Ppk	180	W
Peak Pulse Current (8/20 $\mu$ s)	Ipp	6	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	VESD	$\pm 30$ $\pm 30$	KV
Operating Temperature Range	TJ	-55 to +125	$^{\circ}C$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}C$

**Electrical Characteristics** ( $T_A=25^{\circ}C$  unless otherwise specified)

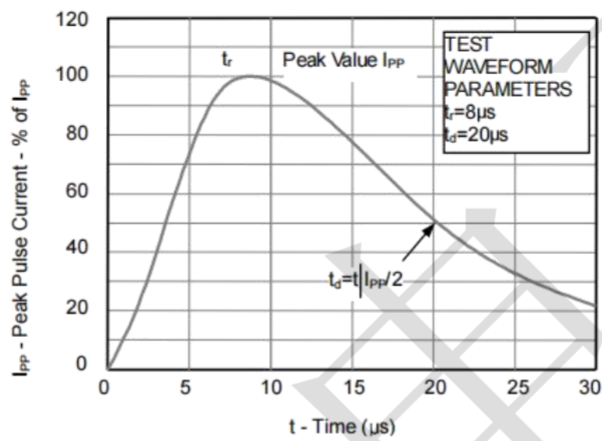
Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM	--	--	15	V	
Breakdown Voltage	VBR	16	--	--	V	$I_T=1mA$
Forward voltage	VF	--	0.8	1.2	V	$I_F=1mA$
Reverse Leakage Current	IR	--	--	0.5	$\mu A$	$VRWM=15V$
Clamping Voltage	VC	--	25	--	V	$I_{pp}=1A(8x 20\mu s \text{ pulse})$
Clamping Voltage	VC	--	22	35	V	$I_{pp}=6A(8x 20\mu s \text{ pulse})$
Junction Capacitance	CJ	--	--	60	pF	$VR = 0V, f = 1MHz$

Symbol	Parameter
$I_{PP}$	Maximum Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_{PP}$
$V_{RWM}$	Working Peak Reverse Voltage
$I_R$	Maximum Reverse Leakage Current @ $V_{RWM}$
$V_{BR}$	Breakdown Voltage @ $I_T$
$I_T$	Test Current
$I_F$	Forward Current
$V_F$	Forward Voltage @ $I_F$
$P_{pk}$	Peak Power Dissipation
C	Capacitance @ $V_R = 0$ and $f = 1.0 \text{ MHz}$

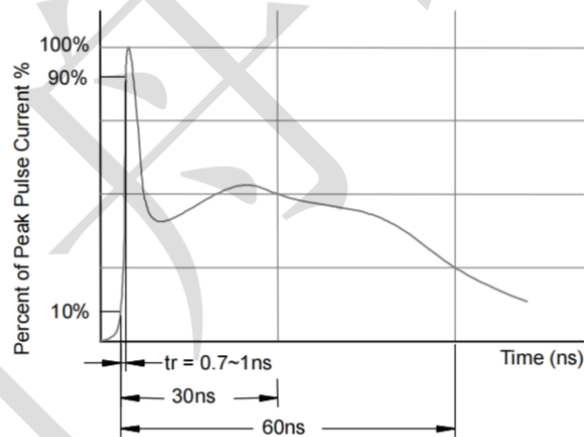


**Characteristic Curves**

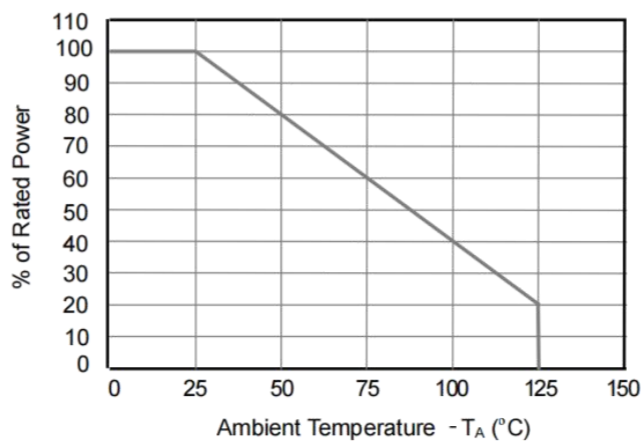
**Fig1. 8/20 $\mu$ s Pulse Waveform**



**Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)**

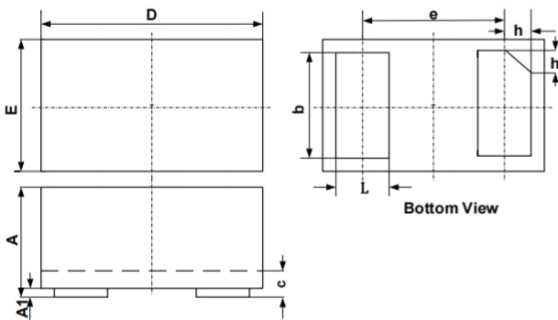


**Fig3. Power Derating Curve**



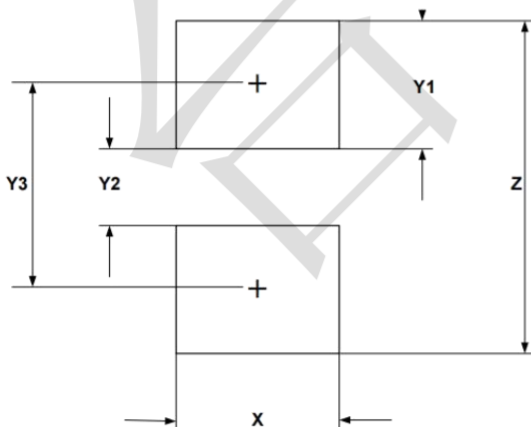
## Package Outline & Dimensions

DFN1006-2 (0402)



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.45	0.50	0.55	0.018	0.020	0.022
c	0.12	0.15	0.18	0.005	0.006	0.007
D	0.95	1.00	1.05	0.037	0.039	0.041
e	0.65 BSC			0.026 BSC		
E	0.55	0.60	0.65	0.022	0.024	0.026
L	0.20	0.25	0.30	0.008	0.010	0.012
h	0.07	0.12	0.17	0.003	0.005	0.007

## Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X	0.60	0.024
Y1	0.50	0.020
Y2	0.30	0.012
Y3	0.80	0.032
Z	1.30	0.052