

2N7002KM

N-Channel MOSFET

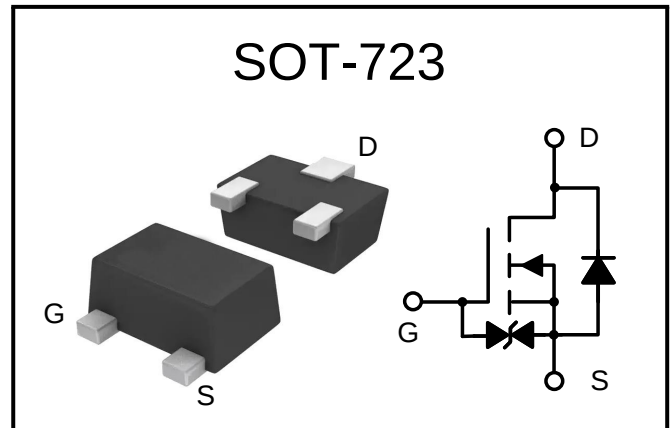
Features

- $V_{DS}=60V$
- $I_D=0.34A$
- $R_{DS(ON)}@V_{GS}=10V, MAX=2.5\Omega$
- $R_{DS(ON)}@V_{GS}=4.5V, MAX=3.0\Omega$

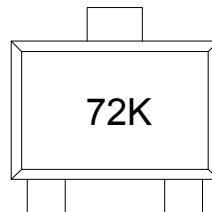
Description

- Super High dense cell design for extremely low $R_{DS(ON)}$
- Reliable and Rugged
- SOT-723 for Surface Mount Package
- ESD protected

Package



Marking



Ordering information

Order code	Package	Marking	Base qty	Delivery mode
2N7002KM	SOT-723	72K	8K	Tape and reel

Absolute Maximum Ratings (@ $T_A=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current ^{1,5}	0.34	A
I_{DM}	Pulsed Drain Current ²	1.0	A
P_D	Maximum Power Dissipation ^{4,5}	0.2	W
T_J	Junction Temperature	150	$^{\circ}C$
T_{stg}	Storage Temperature	-55 to + 150	$^{\circ}C$
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ⁵	625	$^{\circ}C/W$

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Electrical Characteristics @T_A=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
OFF Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D =250uA	60	–	–	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 60V, V _{GS} = 0V	–	–	1	uA
I _{GSS}	Gate-body leakage	V _{GS} = ±20V, V _{DS} = 0V	–	–	±10	uA
On Characteristics ³						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250uA	1	1.5	2.5	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} = 10V, I _D = 0.3A	–	0.9	2.5	Ω
		V _{GS} = 4.5V, I _D = 0.2A	–	1.1	3.0	
g _{fs}	Forward transconductance	V _{DS} = 30V, I _D = 0.2A	–	159	–	ms
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 30V, V _{GS} = 0V, f = 1.0MHZ	–	23.7	–	pF
C _{oss}	Output Capacitance		–	5.3	–	
C _{rss}	Reverse Transfer Capacitance		–	2.5	–	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 30V, V _{GS} = 10V, I _D = 0.3A, R _G = 3Ω, R _L = 100Ω	–	3.5	–	ns
t _r	Rise time		–	3.2	–	
t _{d(off)}	Turn-off delay time		–	12	–	
t _f	Fall time		–	10	–	
Q _g	Total Gate Charge	V _{DS} = 30V I _D = 0.3A, V _{GS} = 10V	–	0.29	–	nC
Q _{gs}	Gate-Source Charge		–	0.23	–	
Q _{gd}	Gate-Drain Charge		–	0.12	–	
DRAIN-SOURCE DIODE CHARACTERISTICS						
V _{SD}	Diode forward voltage ³	V _{GS} = 0V,I _S = 0.3A	–	–	-1.2	V

Notes:

- 1.The maximum current rating is limited by package.
- 2.Pulse Test : Pulse Width ≤ 10μs, duty cycle ≤ 1%.
- 3.Pulse Test : Pulse Width ≤ 300μs, duty cycle ≤ 2%.
- 4.The power dissipation PD is limited by T_J(MAX) = 150° C.
- 5.Device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A = 25°C.



Typical Performance Characteristics ($T_J = 25^\circ\text{C}$, unless otherwise noted)

Figure 1 :Output Characteristics

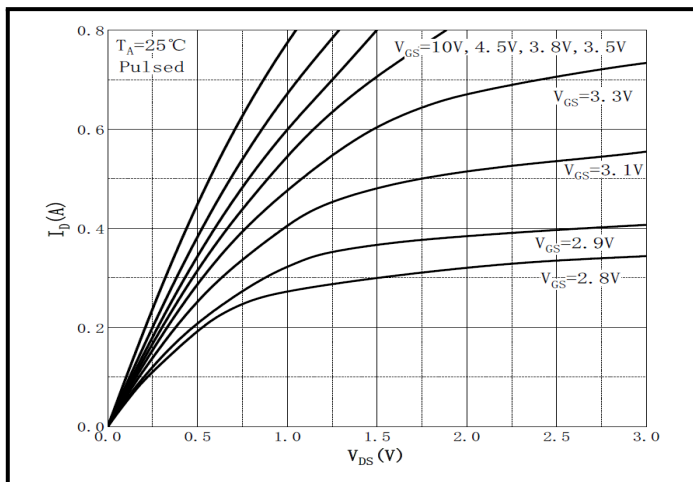


Figure 2 :Transfer Characteristics

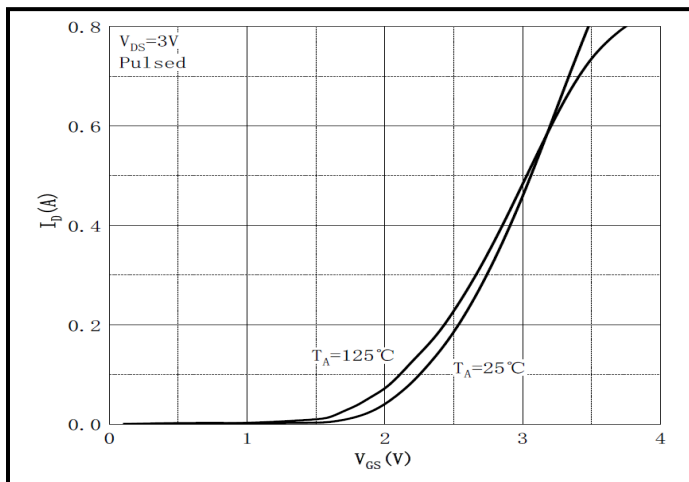


Figure 3 : $R_{DS(ON)}$ VS I_D

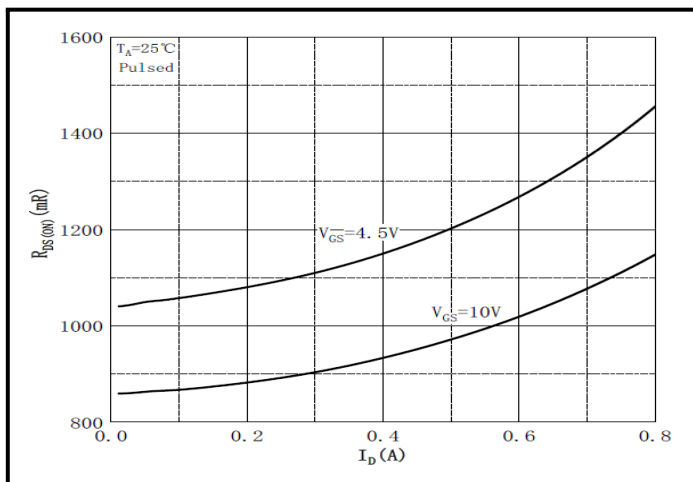
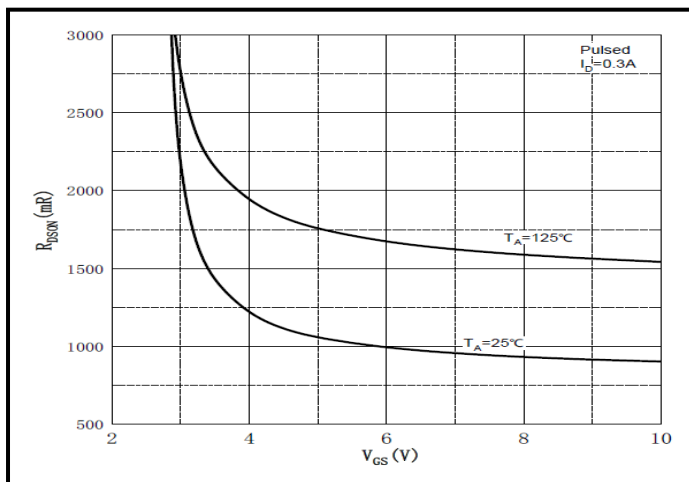


Figure 4 : $R_{DS(ON)}$ VS V_{GS}



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Typical Performance Characteristics($T_J = 25^\circ\text{C}$, unless otherwise noted)

Figure 5 : I_S VS V_{SD}

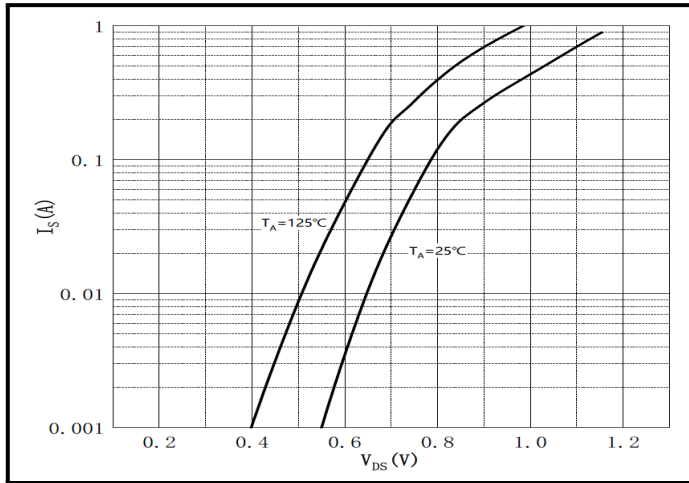
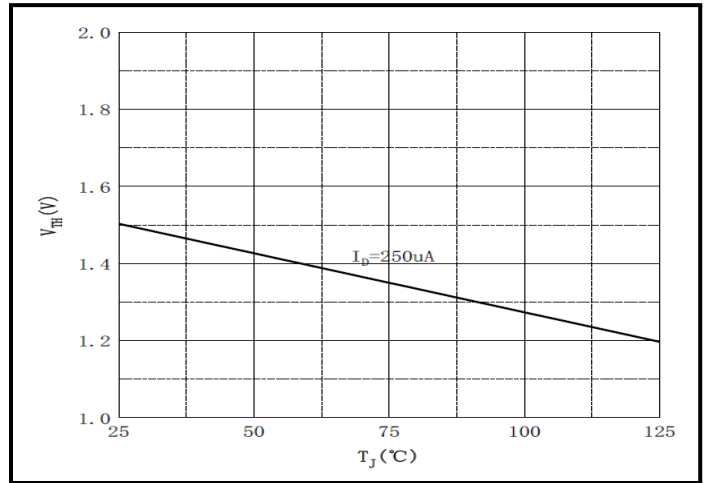


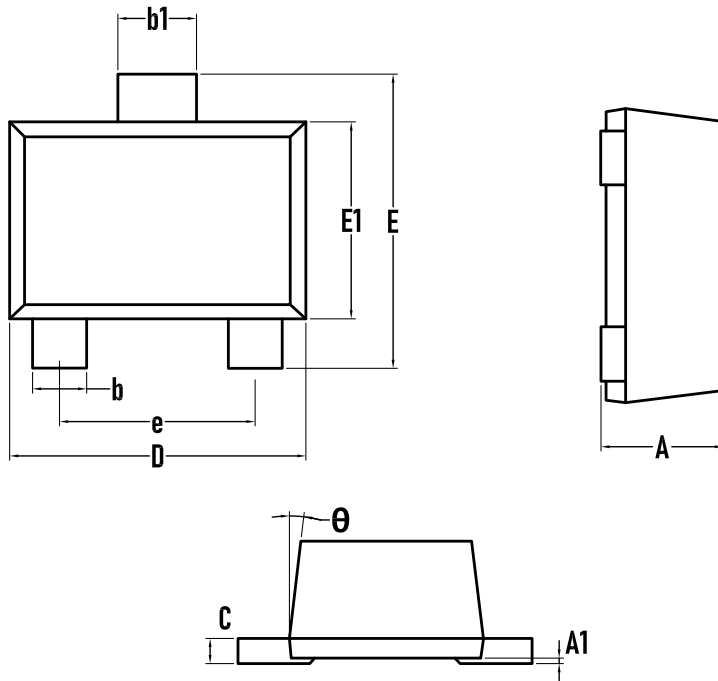
Figure 6 : Threshold Voltage



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Outline Drawing - SOT-723



SYMBOL	MILLIMETER		
	MIN.	Typ.	MAX.
A	0.350	—	0.490
A1	0.000	—	0.100
b	0.170	0.220	0.270
b1	0.225	—	0.370
C	0.080	—	0.150
D	1.150	—	1.250
E	1.150	—	1.250
E1	0.750	—	0.850
e	0.800TYP.		
θ	0°	—	7°

