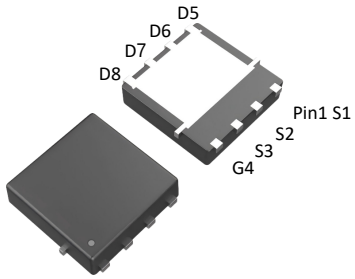
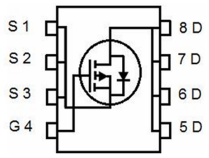
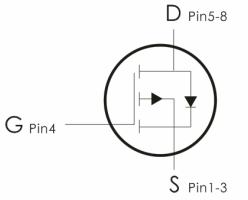
Description：

This P-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.







Features：

1) V_{DS}=-60V,I_D=-20A,$R_{DS(ON)}$<35mΩ @V_{GS}=-10V（Typ：25mΩ）
2) Low gate charge.
3) Green device available.
4) Advanced high cell denity trench technology for ultra low $R_{DS(ON)}$.
5) Excellent package for good heat dissipation.
6) MSL3

Package Marking and Ordering Information：

Part NO.	Marking	Package	Packing
ZE035PG-C	E035P-C	DFN3*3-8	5000 pcs/Reel

Absolute Maximum Ratings：(T_c=25℃ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Continuous Drain Current[1]	-20	A
	Continuous Drain Current-T_c=100℃[1]	-14	
I_{DM}	Pulsed Drain Current[2]	-80	
P_D	Power Dissipation	42	W
E_{AS}	Single pulse avalanche energy[3]	225	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55-+175	℃

Thermal Characteristics：

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance,Junction to Case	3	℃/W
$R_{\theta JA}$	Thermal Resistance,Junction to Ambient	42	℃/W

Electrical Characteristics: (T_c=25℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Sourtce Breakdown Voltage	V_{GS}=0V,I_D=250μA	-60	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{GS}=0V, V_{DS}=-60V	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V, V_{DS}=0A	---	---	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate-Source Threshold Voltage	V_{GS}=V_{DS}, I_D=250μA	-1	-1.69	-2.5	V
$R_{DS(ON)}$	Drain-Source On Resistance[4]	V_{GS}=-10V,I_D=-10A	---	25	35	mΩ
		V_{GS}=-4.5V,I_D=-5A	---	30	40	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS}=-30V, V_{GS}=0V, f=1MHz	---	3500	---	pF
C_{oss}	Output Capacitance		---	130	--	
C_{rss}	Reverse Transfer Capacitance		---	75	---	
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DS}=-30V, I_D=-10A, R_{ENG}=6.8Ω,V_{GS}=-10V	---	14	---	ns
t_r	Rise Time		---	18	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	42	---	ns
t_f	Fall Time		---	15	---	ns
Q_g	Total Gate Charge	V_{GS}=-10V, V_{DS}=-30V, I_D=-10A	---	46	---	nC
Q_{gs}	Gate-Source Charge		---	11	---	nC
Q_{gd}	Gate-Drain “Miller” Charge		---	10	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Diode Forward Voltage	V_{GS}=0V, I_{SD}=-15A	---	-0.88	-1.2	V
I_S	Continuous Drain Curren	V_D=V_G=0V	---	---	-16.6	A
I_{SM}	Pulsed Drain Current		---	---	-100	A
Trr	Reverse Recovery Time	I_F=20A,T_J=25℃ dI/dt=100A/us	---	28	---	ns
Qrr	Reverse Recovery Charge		---	165	---	nC

Notes:

1. omputed continuous current assumes the condition of $T_{j,Max}$ while the actual continuous current depends on the thermal & electro-mechanical application board design
2. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
3. EAS condition : TJ=25℃,VDD=-30V,V_G=-10V,L=0.5mH
4. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Typical Characteristics： (T_c=25℃ unless otherwise noted)

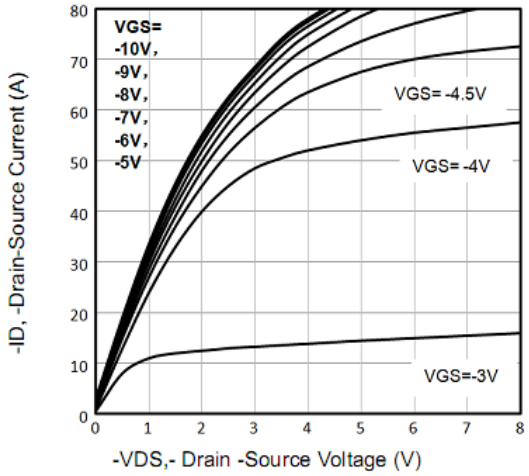


Fig1. Typical Output Characteristics

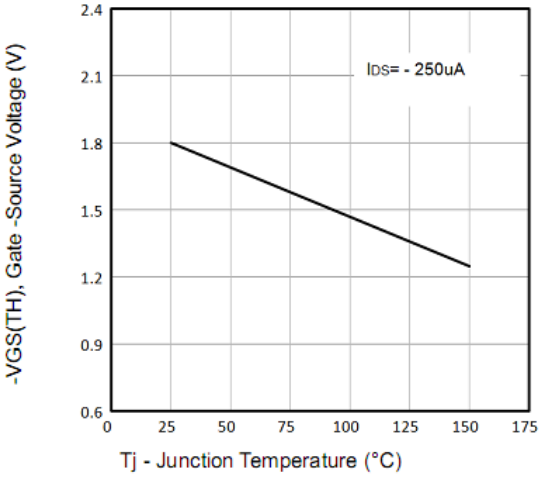


Fig2. -VGS(TH) Gate -Source Voltage Vs.Tj

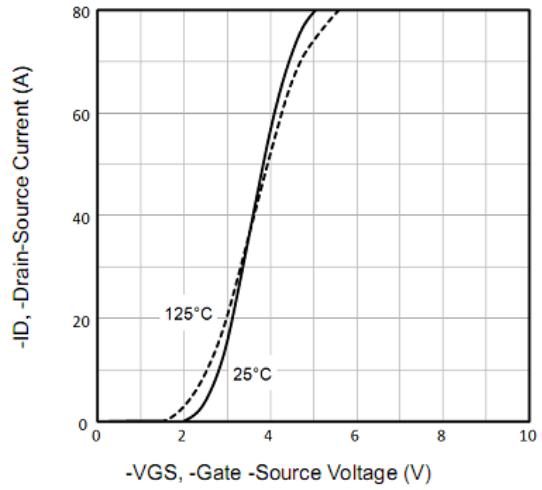


Fig3. Typical Transfer Characteristics

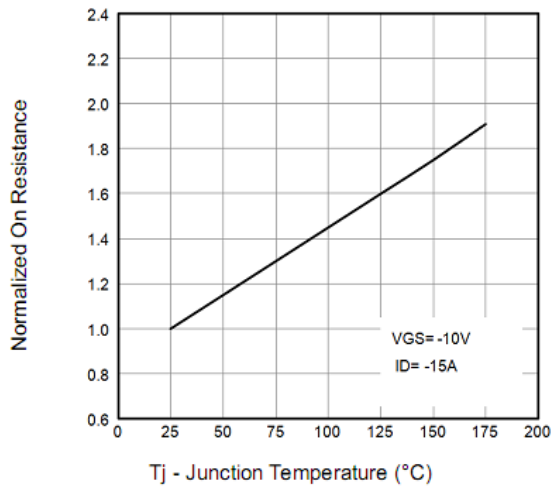


Fig4. Normalized On-Resistance Vs. Tj

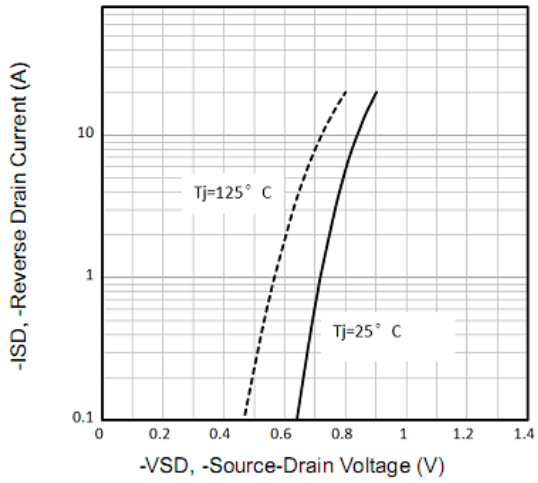


Fig5. Typical Source-Drain Diode Forward Voltage

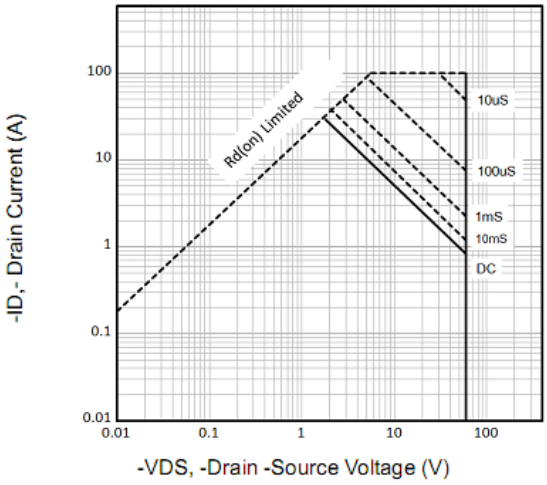


Fig6. Maximum Safe Operating Area

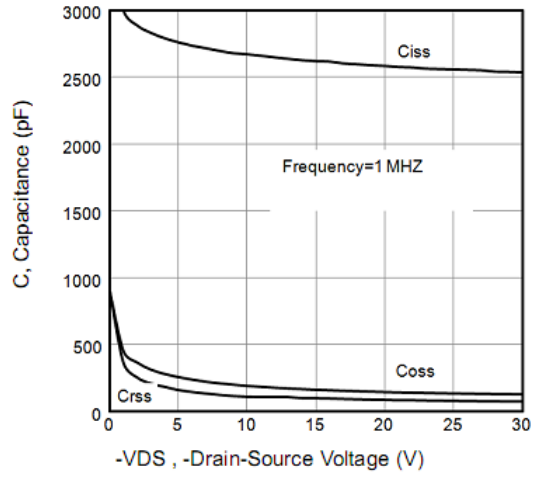


Fig7. Typical Capacitance Vs.Drain-Source Voltage

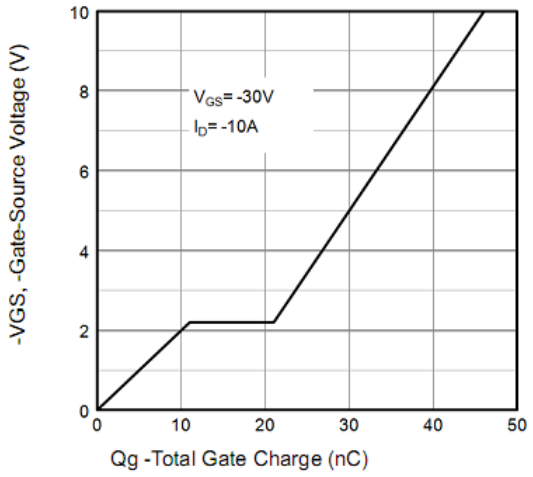


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

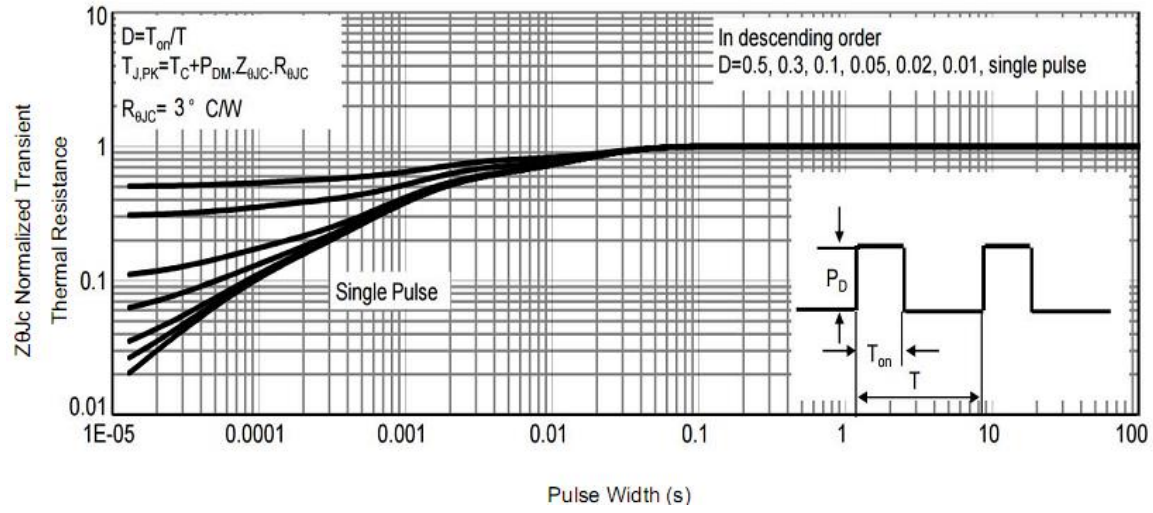
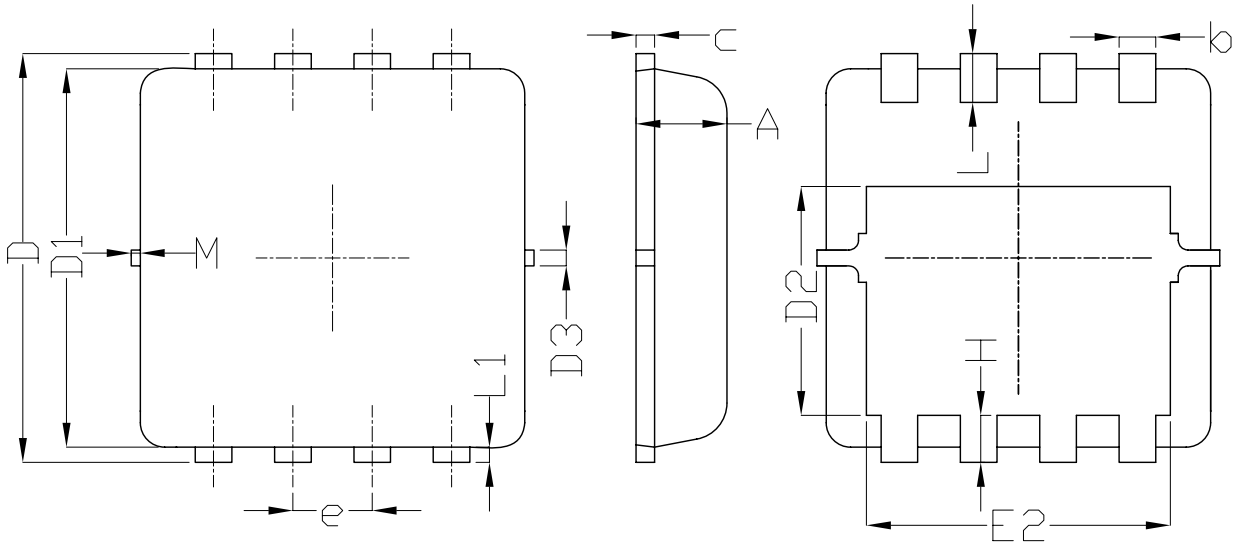
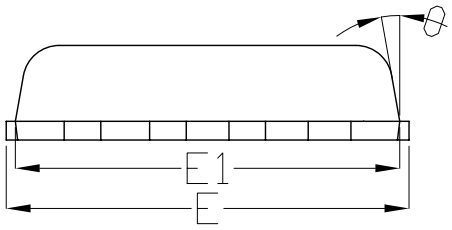
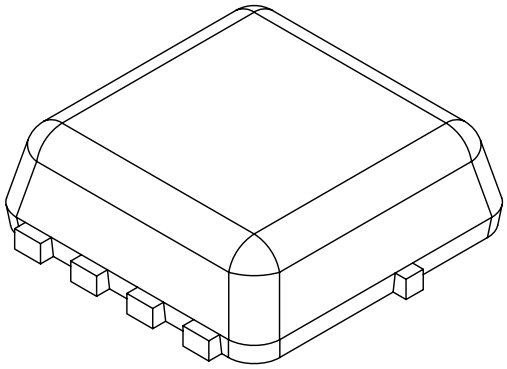


Fig9. Normalized Maximum Transient Thermal Impedance

DFN3X3-8 Package Information：

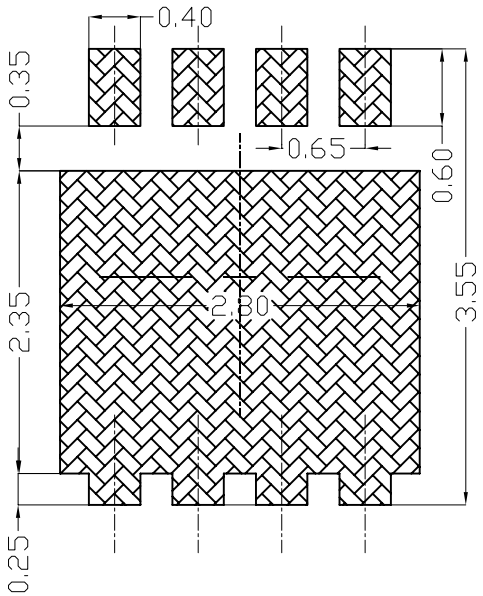






SYMBOL	DIMENSIONAL REQMTS		
	MIN	NOM	MAX
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3	---	0.13	---
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	---	0.13	---
θ	---	10°	12°
M	*	*	0.15

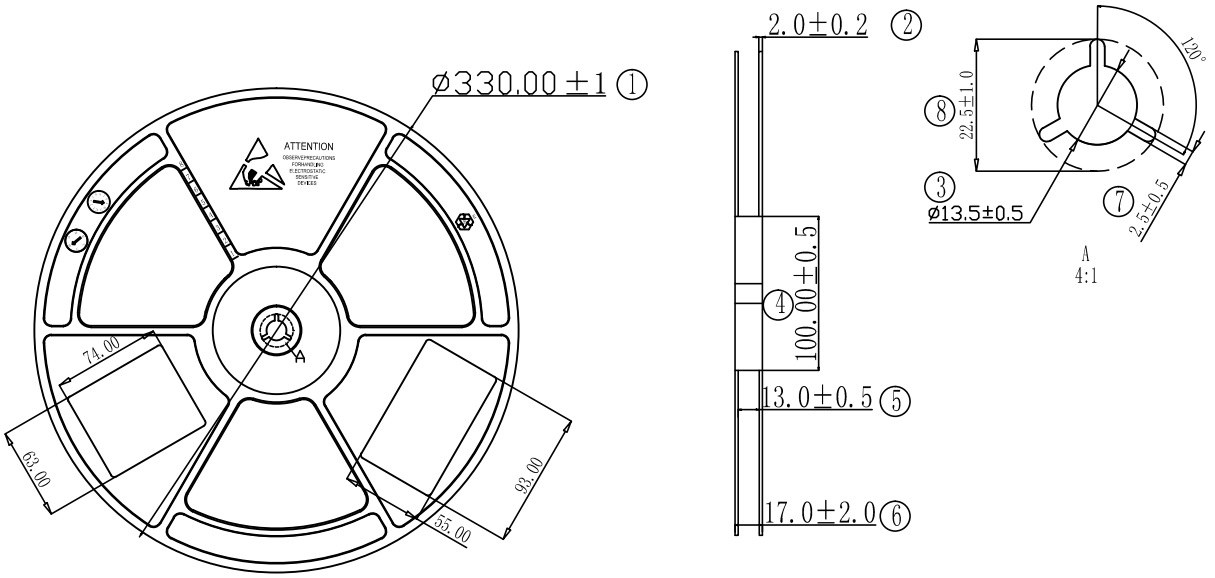
* Not specified



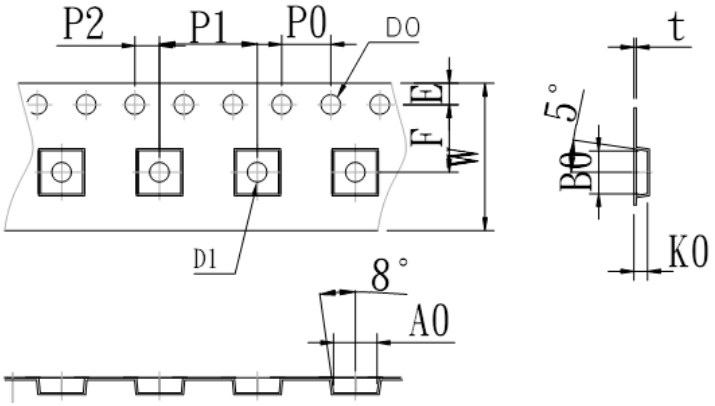
UNIT: mm

Tape & Reel Information

Dimensions in mm



Symbol	A0	B0	K0	D0	D1	P0	P1	10*P0
Spec	3.55±0.10	3.45±0.10	1.13±0.10	1.55±0.10	1.55±0.10	4.00±0.10	8.00±0.10	40.0±0.10
Symbol	W	E	F	P2	t			
Spec	12.00±0.10	1.75±0.10	5.50±0.10	2.00±0.10	0.20±0.05			



Pulling direction

Marking Information：

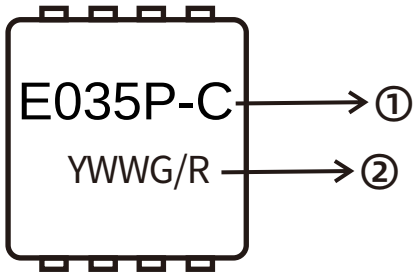
①. Part NO.

②. Date Code(YWWG / R)

Y：Year Code，last digit of the year

WW：Week Code(01-53)

G/R：G(Green) /R(Lead Free)



Previous Version

Version	Date	Subjects (major changes since last revision)
1.1	2025-10-15	Release of final version

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