

DEP-DK Series

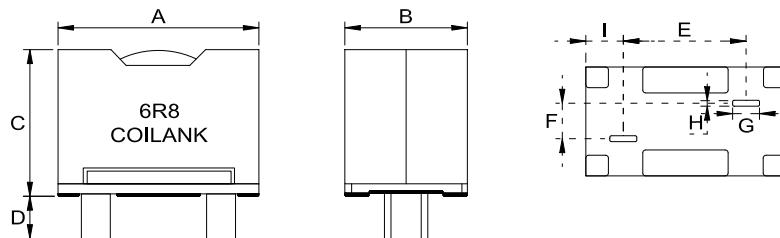
Features

- Assemblage design, sturdy structure.
- High inductance, high current, low magnetic loss, low ERS, small parasitic capacitance.
- Flat wire winding, achieve a low DCR.
- Temperature rise current and saturation current is less influenced by environment.
- Placement form is DIP.

Test Conditions

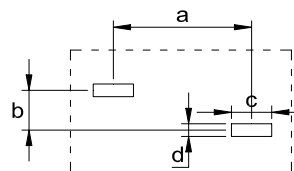
- All test data is referenced to 25°C ambient.
- Operating temperature range -25°C to +125°C. (Including self - temperature rise)
- Isat :DC current(A) that will cause Io to drop approximately 30%.
- Irms :DC current(A) that will cause an approximate ΔT of 40°C.
- The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions.
Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature, part temperature should be verified in the end application.

External Dimensions (Unit:mm)

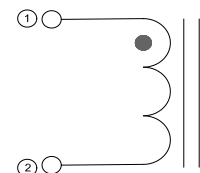


TYPE	A	B	C	D	E	F	G	H	I
DEP2724	27.5Max	17Max	24Max	4.0±1.0	16.5±0.5	5.5±0.5	3.8±0.5	0.8±0.2	5.0Typ

a(Ref.)	b(Ref.)	c(Ref.)	d(Ref.)	Q'TY/Reel
16.5	5.5	5.0	1.5	280



Typical Land Pattern



Schematic

Part Number Code

DEP	27	24	L	6R8	DK
Series Name	Dimensions: L*W	Dimensions: H	Tolerance 15%	Inductance	Internal Control

DEP-DK Series

Part Number	Inductance (μ H) @100KHz/0.1V	DC Resistance (m Ω) Max.	Heat Rating Current I _{rms} (A)Typ.	Saturation Current I _{sat} (A)Typ.
DEP2724L6R8DK	6.8	2.6	26	54.5
DEP2724L100DK	10	2.6	26	37
DEP2724L150DK	15	2.6	26	24.5
DEP2724L220DK	22	2.6	26	16.4
DEP2724L330DK	33	2.6	26	10.3