

100V N-Channel Power MOSFET

Description

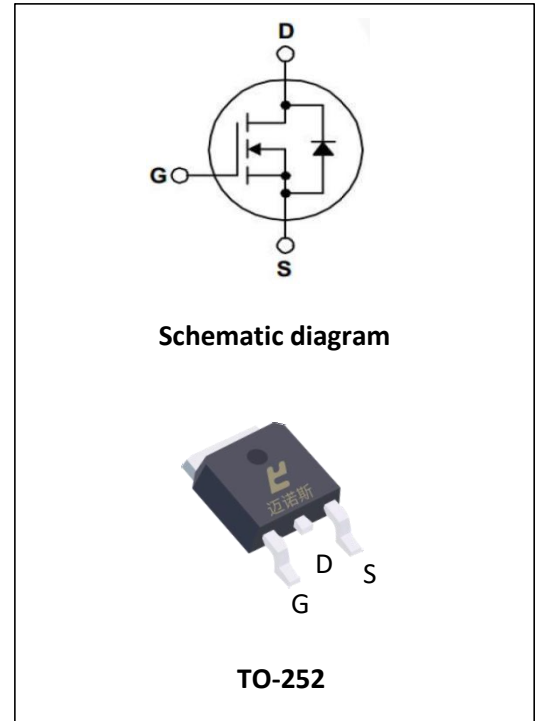
The IRFR3910TR uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.

General Features

- ① $V_{DS}=100V, I_D=15A$
 $R_{DS(ON)} < 110m\Omega @ V_{GS}=10V$
- ② Special process technology for high ESD capability
- ③ High density cell design for ultra low R_{dson}
- ④ Fully characterized avalanche voltage and current
- ⑤ Good stability and uniformity with high EAS
- ⑥ Excellent package for good heat dissipation

Application

- ① Power switching application
- ② Hard switched and High frequency circuits
- ③ Uninterruptible power supply



Package Marking And Ordering Information

Ordering Codes	Package	Product Code	Packing
IRFR3910TR	TO-252	FR3910	Reel

ABSOLUTE RATINGS (at $T_C=25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	15	A
I_{DM}	Drain Current-Pulsed (Note 1)	40	A
P_D	Maximum Power Dissipation($T_C=25^\circ C$)	31	W
E_{AS}	Single pulse avalanche energy (Note 2)	21	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 175	$^\circ C$

Thermal Characteristic

Symbol	Parameter	Typ	Units
$R_{\theta JC}$	Junction-to-Case	4.8	$^\circ C/W$

Electrical Characteristics (Tc=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Units
Off Characteristics						
B _{VDS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	100	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =100V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	1.8	2.4	V
R _{DS(ON)}	Drain-Source On-State Resistance ^(Note 3)	V _{GS} =10V, I _D =5A	--	90	110	mΩ
G _{FS}	Forward Transconductance	V _{DS} =25V, I _D =3.6A	--	5	--	S
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, F=1.0MHz	--	680	--	pF
C _{OSS}	Output Capacitance		--	110	--	pF
C _{RSS}	Reverse Transfer Capacitance		--	85	--	pF
Switching Characteristics ^(Note 4)						
t _{d(on)}	Turn-on Delay Time	V _{DD} =50V, I _D =5A, V _{GS} =10V, R _{GEN} =2.5Ω	--	10	--	nS
t _r	Turn-on Rise Time		--	7	--	nS
t _{d(off)}	Turn-Off Delay Time		--	34	--	nS
t _f	Turn-Off Fall Time		--	9	--	nS
Q _g	Total Gate Charge	V _{DS} =80V, I _D =3A V _{GS} =10V	--	16	--	nC
Q _{gs}	Gate-Source Charge		--	4	--	nC
Q _{gd}	Gate-Drain Charge		--	5	--	nC
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage(Note 3)	V _{GS} =0V, I _S =15A	--	--	1.2	V

Notes:

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2.EAS condition :T_J=25 °C, V_{DD}=50V, V_G=10V, L=0.5mH, R_g=25Ω
- 3.Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
- 4.Guaranteed by design, not subject to production.

Characteristics Curves

Figure 1 Output Characteristics

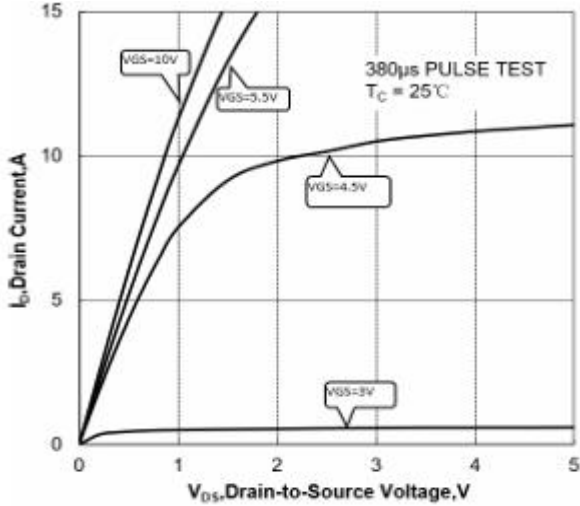


Figure 2 Transfer Characteristics

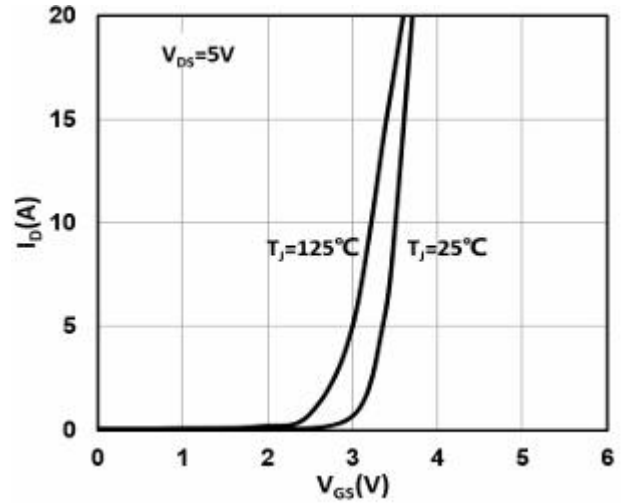


Figure 3 On-Resistance vs. ID and VGS

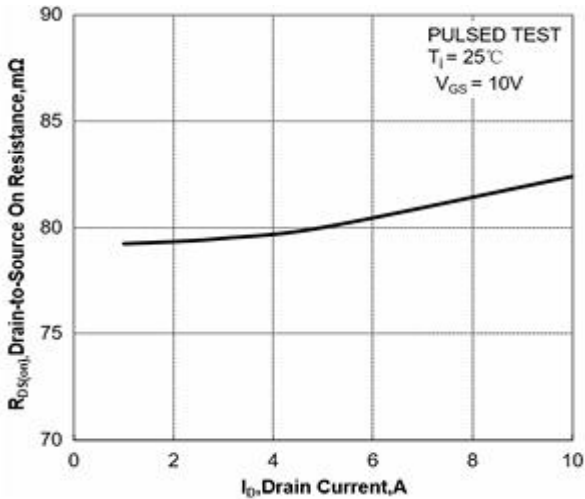


Figure 4 On-Resistance vs. Junction Temperature

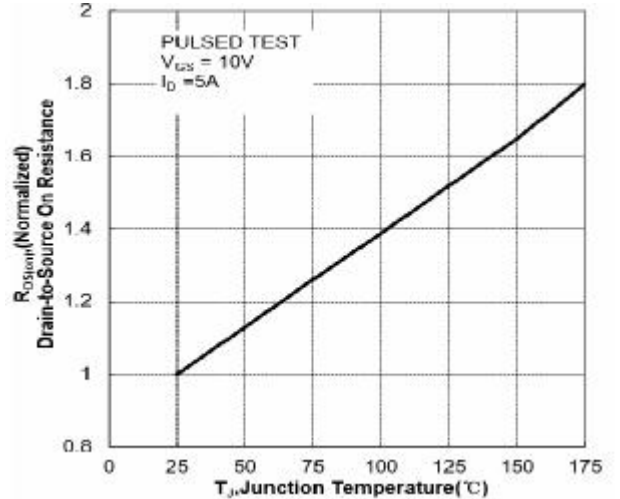


Figure 5 On-Resistance vs. VGS

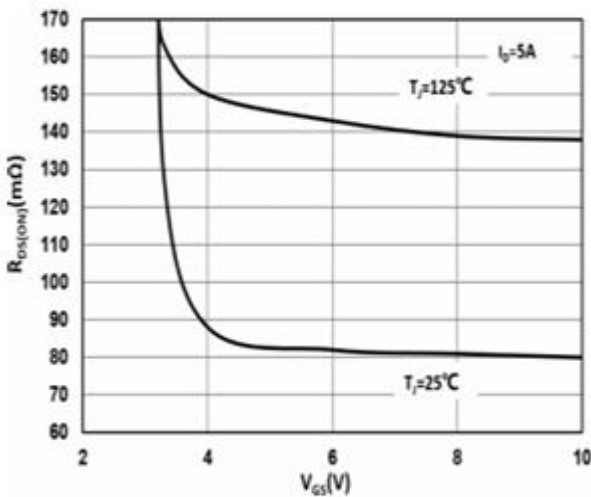


Figure 6 Body Diode Forward Voltage

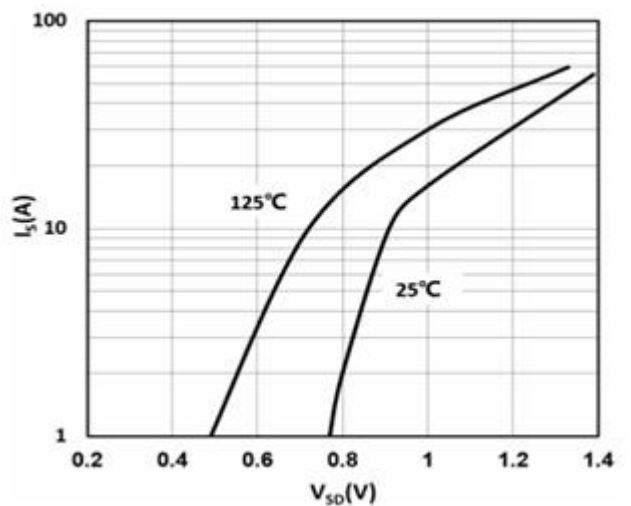


Figure 7 Gate-Charge Characteristics

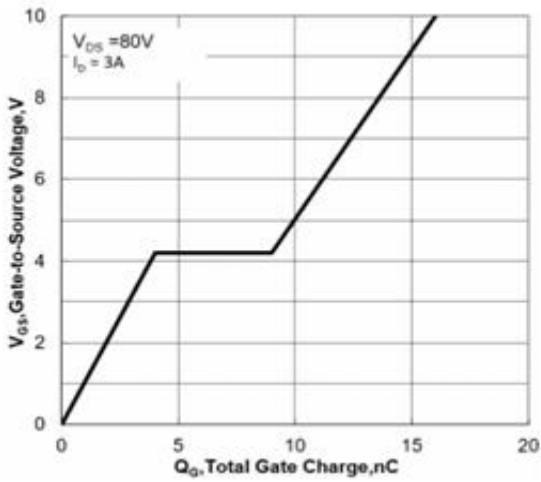


Figure 8 Capacitance Characteristics

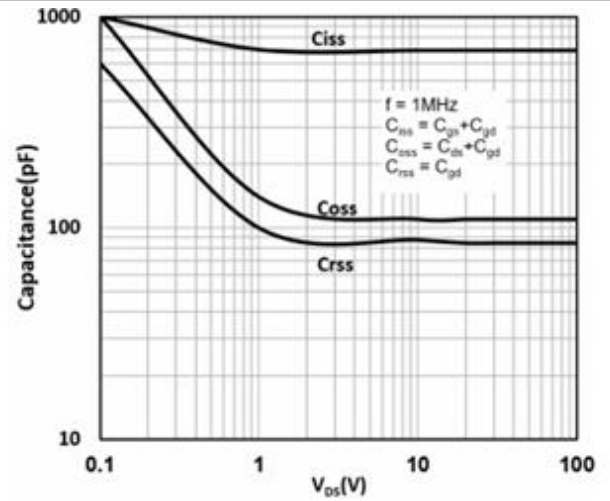


Figure 9 Maximum Forward Biased Safe Operation Area

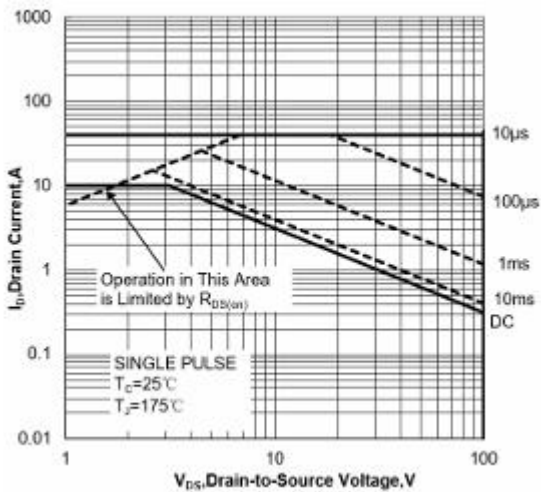


Figure 10 Single Pulse Power Rating Junction-to-Ambient

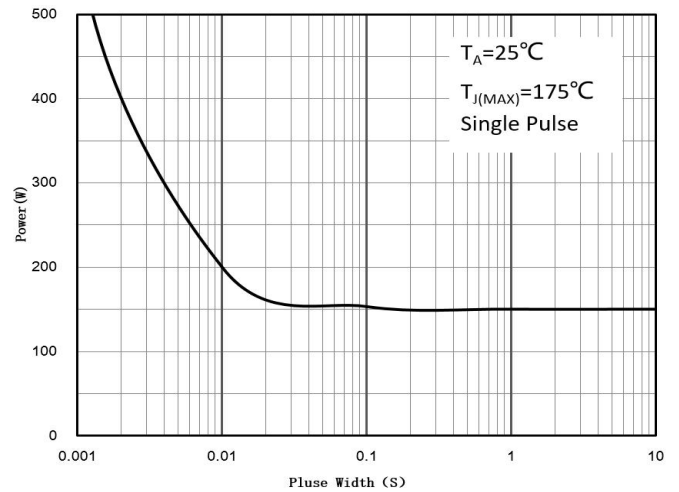
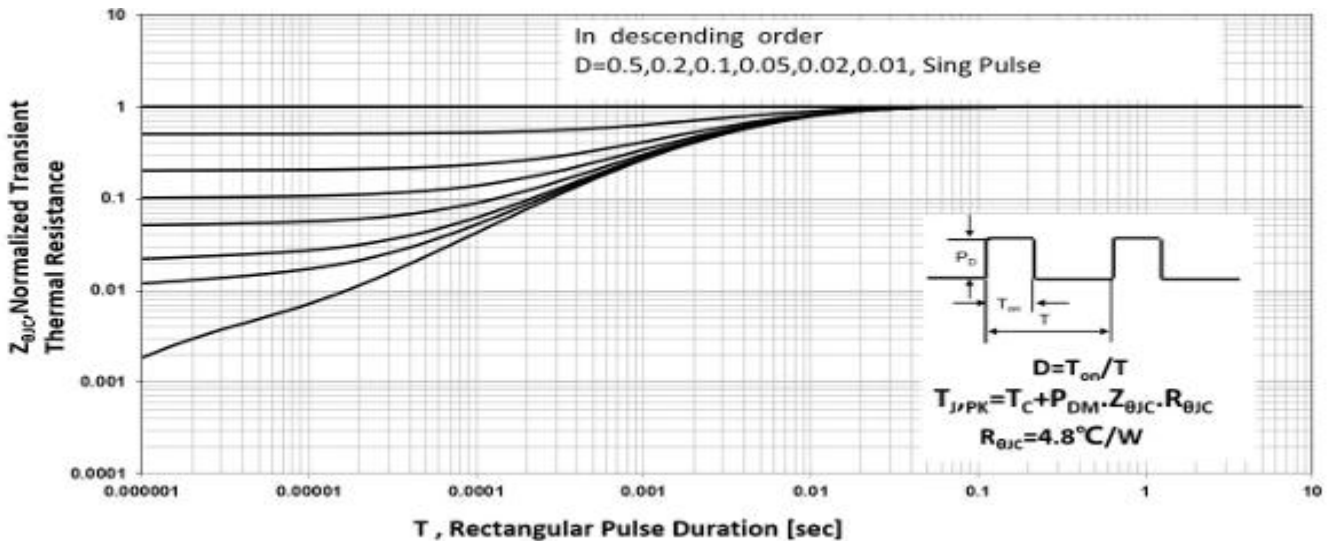
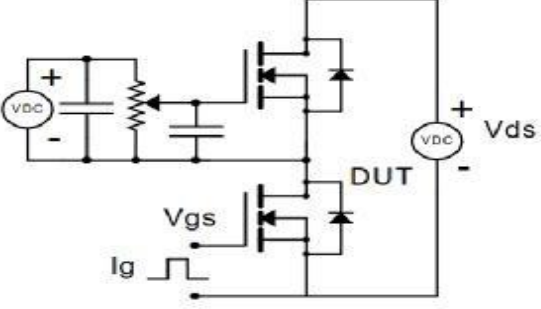
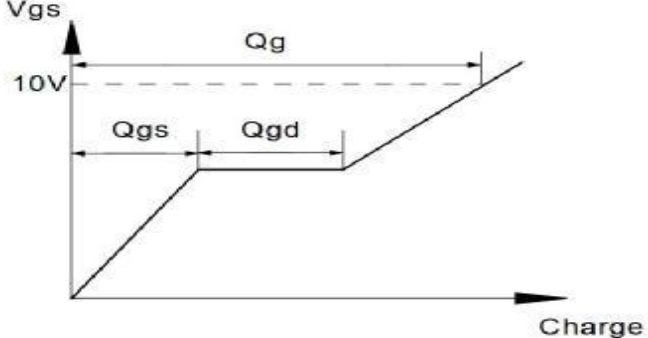
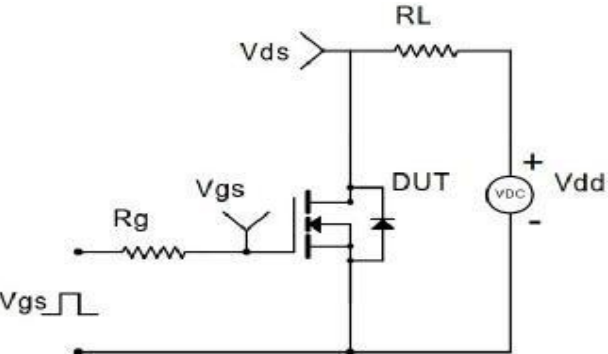
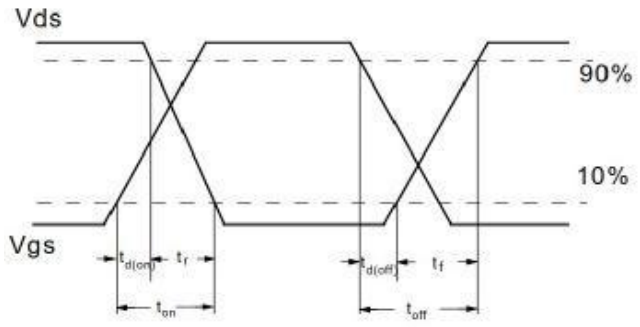
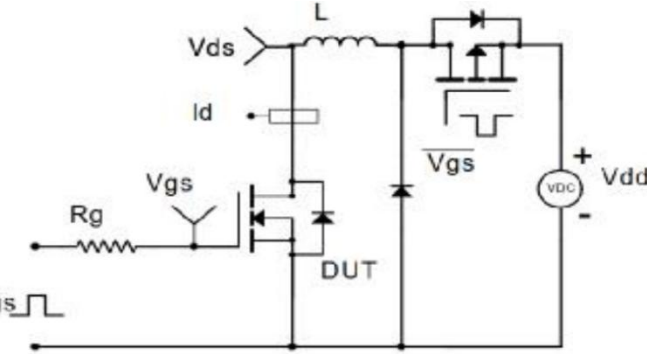
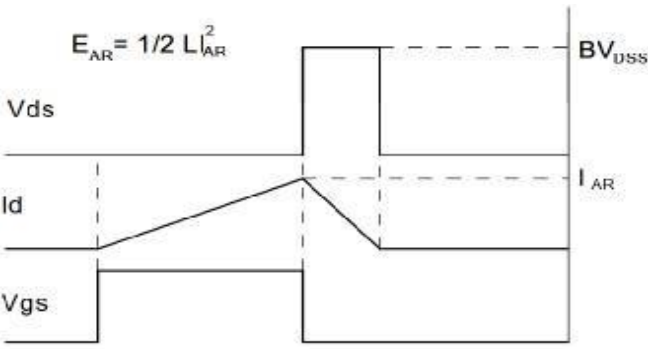
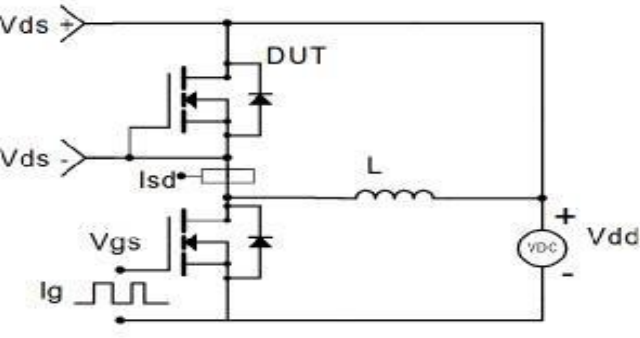
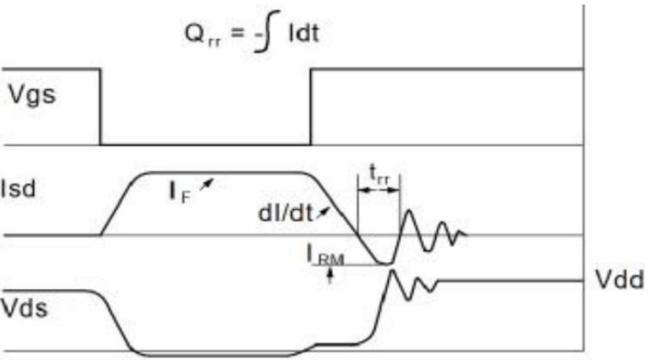


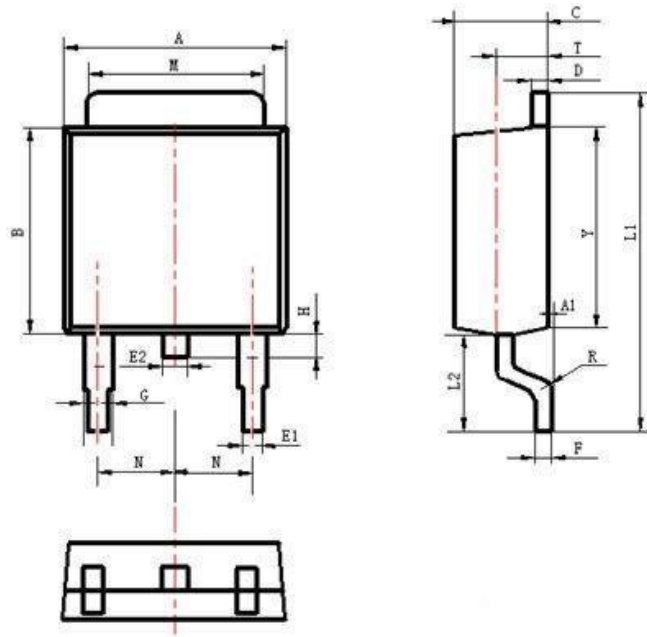
Figure 11 Normalized Maximum Transient Thermal Impedance



Test Circuit and Waveforms

<p style="text-align: center;">Gate Charge Test Circuit</p> 	<p style="text-align: center;">Gate Charge Test Waveform</p> 
<p style="text-align: center;">Resistive Switching Test Circuit</p> 	<p style="text-align: center;">Resistive Switching Test Waveforms</p> 
<p style="text-align: center;">Unclamped Inductive Switching (UIS) Test Circuit</p> 	<p style="text-align: center;">Unclamped Inductive Switching (UIS) Test Waveforms</p> 
<p style="text-align: center;">Diode Recovery Test Circuit</p> 	<p style="text-align: center;">Diode Recovery Test Waveforms</p> 

Package Description



Items	Values(mm)	
	MIN	MAX
A	6.30	6.90
A1	0	0.13
B	5.70	6.30
C	2.10	2.50
D	0.30	0.60
E1	0.60	0.90
E2	0.70	1.00
F	0.30	0.60
G	0.70	1.20
L1	9.60	10.50
L2	2.70	3.10
H	0.60	1.00
M	5.10	5.50
N	2.09	2.49
R	0.3	
T	1.40	1.60
Y	5.10	6.30

TO-252 Package

NOTE:

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shenzhen Minos reserves the right to make changes in this specification sheet and is subject to change without prior notice.

CONTACT:

深圳市迈诺斯科技有限公司（总部）

地址：深圳市福田区华富街道田面社区深南中路4026号田面城市大厦16D

邮编：518025

电话：0755-83273777