1. Description

The DMP2066LSN-7(ES) is P-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product DMP2066LSN-7(ES) is Pb-free.

2. Features

- -20V, $R_{DS(ON)}$=33mΩ(Typ.) @V_{GS}=-4.5V
  $R_{DS(ON)}$=45mΩ(Typ.) @V_{GS}=-2.5V
- Fast Switching
- High density cell design for low $R_{DS(on)}$
- Material: Halogen free
- Reliable and rugged
- Avalanche Rated
- Low leakage current

3. Applications

- PWM applications
- Load switch
- Power management in portable/desktop PCs
- DC/DC conversion

4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity per reel	Flammability Rating	Reel Size
DMP2066LSN-7(ES)	SOT-23	3415C	Halogen free	Tape & Reel	3,000 PCS	UL 94V-0	7 inches

Table-1 Ordering information

5. Pin Configuration and Functions

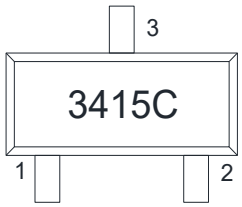
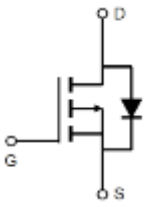
Pin	Function	Outline	Circuit Diagram
1	Gate		
2	Source		
3	Drain		

Table-2 Pin configuration

6. Specification

Absolute Maximum Rating & Thermal Characteristics

Ratings at 25 ˚C ambient temperature unless otherwise specified.

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		BV_{DSS}	-20	V
Gate-Source Voltage		V_{GS}	±12	V
Continuous Drain Current	T_A=25°C	I_D	-4.3	A
	T_A=100°C		-2.7	
Maximum Power Dissipation	T_A=25°C	P_D	1.39	W
Pulsed Drain Current		I_{DM}	-17.4	A
Operating Junction Temperature		T_J	150	°C
Storage Temperature Range		T_{stg}	-55 to +150	°C

Thermal resistance ratings

Single Operation				
Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance (t ≤10s)	$R_{\theta JA}$	65	90	°C/W
Junction-to-Lead Thermal Resistance	$R_{\theta JL}$	43	52	

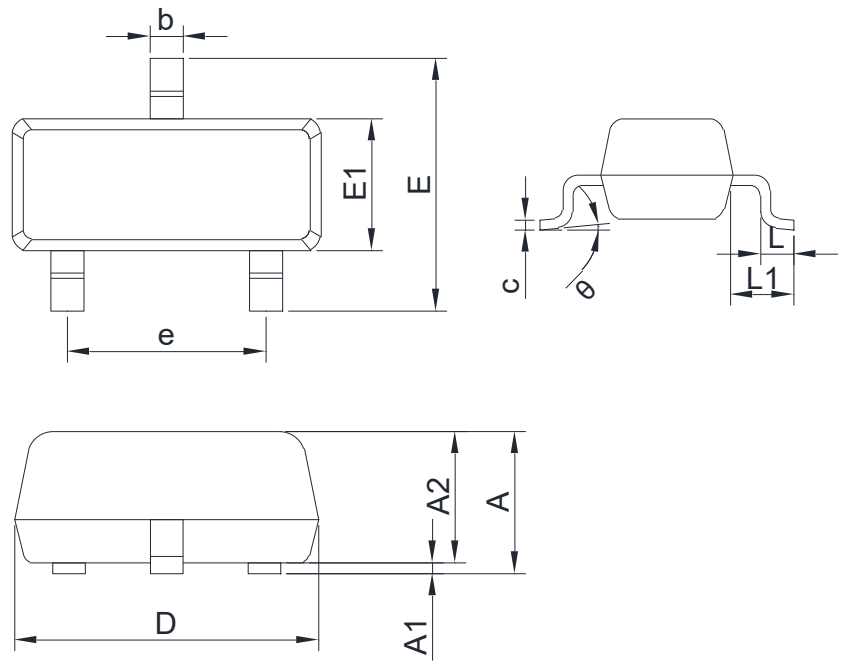
Electrical Characteristics

At TA = 25℃ unless otherwise specified

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=-250uA	-20			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{GS}=0V, V_{DS}=-20V			-100	nA
Gate-to-source Leakage Current	I_{GSS}	V_{GS}=±12V, V_{DS}=0V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	V_{GS}=V_{DS}, I_D=-250uA	-0.4	-0.6	-1	V
Drain-to-source On-resistance	$R_{DS(on)}$	V_{GS}=-4.5V, I_D=-3.8A		33	43	mΩ
		V_{GS}=-2.5V, I_D=-3A		45	59	
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	C_{ISS}	V_{GS}=0V, V_{DS} =-10V f=1MHz		475		pF
Output Capacitance	C_{OSS}			66		
Reverse Transfer Capacitance	C_{RSS}			55		
Total Gate Charge	$Q_{G(TOT)}$	V_{GS}=-4.5V, V_{DS}=-10V I_D=-4A		9.5		nC
Gate-to-Source Charge	Q_{GS}			1		
Gate-to-Drain Charge	Q_{GD}			2.3		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{d(ON)}$	V_{GS}=-4.5V, V_{DS}=-10V R_L=2.5Ω, R_G=3Ω		13		ns
Rise Time	t_r			10		
Turn-Off Delay Time	$t_{d(OFF)}$			20		
Fall Time	t_f			30		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V_{SD}	V_{GS}=0V, I_{SD}=-3.8A	-0.4		-1.5	V

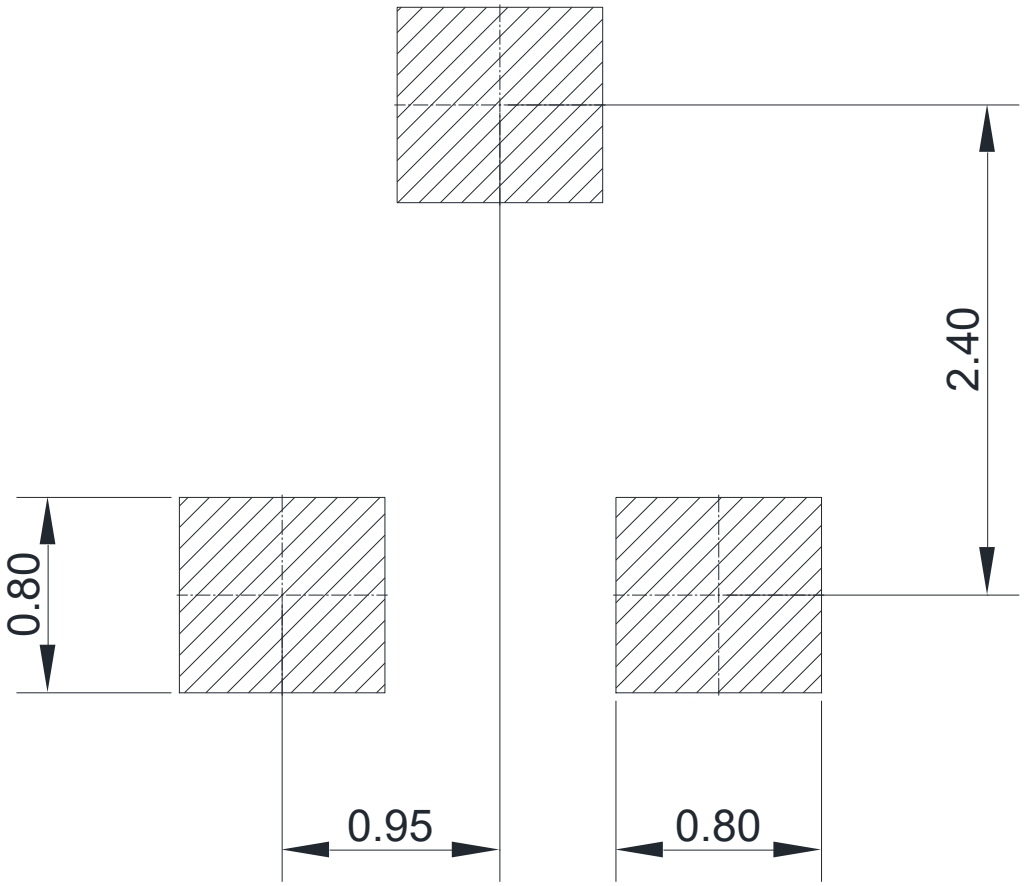
7. Dimension (SOT-23)

POD(Z)



COMMON DIMENSIONS CUNITS MEASURE=MILLIMETER					
SYMBOL	MIN	MAX	SYMBOL	MIN	MAX
A	0.90	1.20	E	2.25	2.55
A1	0.00	0.10	E1	1.20	1.40
A2	0.90	1.10	e	1.80	2.00
b	0.30	0.50	L	0.30	0.50
c	0.07	0.18	L1	0.475	0.625
D	2.80	3.04	θ	0°	8°

8. Recommended Soldering Footprint



DIMENSIONS: MILLIMETERS

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