

6.5MHz, Rail-to-Rail I/O CMOS Operational Amplifier



FEATURES

- HIGH GAIN BANDWIDTH:6.5MHz
- RAIL-TO-RAIL INPUT AND OUTPUT
0.8mV Typical Vos
- INPUT VOLTAGE RANGE: -0.1V to +5.6V
with Vs = 5.5V
- SUPPLY RANGE: +2.5V to +5.5V
- SPECIFIED UP TO +125°C

APPLICATIONS

- SENSORS
- PHOTODIODE AMPLIFICATION
- ACTIVE FILTERS
- TEST EQUIPMENT
- DRIVING A/D CONVERTERS

DESCRIPTION

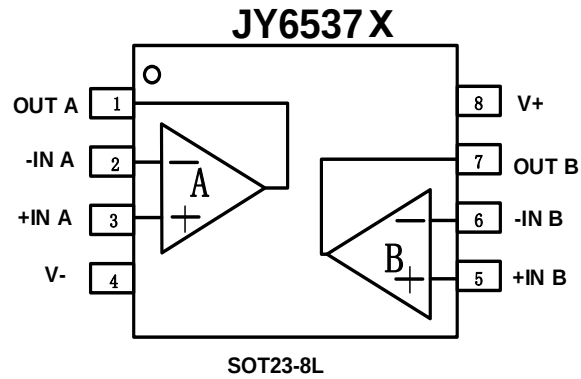
The JY6537X offer low voltage operation and rail-to-rail input and output, as well as excellent speed/power consumption ratio, providing an excellent bandwidth (6.5MHz) and slew rate of 3.7V/us. The op-amps are unity gain stable and feature an ultra-low input bias current.

The devices are ideal for sensor interfaces, active filters and portable applications. The JY6537 operational amplifier are specified at the full temperature range of -40° C to +125° C under single or dual power supplies of 2.5V to 5.5V.

Device Information (1)

PART NUMBER	PACKAGE	BODY SIZE(NOM)
JY6537X	SOT23-8L	2.90mm×1.60mm

Pin Configuration and Functions (Top View)



Pin Description

NAME	PIN	I/O	DESCRIPTION
	JY6537		
	SOT23-8L		
-INA	2	I	Inverting input, channel A
+INA	3	I	Noninverting input, channel A
-INB	6	I	Inverting input, channel B
+INB	5	I	Noninverting input, channel B
OUTA	1	O	Output, channel A
OUTB	7	O	Output, channel B
V-	4	-	Negative (lowest) power supply
V+	8	-	Positive (highest) power supply

SPECIFICATIONS

Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) ⁽¹⁾

		MIN	MAX	UNIT
Voltage	Supply, $V_S = (V+) - (V-)$		7	V
	Signal input pin ⁽²⁾	(V-)-0.5	(V+) +0.5	
	Signal output pin ⁽³⁾	(V-)-0.5	(V+) +0.5	
Current	Signal input pin ⁽²⁾	-10	10	mA
	Signal output pin ⁽³⁾	-55	55	mA
	Output short-circuit ⁽⁴⁾	Continuous		
Temperature	Operating range, T_A	-40	125	°C
	Junction, T_J		150	
	Storage, T_{stg}	-65	150	

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

(2) Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current-limited to 10mA or less.

(3) Output terminals are diode-clamped to the power-supply rails. Output signals that can swing more than 0.5V beyond the supply rails should be current-limited to ± 55 mA or less.

(4) Short-circuit to ground, one amplifier per package.

ESD Ratings

		VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human-body model (HBM)	± 5000
		Machine Model (MM)	± 400

Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
Supply voltage, $V_S = (V+) - (V-)$	Single-supply	2.5		5.5	V
	Dual-supply	± 1.25		± 2.75	

Thermal Information: JY6537

THERMAL METRIC ⁽¹⁾		JY6537	UNIT
		8PINS	
		SOT23-8L	
$R_{\theta JA}$	Junction-to-ambient thermal resistance	116	°C/W
$R_{\theta JC(top)}$	Junction-to-case(top) thermal resistance	60	°C/W
$R_{\theta JB}$	Junction-to-board thermal resistance	56	°C/W
Ψ_{JT}	Junction-to-top characterization parameter	12.8	°C/W
Ψ_{JB}	Junction-to-board characterization parameter	98.3	°C/W
$R_{\theta JC(bot)}$	Junction-to-case(bottom) thermal resistance	N/A	°C/W

PACKAGE/ORDERING INFORMATION

Orderable Device	Package Type	Pin	Channel	Op Temp(°C)	Device Marking ⁽¹⁾	Package Qty
JY6537XKX	SOT23-8L	8	2	-40°C~125°C	.6537	Tape and Reel,3000

NOTE:

- (1) There may be additional marking, which relates to the lot trace code information(data code and vendor code), the logo or the environmental category on the device.

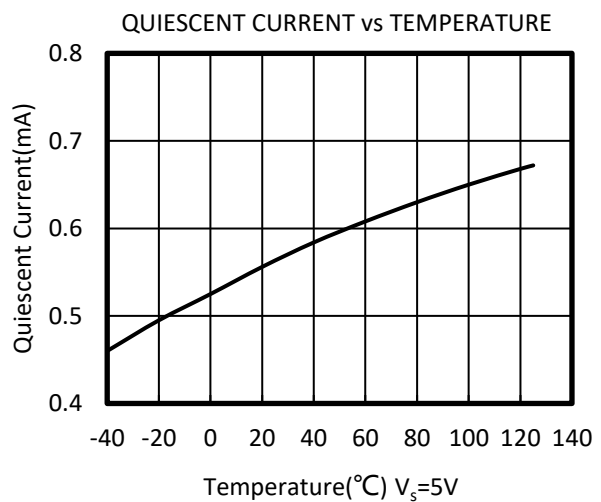
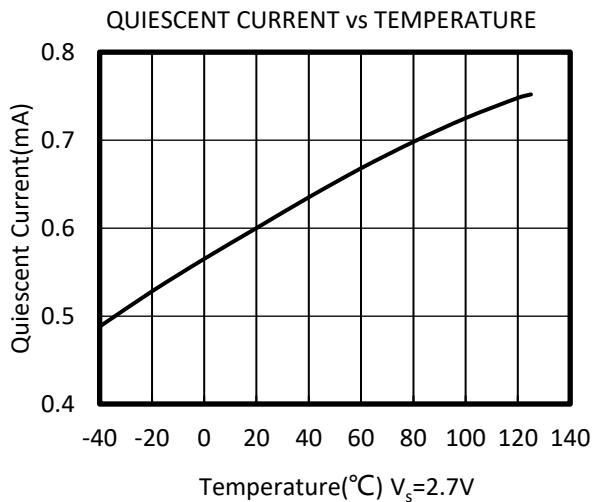
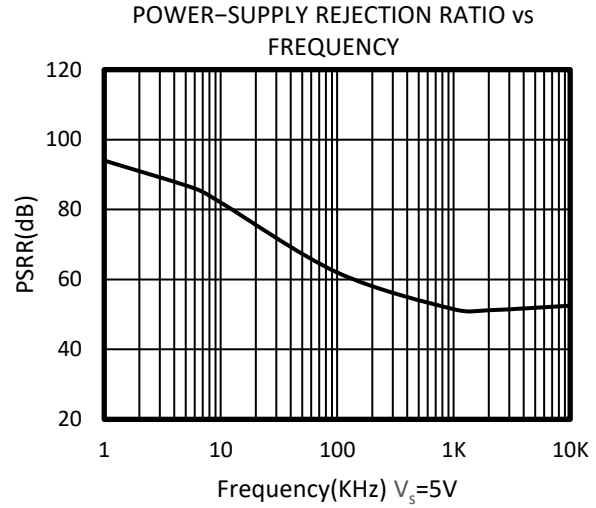
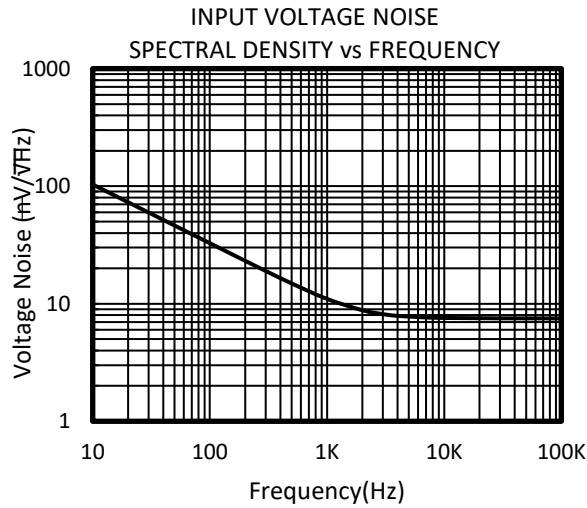
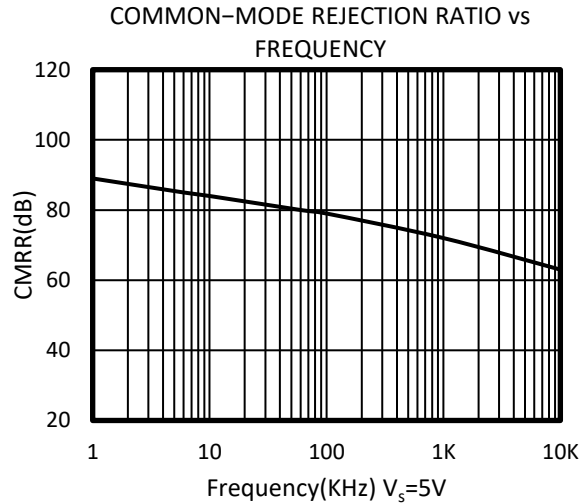
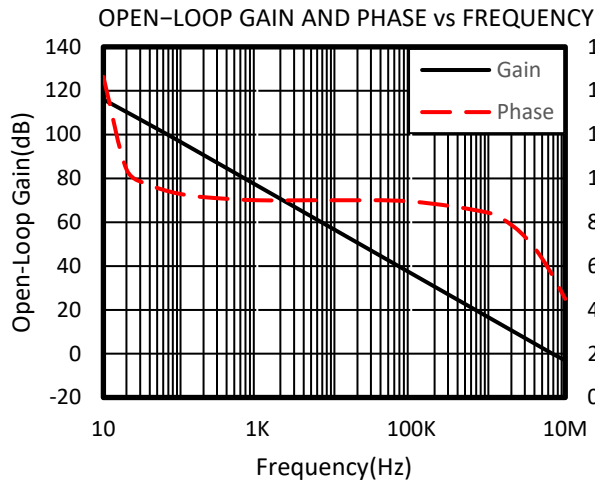
ELECTRICAL CHARACTERISTICS

(At $T_A = +25^\circ\text{C}$, $V_S = 5\text{V}$, $R_L = 10\text{k}\Omega$ connected to $V_S/2$, and $V_{OUT} = V_S/2$, unless otherwise noted.)

PARAMETER		CONDITIONS	T _J	JY6537			UNITS
				MIN	TYP	MAX	
POWER SUPPLY							
V _s	Operating Voltage Range		25°C	2.5		5.5	V
I _Q	Quiescent Current/Amplifier		25°C		590	820	uA
PSRR	Power-Supply Rejection Ratio	V _S =2.5V to 5.5V, V _{CM} =(V ₋)+0.5V	25°C	75	94		dB
			-40°C to 125°C	72			
INPUT							
V _{OS}	Input Offset Voltage	V _{CM} = V _S /2	25°C	-3	±0.8	3	mV
V _{OS} T _C	Input Offset Voltage Average Drift	-40°C to125°C			2		uV/°C
I _B	Input Bias Current		25°C		1	10	pA
I _{OS}	Input Offset Current		25°C		1	10	pA
V _{CM}	Common-Mode Voltage Range	V _S = 5.5V	25°C	-0.1		5.6	V
CMRR	Common-Mode Rejection Ratio	V _S = 5.5V, V _{CM} =-0.1V to 4V	25°C	74	92		dB
			-40°C to 125°C	68			
		V _S = 5.5V, V _{CM} =-0.1V to 5.6V	25°C	62	83		
			-40°C to 125°C	60			
OUTPUT							
AOL	Open-Loop Voltage Gain	R _L =2KΩ, V _O = 0.15V to 4.85V	25°C	95	100		dB
			-40°C to 125°C	83			
		R _L =10KΩ, V _O = 0.05V to 4.95V	25°C	98	106		
			-40°C to 125°C	85			
	Output Swing From Rail	R _L =2KΩ	25°C		40		mV
		R _L =10KΩ			7		
I _{out}	Output Current Source		25°C		50		mA
FREQUENCY RESPONSE							
SR	Slew Rate		25°C		3.7		V/us
GBP	Gain-Bandwidth Product		25°C		6.5		MHz
PM	Phase Margin		25°C		63		°
ts	Setting Time,0.1%		25°C		0.5		us
	Overload Recovery Time	V _{IN} ·Gain≥V _S	25°C		0.5		us
NOISE							
en	Input Voltage Noise Density	f = 1KHz	25°C		12		nV/ √ Hz
		f = 10KHz	25°C		8		nV/ √ Hz

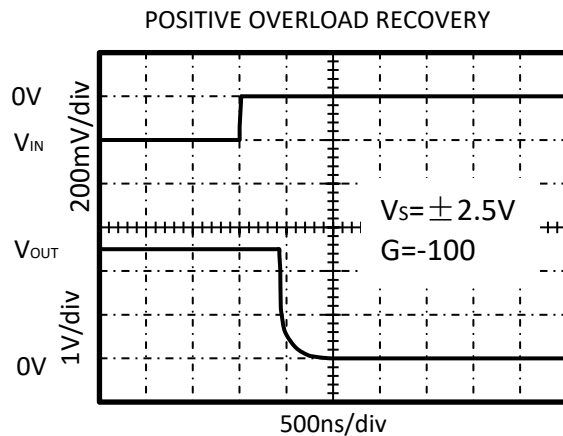
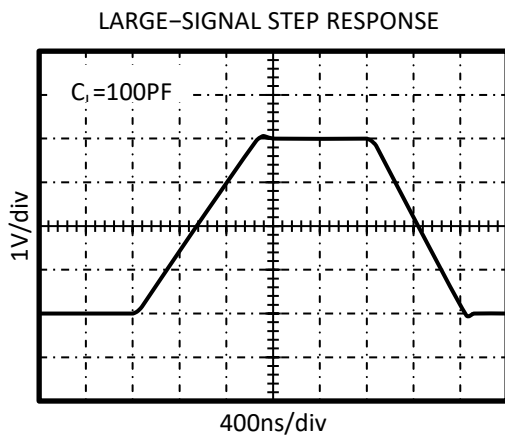
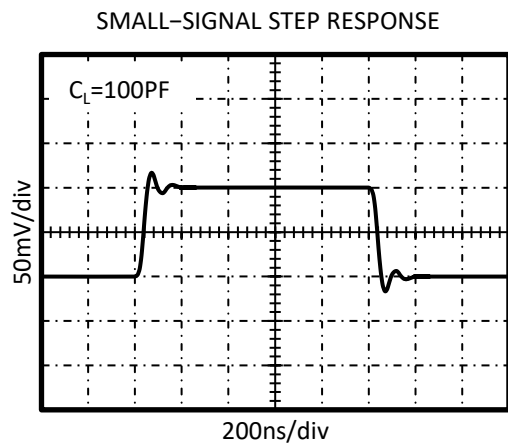
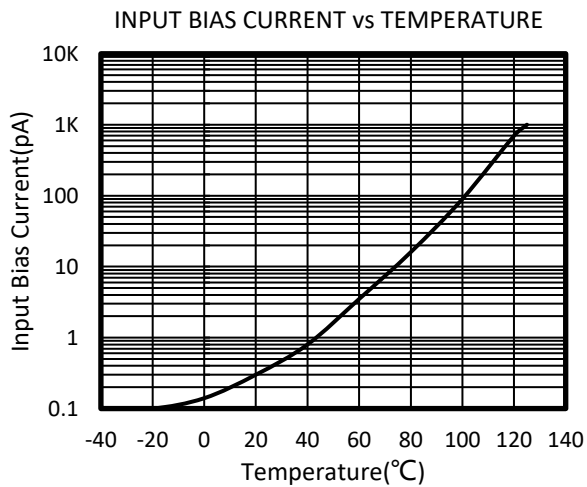
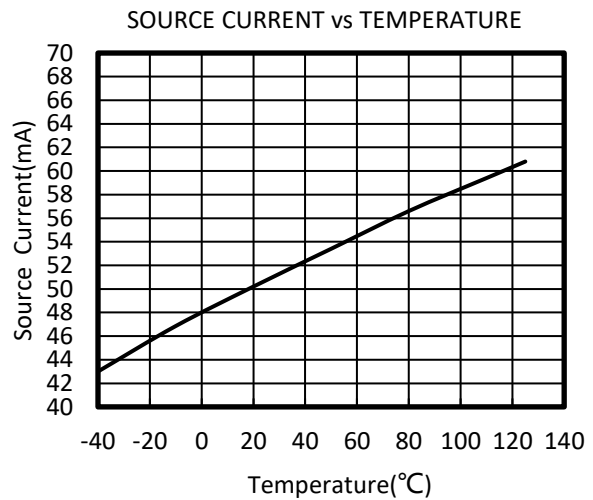
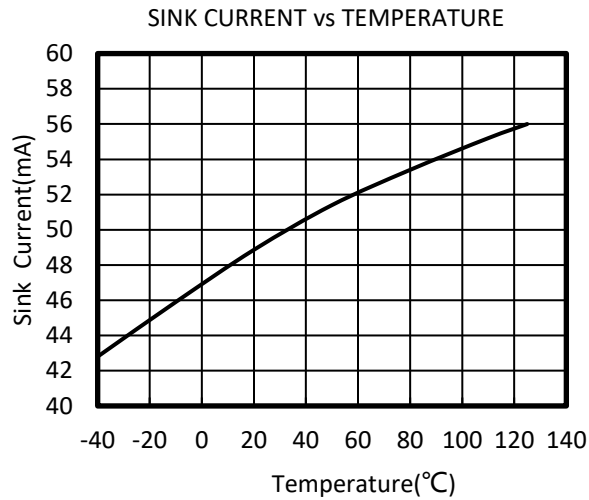
TYPICAL CHARACTERISTICS

At $T_A = +25^\circ\text{C}$, $V_S = 5\text{V}$, $R_L = 10\text{k}\Omega$ connected to $V_S/2$, $V_{OUT} = V_S/2$, unless otherwise noted.



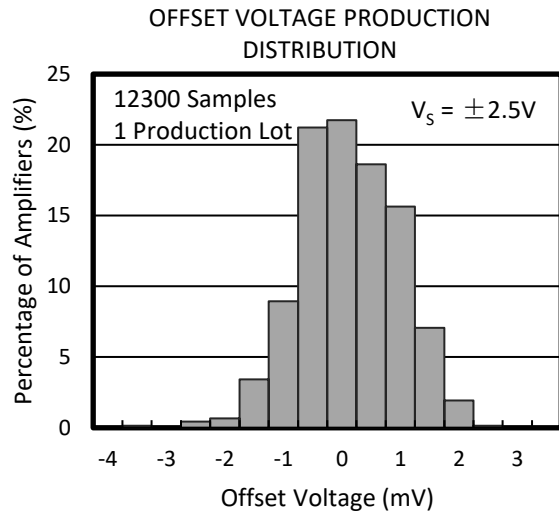
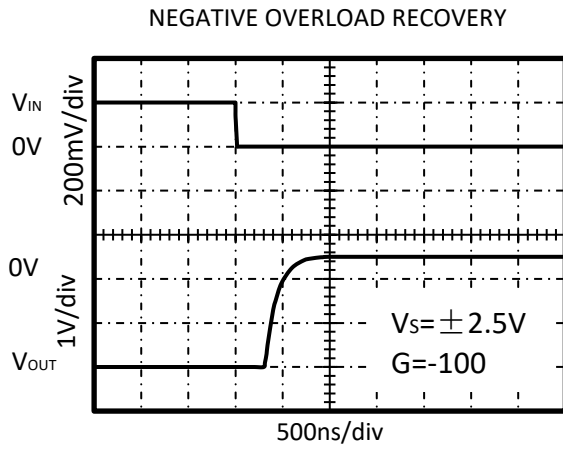
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APPLICATION NOTES

The JY6537X is high precision, rail-to-rail operational amplifiers that can be run from a single -supply voltage 2.5V to 5.5V ($\pm 1.25\text{V}$ to $\pm 2.75\text{V}$). Supply voltages higher than 7V (absolute maximum) can permanently damage the amplifier.

Rail -to-rail input and output swing significantly increases dynamic range , especially in low-supply applications.

Good layout practice mandates use of a 0.1uF capacitor place closely across the supply pins.

LAYOUT GUIDELINS

Attention to good layout practices is always recommended . Keep traces short. When possible , use a PCB ground plane with surface -mount components placed as close to the device pins as possible . Place a 0.1uF capacitor closely across the supply pins. These guidelines should be applied throughout the analog circuit to improve performance and provide benefits such as reducing the EMI susceptibility.

INSTRUMENTATION AMPLIFIER

In the three-op amp, instrumentation amplifier configuration shown in Figure2,

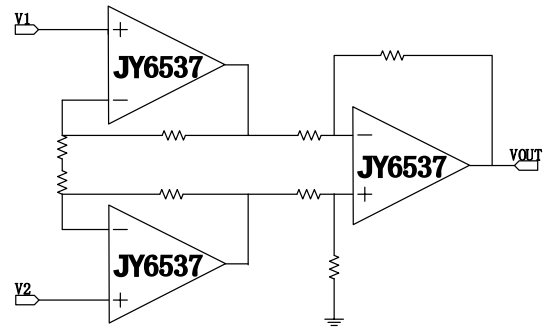


Figure2. Amplifier instrumentation amplifier

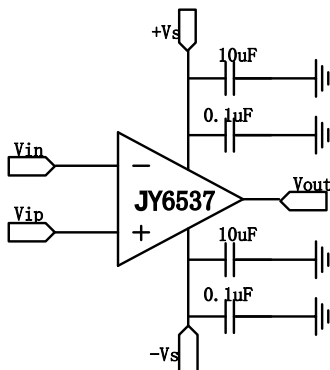
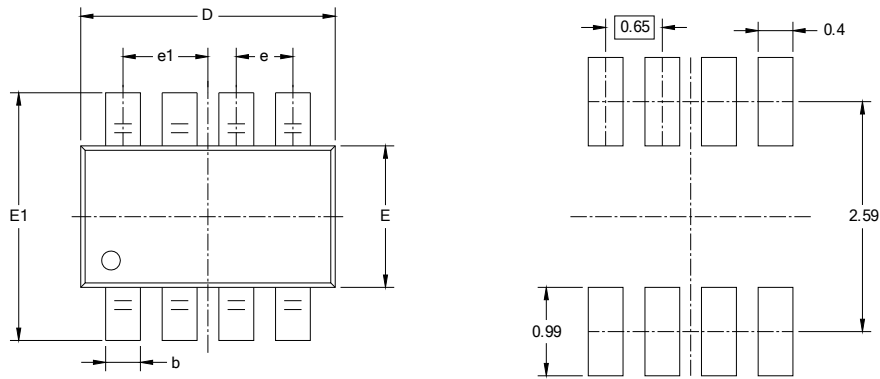
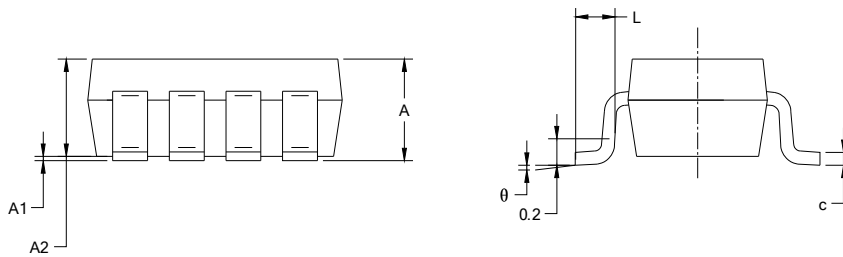


Figure1. Amplifier with Bypass Capacitors

PACKAGE OUTLINE DIMENSIONS **SOT23-8L**



RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.650(BSC)		0.026(BSC)	
e1	0.975(BSC)		0.038(BSC)	
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°