

BST10NH032SK1

N-Channel Power MOSFET

100 V, 210 A, 3.2 mΩ

Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency. It has been optimized for low gate charge, low $R_{DS(on)}$, fast switching speed.

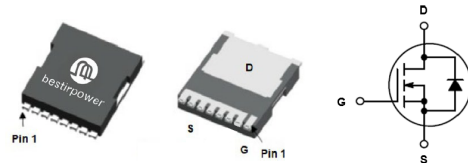
Features

BV_{DSS}	I_D	$R_{DS(on),max}$	$Q_{rr,typ}$
100 V	210 A	3.2 mΩ	160 nC

- $R_{DS(on),MAX}=3.2m\Omega@V_{GS}=10V$
- Low gate Charge (typical 121nC)
- Low C_{rss} (typical 44pF)
- Low On-Resistance
- Excellent FoM(figure of merit)
- 100% UIS and Rg tested

Applications

- Motor Driver
- DC/DC Converter
- Battery Management System



Absolute Maximum Ratings ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage		100	V
V_{GSS}	Gate to Source Voltage		± 20	V
I_D	Continuous Drain Current, ($T_C = 25^\circ\text{C}$) ¹⁾		210	A
	Continuous Drain Current, ($T_C = 100^\circ\text{C}$) ¹⁾		168	
I_{DM}	Drain Current (2)	Pulsed	840	A
E_{AS}	Single Pulsed Avalanche Energy (3)		1067	mJ
P_D	Power Dissipation	($T_C = 25^\circ\text{C}$)	260	W
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.48	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	45	

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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Off Characteristics

BV_{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 100\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$	-	-	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS} = \pm 20\text{ V}$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\text{ }\mu\text{A}$	2.0	-	4.0	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10\text{ V}, I_D = 70\text{ A}$	-	2.6	3.2	mΩ

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 50\text{V},$ $f = 1.0\text{MHz}$	-	7483	-	pF
C_{oss}	Output Capacitance		-	1037	-	pF
C_{rss}	Reverse transfer capacitance		-	44	-	pF
$Q_{g(tot)}$	Total Gate Charge at 10 V	$V_{GS} = 10\text{V}, V_{DD} = 50\text{V}$ $I_D = 70\text{A}$	-	121	-	nC
Q_{gs}	Gate to Source Charge		-	36.9	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	31.3	-	nC
R_G	Gate Resistance	$f = 1\text{ MHz}$	-	0.7	-	Ω

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}, V_{DD} = 50\text{V}$ $R_G = 3\Omega, I_D = 70\text{A}$	-	46	-	ns
t_r	Turn-On Rise Time		-	43	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	106	-	ns
t_f	Turn-Off Fall Time		-	76	-	ns

Source-Drain Diode Characteristics

I_S	Continuous Diode Forward Current	$T_J = 25^\circ\text{C}, I_S = 70\text{A}, V_{GS} = 0\text{V}$	-	210	-	A
$I_{S\text{ pulse}}$	Diode Pulsed Current ²⁾		-	840	-	
V_{SD}	Diode Forward Voltage	$T_J = 25^\circ\text{C}, I_S = 70\text{A}, V_{GS} = 0\text{V}$	-	0.9	1.4	V
t_{rr}	Reverse Recovery Time	$I_F = 70\text{A}, diF/dt = 100\text{A/us}$	-	61	-	ns
Q_{rr}	Reverse Recovery Charge		-	160	-	nC

Notes

- 1) Limited by maximum junction temperature.
- 2) Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3) $L = 0.5\text{mH}$, $R_G = 25\Omega$, $V_{GS} = 10\text{V}$, $V_{DD} = 50\text{V}$, Start $T_J = 25^\circ\text{C}$.

Typical Performance Characteristics

Figure 1. Transient Thermal Impedance

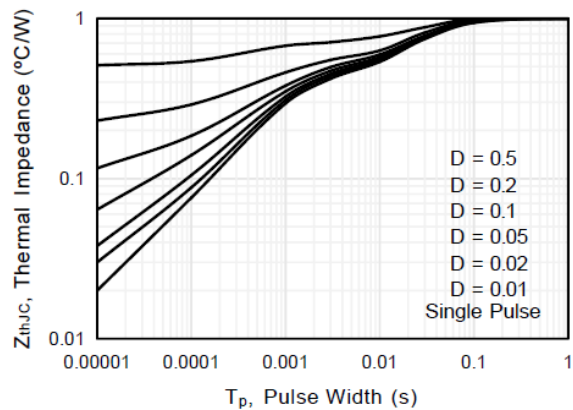


Figure 2 . Safe Operation Area

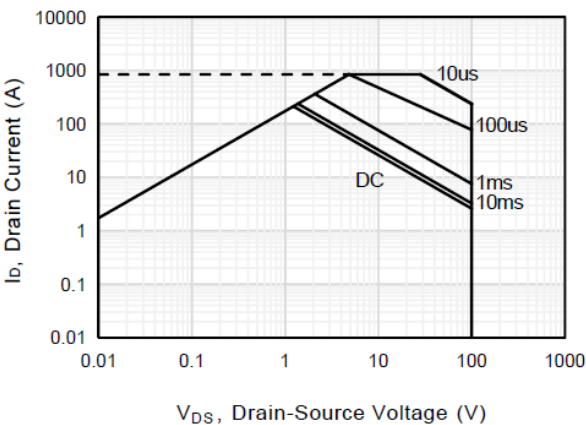


Figure 3 . Output Characteristics

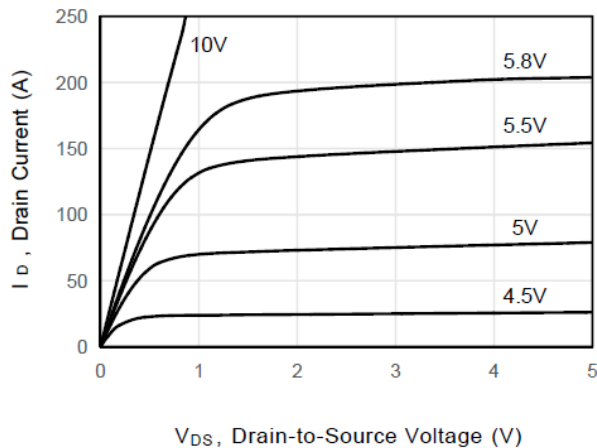


Figure 4 . Transfer Characteristics

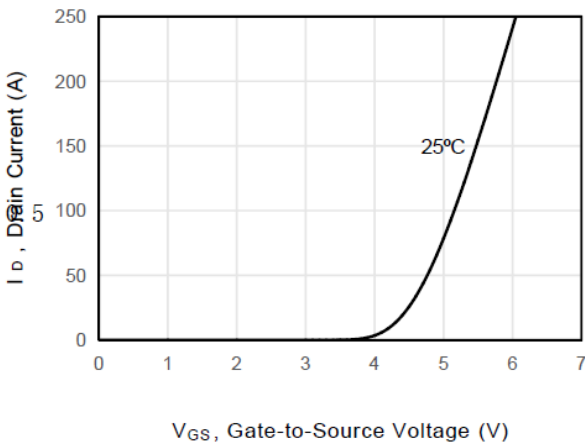


Figure 5 . On-Resistance vs Drain Current

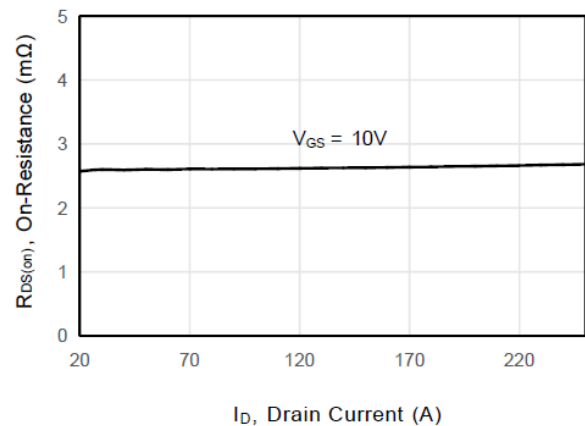
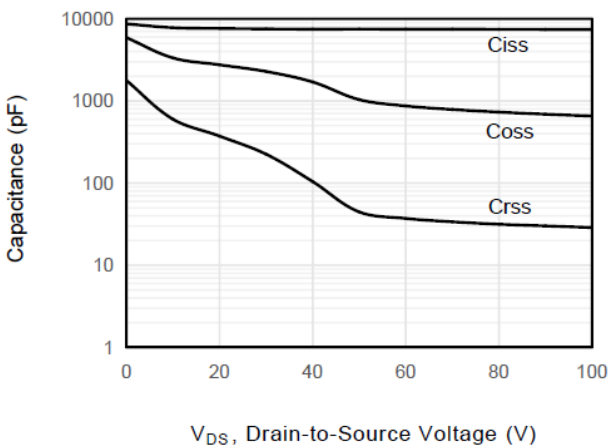


Figure 6 . Capacitance



Typical Performance Characteristics

Figure 7 . Gate Charge

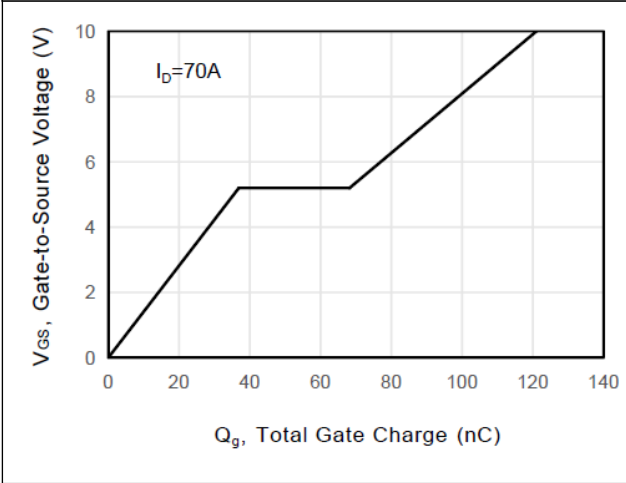


Figure 8 . Body Diode Forward Voltage

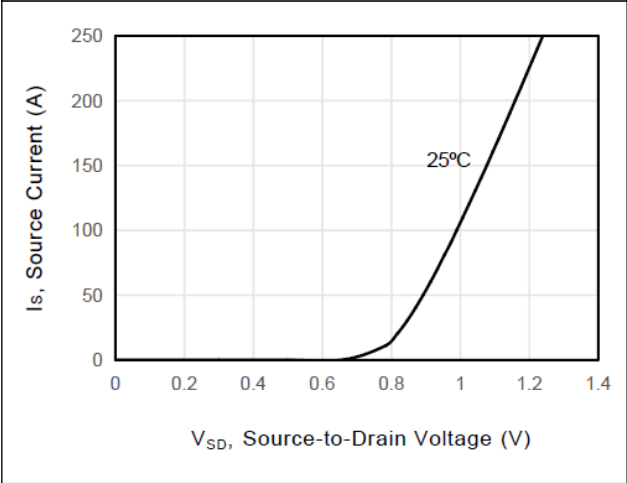


Figure 9. Breakdown Voltage vs Junction Temperature

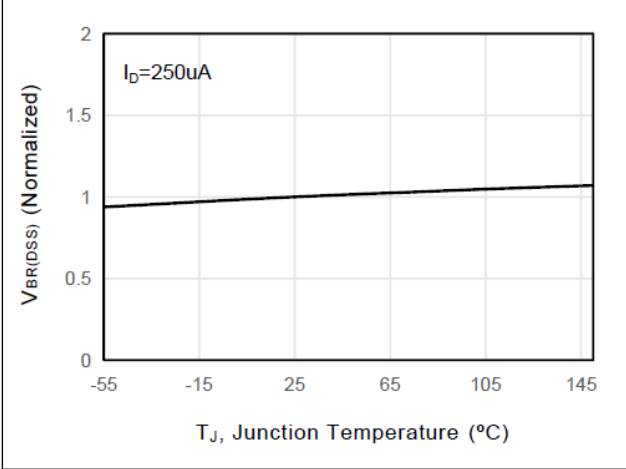


Figure 10. On-Resistance vs Temperature

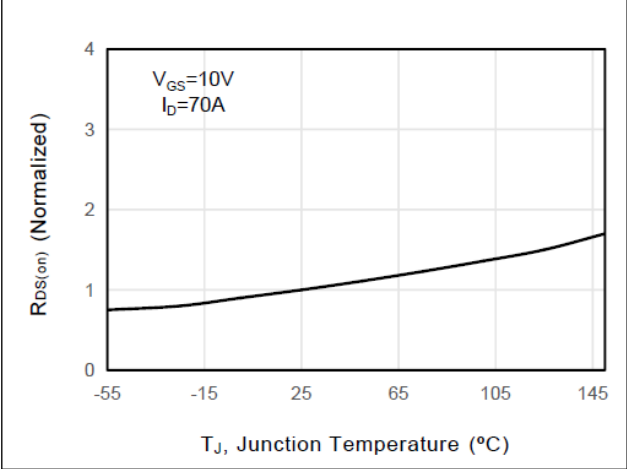
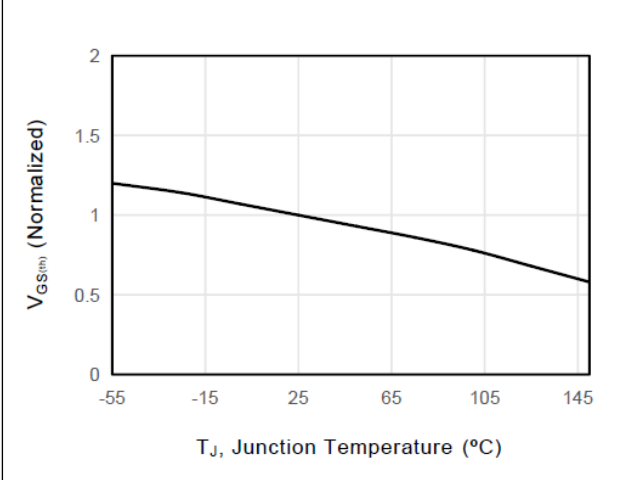


Figure 11. $V_{GS(th)}$ vs Junction Temperature



Test Circuits

Figure 12: Gate Charge Test Circuit and Waveform

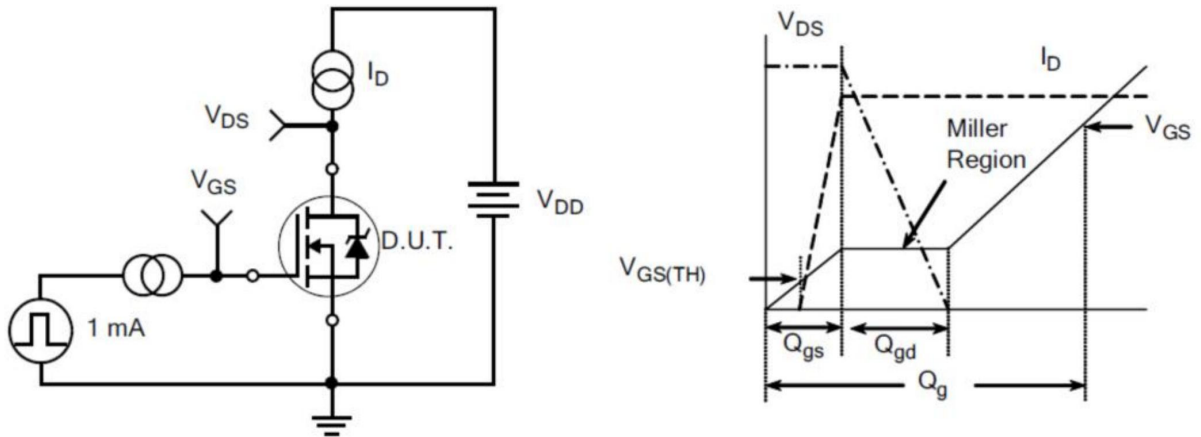


Figure 13: Resistive Switching Test Circuit and Waveform

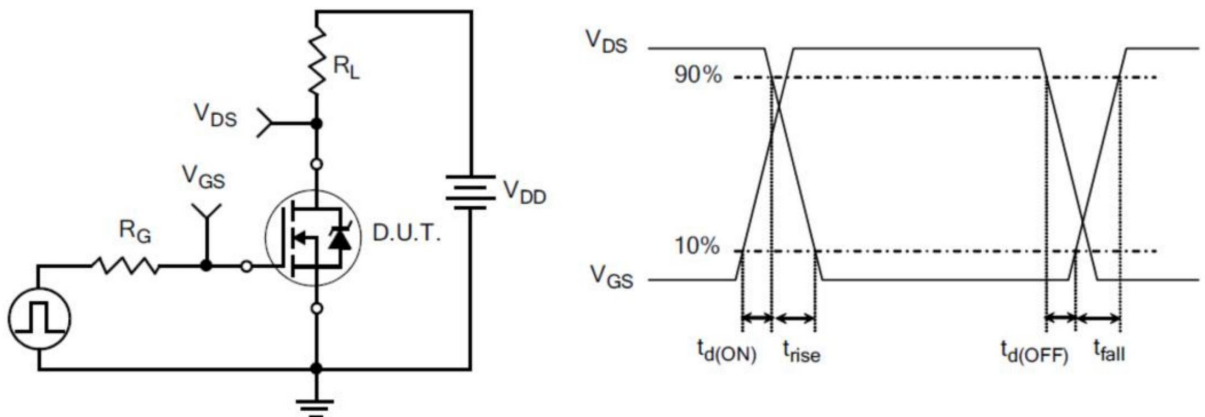
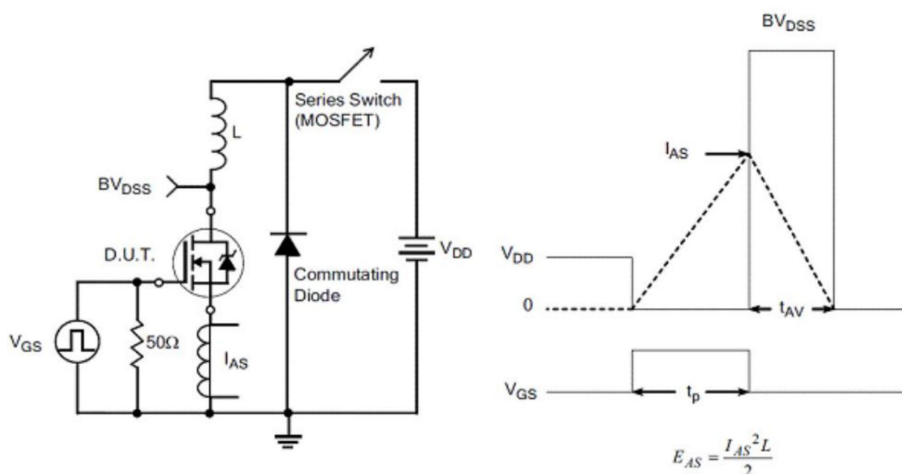
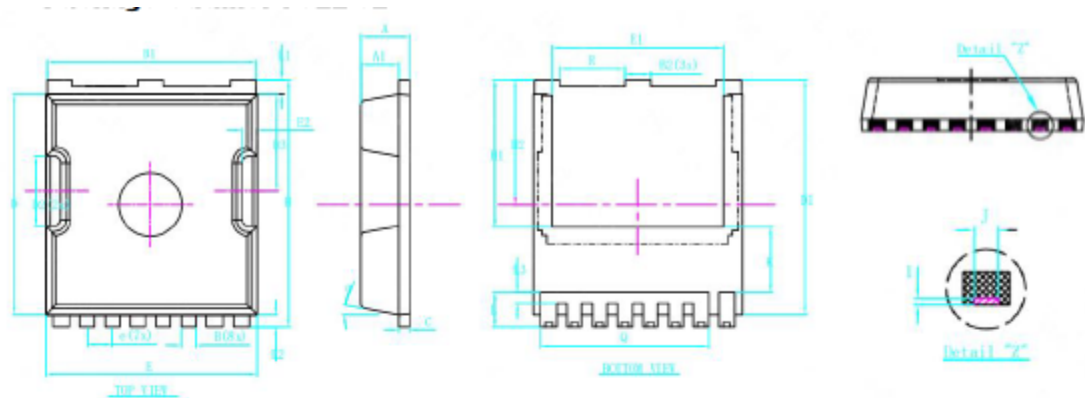


Figure 14: Unclamped Inductive Switching Test Circuit and Waveform



Package Outlines

TOLL



SYMBOL	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.200	2.300	2.400
A1	1.700	1.800	1.900
B	0.700	0.800	0.900
B1	9.700	9.800	9.900
B2	1.100	1.200	1.300
C	0.400	0.500	0.600
D	10.300	10.400	10.500
D1	11.000	11.100	11.200
D2	3.200	3.300	3.400
D3	4.470	4.570	4.670
E	9.800	9.900	10.000
E1	8.000	8.100	8.200
E2	0.500	0.600	0.700
e	1.200 BSC		
H	11.600	11.700	11.800
H1	6.950 BSC		
H2	5.900 BSC		
I	0.050	0.100	0.15
J	0.350 REF.		
K	3.100 REF.		
L	1.550	1.650	1.750
L1	0.600	0.700	0.800
L2	0.500	0.600	0.700
L3	0.400	0.500	0.600
Q	8.000 REF.		
R	3.000	3.100	3.200
a	10° REF.		

* Dimensions in millimeters

Package Marking and Ordering Information

Part Number	Top Marking	Package	Packing Method	Quantity
BST10NH032SK1	BST10NH032SK1	TOLL	Tape & Reel	1200 units

Disclaimer

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