

BSB10NH027SK1

N-Channel Power MOSFET

100 V, 245 A, 2.7 mΩ

Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency. It has been optimized for low gate charge, low $R_{DS(on)}$, fast switching speed.

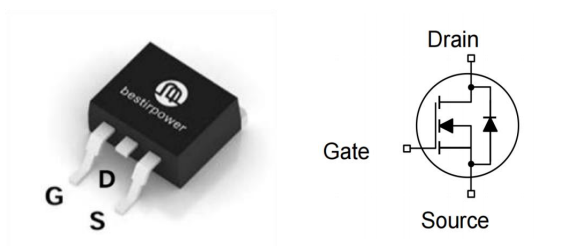
Features

BV_{DSS}	I_D	$R_{DS(on),max}$	$Q_{rr,typ}$
100 V	245 A	2.7 mΩ	200 nC

- $R_{DS(on),MAX}=2.7m\Omega@V_{gs}=10V$
- Low gate Charge (typical 141nC)
- Low C_{rss} (typical 52pF)
- Low On-Resistance
- Excellent FoM(figure of merit)
- 100% UIS and Rg tested

Applications

- DC/DC Converter
- Motor Driver
- Uninterruptible Power Supplies



Absolute Maximum Ratings ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage		100	V
V_{GSS}	Gate to Source Voltage		± 20	V
I_D	Continuous Drain Current, ($T_C = 25^\circ\text{C}$) ¹⁾		245	A
	Continuous Drain Current, ($T_C = 100^\circ\text{C}$) ¹⁾		172	
I_{DM}	Drain Current (2)	Pulsed	980	A
E_{AS}	Single Pulsed Avalanche Energy (3)		1193	mJ
P_D	Power Dissipation	($T_C = 25^\circ\text{C}$)	298	W
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter		Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case		0.42	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		40	

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
--------	-----------	-----------------	-----	-----	-----	------

Off Characteristics

BV_{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}, T_J = 25^\circ\text{C}$	-	-	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS} = \pm 20\text{ V}$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\text{ }\mu\text{A}$	2.0	-	4.0	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10\text{ V}, I_D = 40\text{ A}$	-	2.4	2.7	mΩ

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{GS} = 0\text{ V}, V_{DS} = 50\text{ V},$ $f = 1.0\text{ MHz}$	-	8749	-	pF
C_{oss}	Output Capacitance		-	1233	-	pF
C_{rss}	Reverse transfer capacitance		-	52	-	pF
$Q_{g(tot)}$	Total Gate Charge at 10 V	$V_{GS} = 10\text{ V}, V_{DD} = 50\text{ V}$ $I_D = 40\text{ A}$	-	141	-	nC
Q_{gs}	Gate to Source Charge		-	42.6	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	37	-	nC
R_G	Gate Resistance	$f = 1\text{ MHz}$	-	0.9	-	Ω

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{ V}, V_{DD} = 50\text{ V}$ $R_G = 3\text{ }\Omega, I_D = 40\text{ A}$	-	56	-	ns
t_r	Turn-On Rise Time		-	48	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	122	-	ns
t_f	Turn-Off Fall Time		-	128	-	ns

Source-Drain Diode Characteristics

I_S	Continuous Diode Forward Current	$T_J = 25^\circ\text{C}, I_S = 40\text{ A}, V_{GS} = 0\text{ V}$	-	245	-	A
$I_{S\text{ pulse}}$	Diode Pulsed Current ²⁾		-	980	-	
V_{SD}	Diode Forward Voltage	$T_J = 25^\circ\text{C}, I_S = 40\text{ A}, V_{GS} = 0\text{ V}$	-	0.84	1.4	V
t_{rr}	Reverse Recovery Time	$I_F = 40\text{ A}, diF/dt = 100\text{ A/us}$	-	71	-	ns
Q_{rr}	Reverse Recovery Charge		-	200	-	nC

Notes

- 1) Limited by maximum junction temperature.
- 2) Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3) $L = 0.5\text{ mH}$, $R_G = 25\text{ }\Omega$, $V_{GS} = 10\text{ V}$, $V_{DD} = 50\text{ V}$, Start $T_J = 25^\circ\text{C}$.

Typical Performance Characteristics

Figure 1. Transient Thermal Impedance

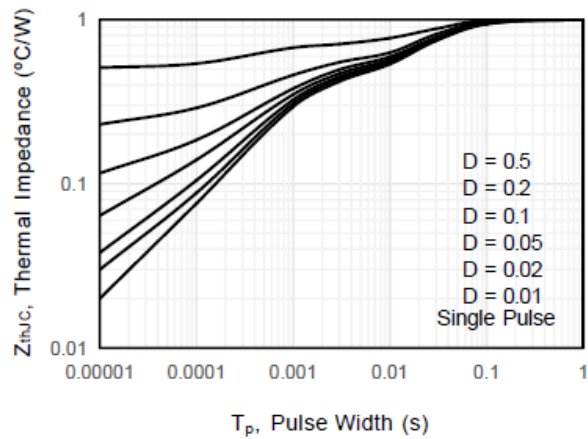


Figure 2 . Safe Operation Area

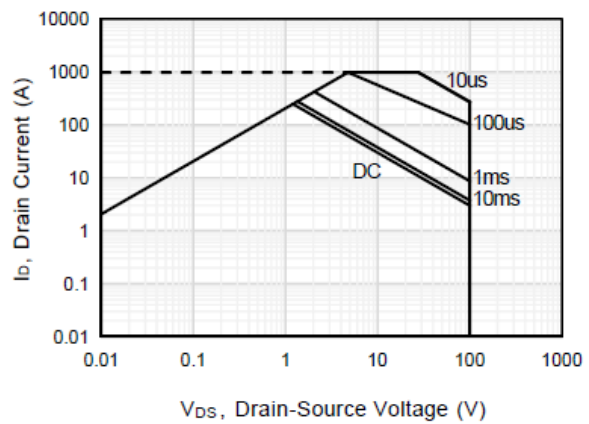


Figure 3 . Output Characteristics

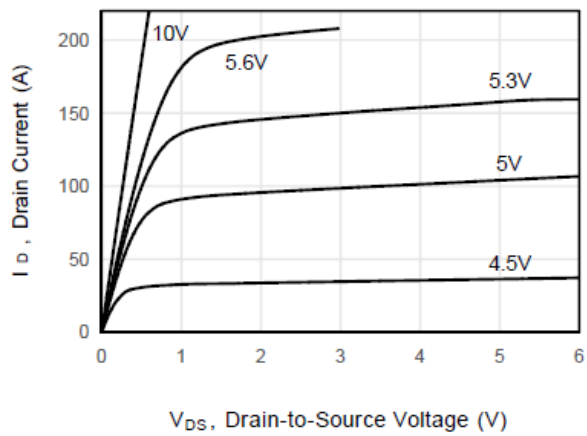


Figure 4 . Transfer Characteristics

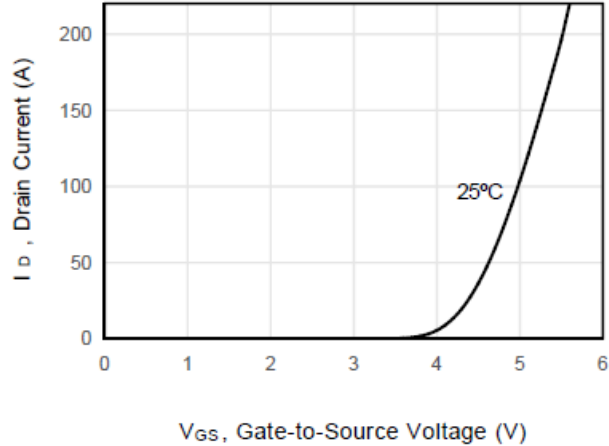


Figure 5 . On-Resistance vs Drain Current

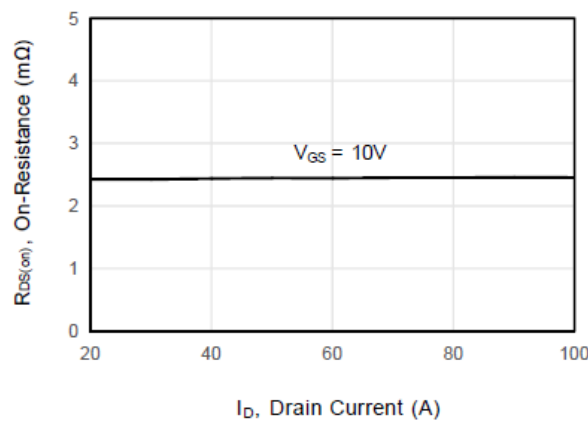
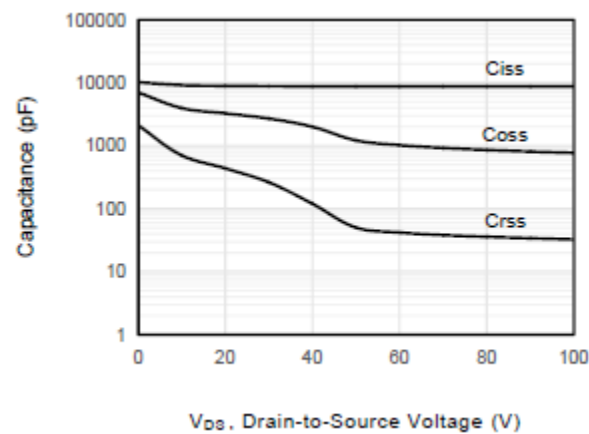


Figure 6 . Capacitance



Typical Performance Characteristics

Figure 7 . Gate Charge

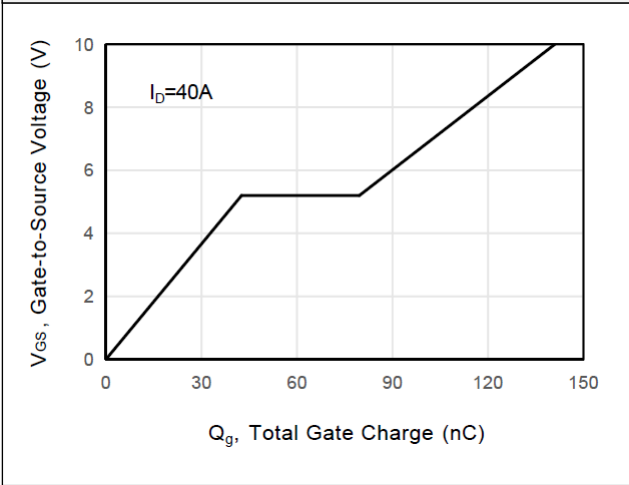


Figure 8 . Body Diode Forward Voltage

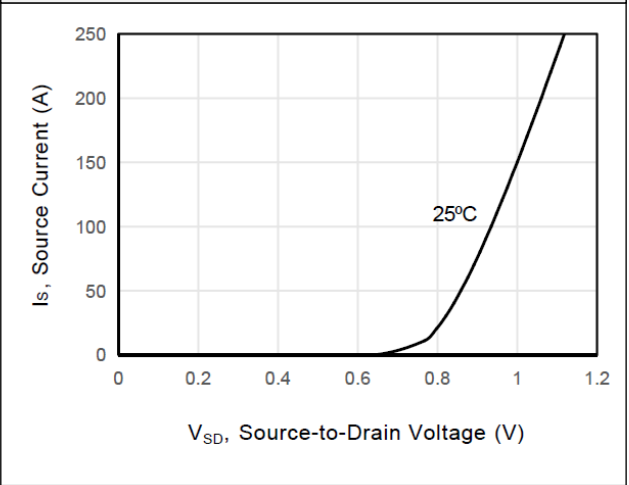


Figure 9. Breakdown Voltage vs Junction Temperature

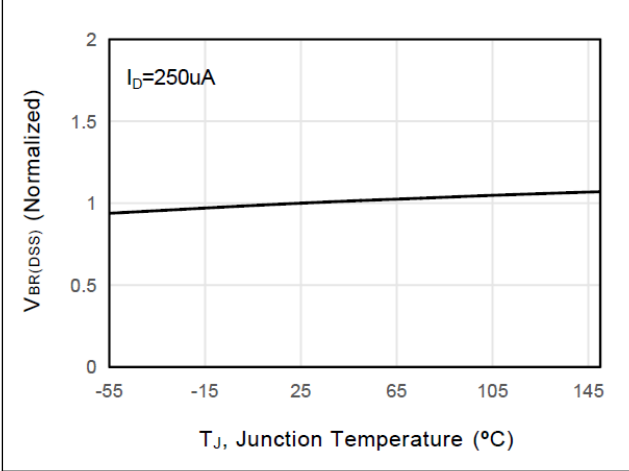


Figure 10. On-Resistance vs Temperature

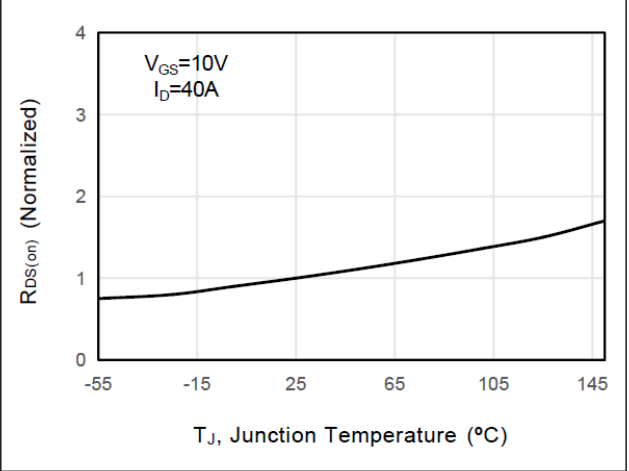
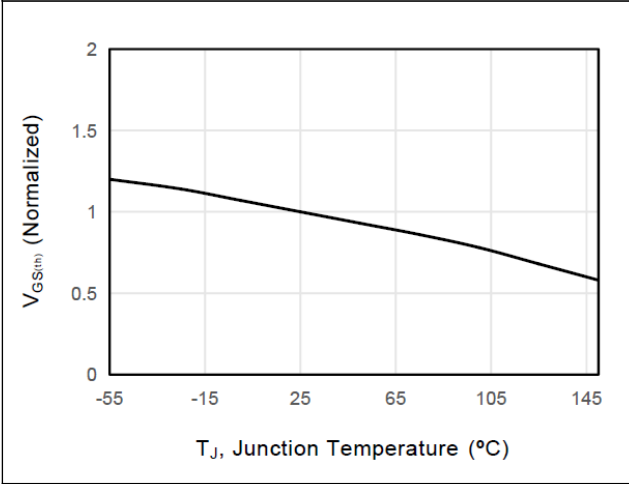


Figure 11. $V_{GS(th)}$ vs Junction Temperature



Test Circuits

Figure 12: Gate Charge Test Circuit and Waveform

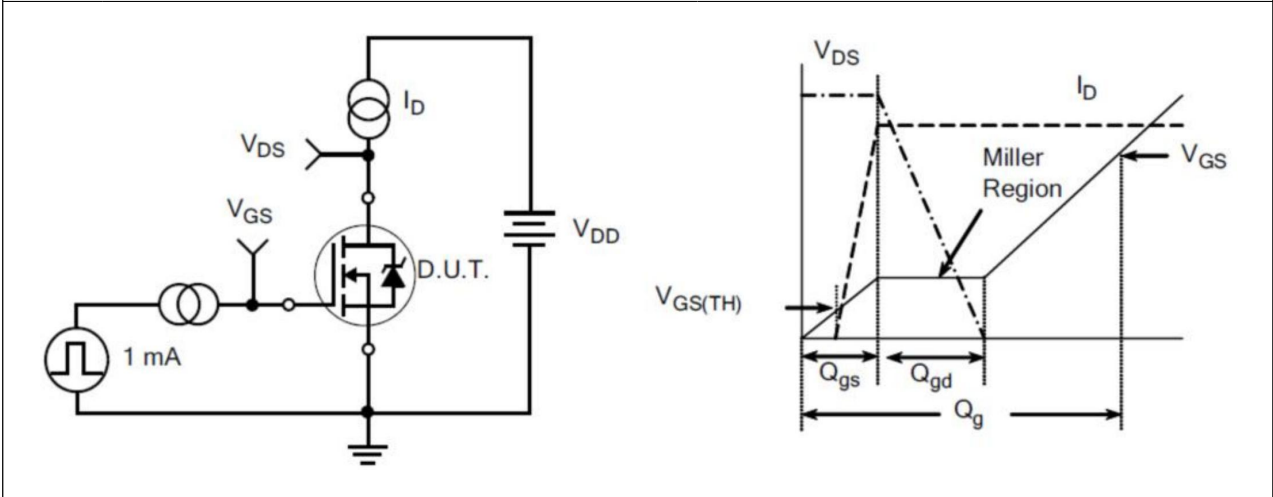


Figure 13: Resistive Switching Test Circuit and Waveform

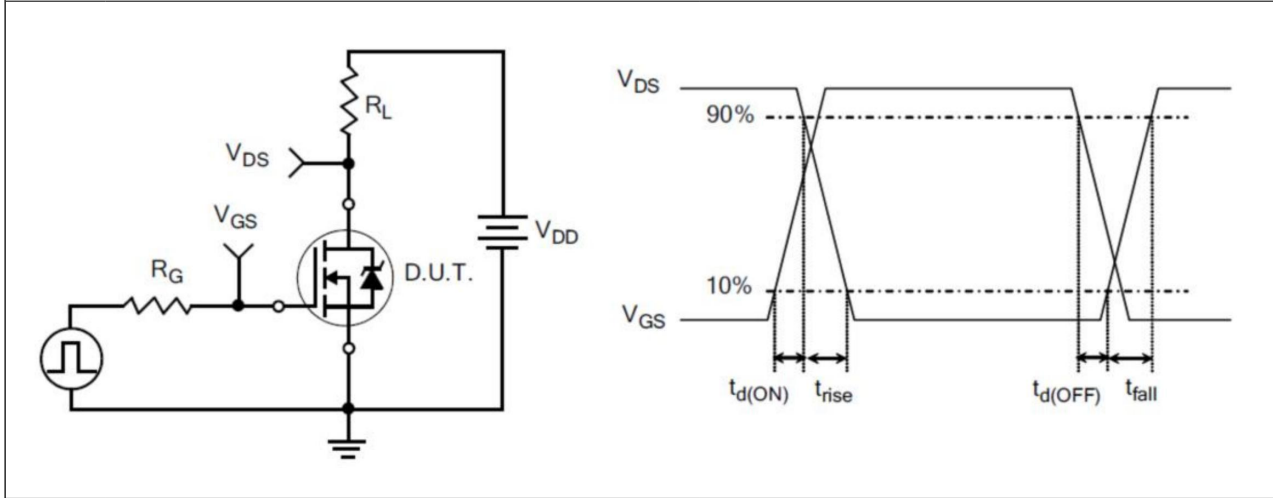
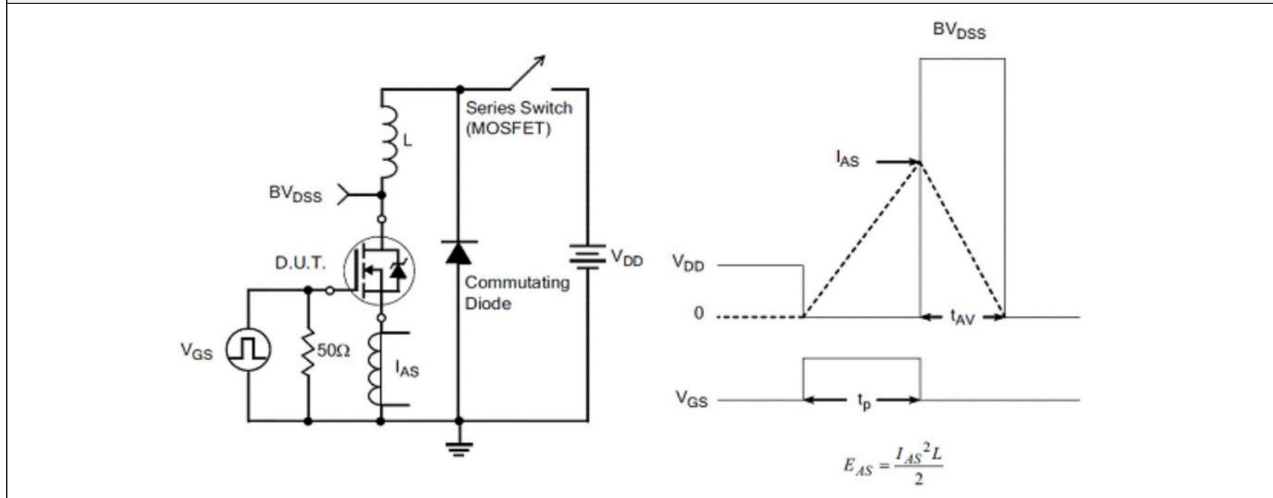
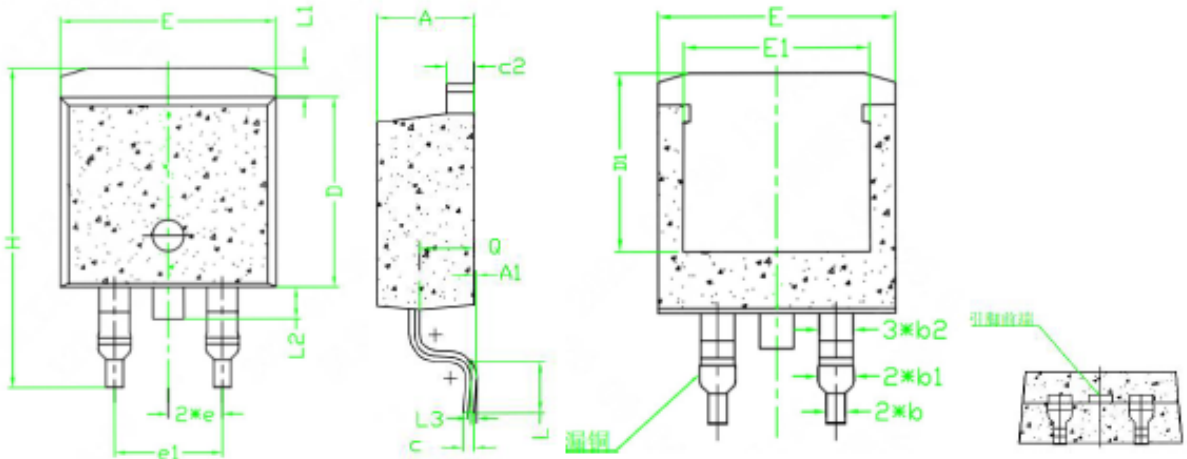


Figure 14: Unclamped Inductive Switching Test Circuit and Waveform



Package Outlines
D²PAK



SYMBOL	SIZE		
	MIN	NOM	MAX
A	4.40	4.50	4.70
A1	0.00	0.10	0.25
b	0.70	0.80	0.90
b1	1.20	1.55	1.75
b2	1.20	1.45	1.70
C		0.50	
c2		1.27	1.30
D	9.1	9.2	9.25
D1	6.86	7.65	
E	9.8	10	10.2
E1	7.9	8	8.1
e	2.54BSC		
e1	5.08BSC		
H	14.61	15.00	15.88
L	1.78	2.32	2.79
L1	1.36REF		
L2	1.50REF		

* Dimensions in millimeters

Package Marking and Ordering Information

Part Number	Top Marking	Package	Packing Method	Quantity
BSB10NH027SK1	BSB10NH027SK1	D ² PAK	Tape & Reel	1000 units

Disclaimer

Bestirpower reserve the right to make changes, corrections, enhancements, modifications, and improvements to Bestirpower products and/or to this document at any time without notice.

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. Bestirpower does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Products or technical information described in this document.

This document is the property of Bestirpower Co., LTD., and not allowed to copy or transformed to other format if not under the authority approval.

© 2026 bestirpower – All rights Reserved