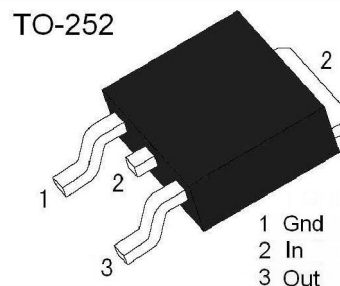


Description

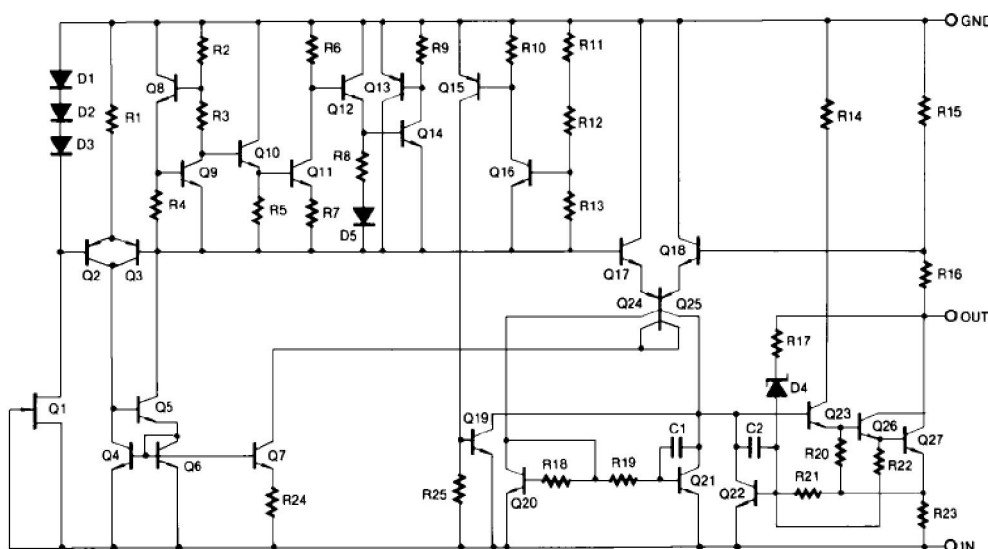
The 7905 series of 3-Terminal medium current negative voltage regulators are monolithic integrated circuits designed as fixed voltage regulators. These regulators employ internal current limiting, thermal shutdown and safe area compensation making them essentially indestructible.

Features

- No external components required
- Output current in excess of 1.5A
- Internal thermal overload
- Internal short circuit current limiting
- Output transistor safe area compensation
- Output voltages of -5V



Internal Block Diagram



Absolute Maximum Ratings (Ta = 25 °C)

Parameter	Symbol	Value	Unit
Input voltage	V_{IN}	-30	V
Output voltage	V_O	-5	V
Thermal resistance junction-air	$R_{\theta JA}$	65	°C/W
Thermal resistance junction-cases	$R_{\theta JC}$	5	°C/W
Operating Junction Temperature Range	T_j	0 ~ 125	°C
Storage Temperature Range	T_{stg}	-65 ~ 150	°C

Electrical Characteristics ($T_a = 25^\circ\text{C}$)

(Refer to the test circuits, $I_O=1\text{A}$, $V_I=-10\text{V}$, $C_I = 2.2\mu\text{F}$, $C_O=1\mu\text{F}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Output voltage	V_O	$T_J = 25^\circ\text{C}$	-4.8	-5.0	-5.2	V
		$I_O = 5\text{mA} \sim 1\text{A}$, $P_O < 15\text{W}$ $V_I = -7\text{V} \sim -20\text{V}$	-4.75	-5.0	-5.25	
Line regulation (Note)	ΔV_O	$T_J = 25^\circ\text{C}$			100	mV
					50	
Load regulation (Note)	ΔV_O	$T_J = 25^\circ\text{C}$			100	mV
					50	
Quiescent current	I_Q	$T_J = 25^\circ\text{C}$			6.0	mA
Quiescent current change	ΔI_Q	$I_O = 5\text{mA} \sim 1.5\text{A}$			0.5	mA
		$V_I = -8 \sim -25\text{V}$			0.8	
Output voltage drift	$\Delta V/\Delta T$	$I_O = 5\text{mA}$		-0.4		mV/ $^\circ\text{C}$
Output noise voltage	V_N	$f = 10\text{HZ} \sim 100\text{KHZ}$		40		μV
Ripple rejection	RR	$f = 120\text{Hz}$, $\Delta V_I = 10\text{V}$		60		dB
Dropout voltage	V_D	$T_J = 25^\circ\text{C}$, $I_O = 1.5\text{A}$		2		V
Short Circuit Current	I_{SC}	$T_J = 25^\circ\text{C}$, $V_I = -30\text{V}$		300		mA
Peak Current	I_{PK}	$T_J = 25^\circ\text{C}$		2.2		A

Notes: Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Applications circuits

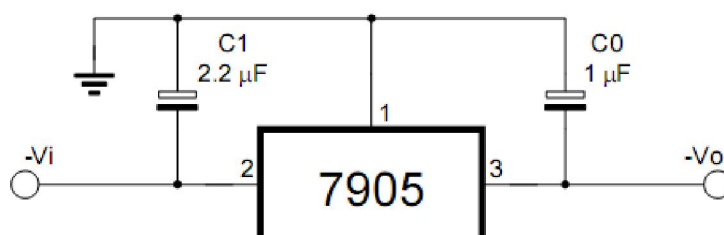


Figure.1 Fixed output regulator

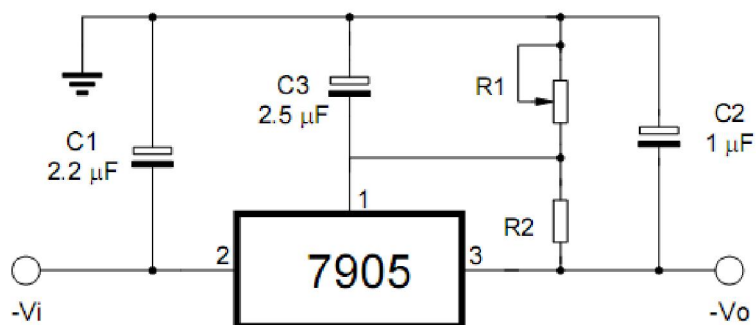
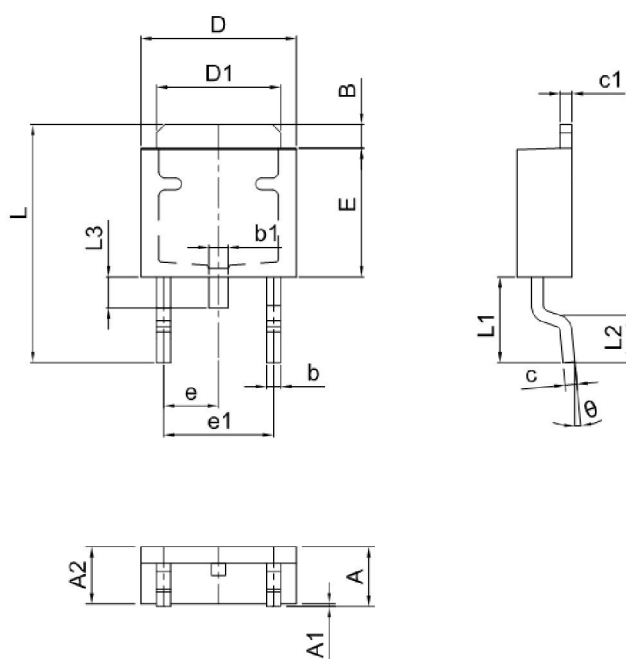


Figure.2 Circuit for increasing output voltage

Package Dimensions



Dim	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	2.20	2.50	0.087	0.098
A1	0.00	0.12	0.000	0.005
A2	2.20	2.40	0.087	0.094
B	1.20	1.60	0.047	0.063
b	0.50	0.70	0.020	0.028
b1	0.70	0.90	0.028	0.035
c	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.35	6.65	0.250	0.262
D1	5.20	5.40	0.205	0.213
E	5.40	5.70	0.213	0.224
e	2.20	2.40	0.087	0.094
e1	4.40	4.80	0.173	0.189
L	9.60	10.20	0.378	0.402
L1	2.70	3.10	0.106	0.122
L2	1.40	1.80	0.055	0.071
L3	0.90	1.50	0.035	0.059