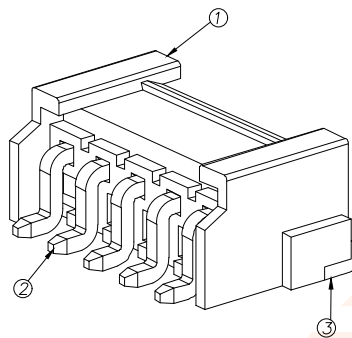
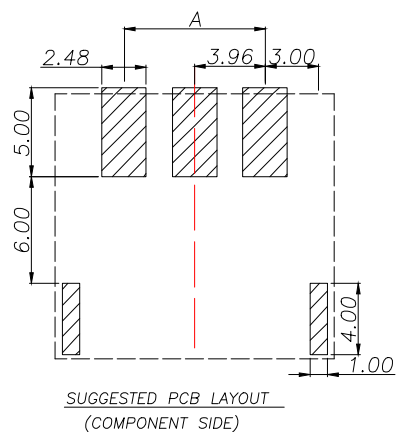
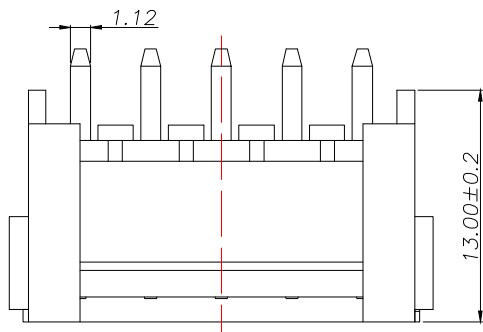
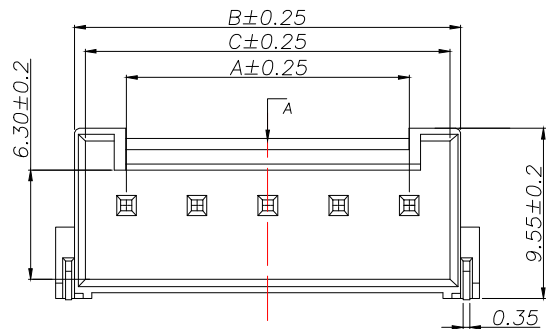
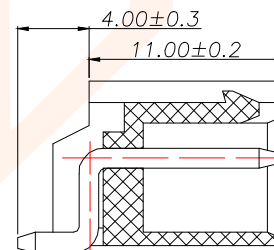
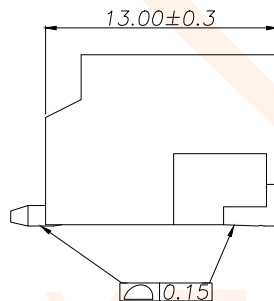


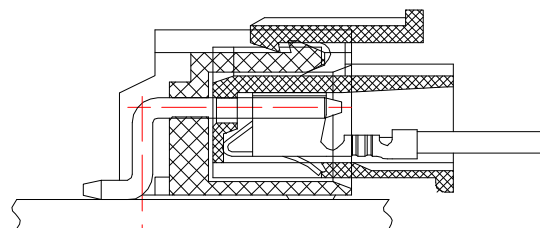
REV.	ECN NO.	DESCRIPTION	DATE	DESIGN



Part No	Pin	A	B	C
JXT-VH3.96-2PWB-P43	2	3.96	9.80	8.60
JXT-VH3.96-3PWB-P43	3	7.92	13.76	12.56
JXT-VH3.96-4PWB-P43	4	11.88	17.72	16.52
JXT-VH3.96-5PWB-P43	5	15.84	21.68	20.48
JXT-VH3.96-6PWB-P43	6	19.80	25.64	24.44
JXT-VH3.96-7PWB-P43	7	23.76	29.60	28.40
JXT-VH3.96-8PWB-P43	8	27.72	33.56	32.36
JXT-VH3.96-9PWB-P43	9	31.68	37.52	36.32
JXT-VH3.96-10PWB-P43	10	35.64	45.44	40.28



SECTION A-A



SUGGESTED PCB LAYOUT
(COMPONENT SIDE)

技术要求:

1. 塑件材料:LCP(UL-94V-0)
2. 接触件:黄铜镀锡
3. 接触电阻:≤30mΩ
4. 绝缘电阻:≥1000MΩ
5. 额定电压:250V AC DC
6. 额定电流:7.0A AC DC
7. 耐压:能承受1000V AC/Minute
8. 工作温度:-25~+85°
9. 可焊性试验:浸锡面积≥95%温度260, 时间2.5±0.5秒
10. 铅和镉等六大有害物质含量要符合环保要求。

3	焊片 Fitting Nail	黄铜	2	电镀(锡):整个表面镀底镍 30u"MIN,再镀锡80u"MIN
2	端子 Contact	黄铜	N*1	电镀(锡):整个表面镀底镍 30u"MIN,再镀锡80u"MIN
1	基座 Wafer	LCP (UL94V-0)	1	米色
序号	名称	材料	数量	备注

TOLERANCE		深圳聚兴泰电子科技有限公司 Shenzhen Juxingtai Electronic Technology Co., Ltd		
X.X	±0.35	X.°	±5°	PART NO.
.XX	±0.25	X.X°	±3°	JXT-VH3.96-NPWB-P43
.XXX	±0.15	X.XX°	±1°	DESIGN: Ethan
UNITS	mm	CHED: Olvia	MANUFACTURE DWG	
MATERILA	---	APPD: Ava	SCALE	SHEET
FINISH	---		N:A	1/1