



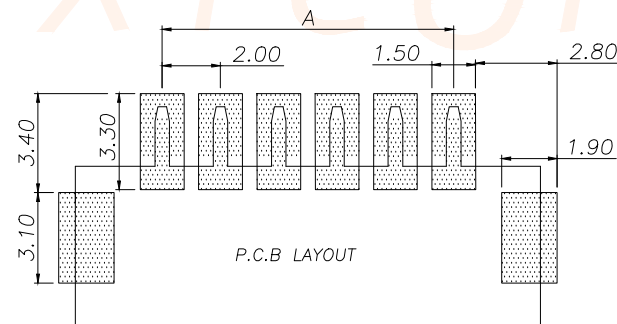
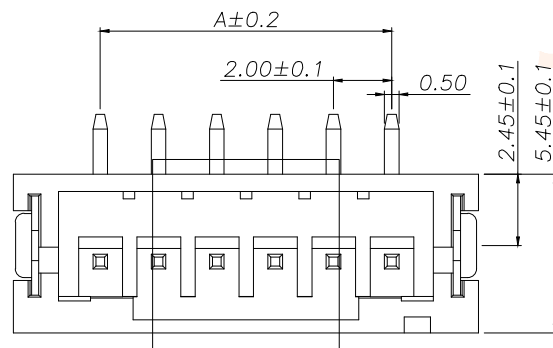
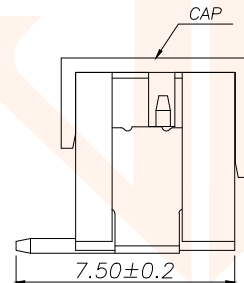
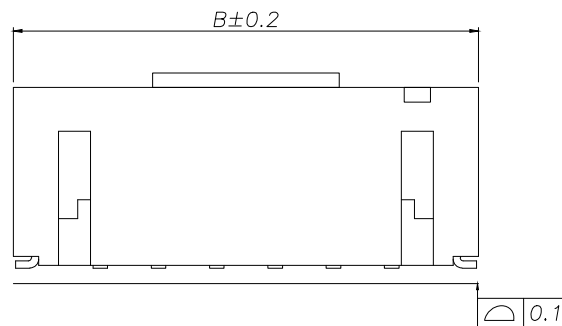
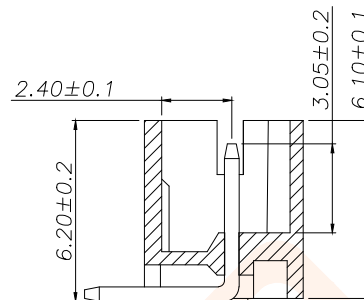
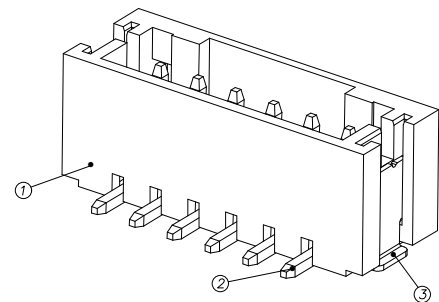
REV.	ECN NO.	DESCRIPTION	DATE	DESIGN

NOTES:

- 1.BASE: LCP
- 2.PIN: Brass
- 3.Solder Tab: Brass

SPECIFICATIONS:

- 1.Spacing: 2.0
- 2.Voltage Rating: 250V AC DC
- 3.Current Rating: 2A AC DC
- 4.Insulation Resistance: 800MΩ Min
- 5.Contact Resistance: 20mΩ Max
- 6.Withstanding voltage: 500V AC 1/Minute
- 7.Temperatuer Range: -25℃ ~+ 85℃



Part No	Pin	A	B
JXT-PH2.0-2PLB-P43	2	2.00	8.00
JXT-PH2.0-3PLB-P43	3	4.00	10.00
JXT-PH2.0-4PLB-P43	4	6.00	12.00
JXT-PH2.0-5PLB-P43	5	8.00	14.00
JXT-PH2.0-6PLB-P43	6	10.00	16.00
JXT-PH2.0-7PLB-P43	7	12.00	18.00
JXT-PH2.0-8PLB-P43	8	14.00	20.00
JXT-PH2.0-9PLB-P43	9	16.00	22.00
JXT-PH2.0-10PLB-P43	10	18.00	24.00
JXT-PH2.0-11PLB-P43	11	20.00	26.00
JXT-PH2.0-12PLB-P43	12	22.00	28.00
JXT-PH2.0-13PLB-P43	13	24.00	30.00
JXT-PH2.0-14PLB-P43	14	26.00	32.00
JXT-PH2.0-15PLB-P43	15	28.00	34.00
JXT-PH2.0-16PLB-P43	16	30.00	36.00

TOLERANCE		深圳聚兴泰电子科技有限公司 Shenzhen Juxingtai Electronic Technology Co., Ltd		
X.X ±0.30	X.* ±5°	PART NO.	TITLE:	
.XX ±0.20	X.X* ±3°	JXT-PH2.0-NPLB-P43	WAFER 2.00 立贴	
.XXX ±0.10	X.XX* ±1°	DESIGN: Ethan	MANUFACTURE DWG	
UNITS	mm	CHED: Olvia		SCALE
MATERILA	---	APPD: Ava		SHEET
FINISH	---		N:A	1/1