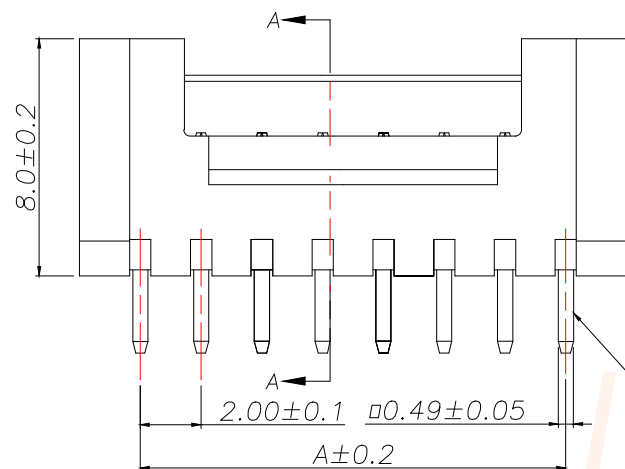
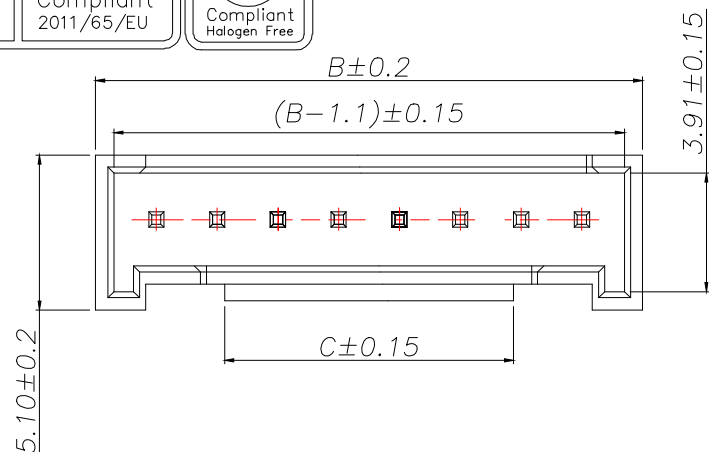
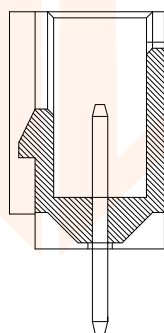
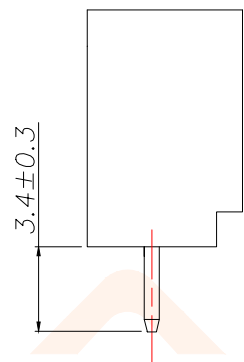




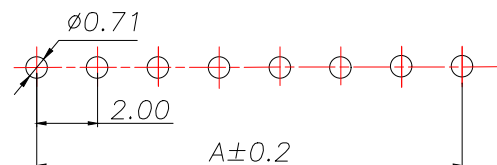
Circuit 1



SECTION A-A

技术要求:

1. 塑件材料: PA66 (UL-94V-0)
2. 接触件: 黄铜镀锡
3. 接触电阻: $\leq 10\text{m}\Omega$
4. 绝缘电阻: $\geq 100\text{M}\Omega$
5. 额定电压: 250V AC DC
6. 额定电流: 2.0A AC DC
7. 耐压: 能承受 1000V AC/Minute
8. 工作温度: $-25^{\circ}\sim+85^{\circ}$
9. 可焊性试验: 浸锡面积 $\geq 95\%$ 温度 $235\pm 5^{\circ}$, 时间 $2.5\pm 0.5\text{s}$
10. 铅和铜等六大有害物质含量要符合环保要求。



SUGGESTED PCB LAYOUT
(COMPONENT SIDE)

REV.	ECN NO.	DESCRIPTION	DATE	DESIGN

Part No	Pin	A	B	C
JXT-HY2.0-2PZZ-P11	2	2.00	6.00	2.50
JXT-HY2.0-3PZZ-P11	3	4.00	8.00	2.50
JXT-HY2.0-4PZZ-P11	4	6.00	10.00	3.50
JXT-HY2.0-5PZZ-P11	5	8.00	12.00	5.50
JXT-HY2.0-6PZZ-P11	6	10.00	14.00	7.50
JXT-HY2.0-7PZZ-P11	7	12.00	16.00	9.50
JXT-HY2.0-8PZZ-P11	8	14.00	18.00	11.50
JXT-HY2.0-9PZZ-P11	9	16.00	20.00	13.50
JXT-HY2.0-10PZZ-P11	10	18.00	22.00	15.50
JXT-HY2.0-11PZZ-P11	11	20.00	24.00	15.50
JXT-HY2.0-12PZZ-P11	12	22.00	26.00	15.50
JXT-HY2.0-13PZZ-P11	13	24.00	28.00	15.50
JXT-HY2.0-14PZZ-P11	14	26.00	30.00	15.50
JXT-HY2.0-15PZZ-P11	15	28.00	32.00	15.50

2	端子 Contact	黄铜	N*1	电镀(锡): 整个表面镀底镍 30u"MIN, 再镀锡 80u"MIN
1	基座 Wafer	LCP (UL94V-0)	1	白色
序号	名称	材料	数量	备注

TOLERANCE		深圳聚兴泰电子科技有限公司 Shenzhen Juxingtai Electronic Technology Co., Ltd		
X.X ±0.20	X.* ±10°	PART NO.	TITLE:	
.XX ±0.10	.X* ± 8°	JXT-HY2.0-NPZZ-P11	HY2.0MM 直针带扣	
.XXX ±0.05	.XX* ± 5°	DESIGN: Ethan	MANUFACTURE DWG	
UNITS	mm	CHED: Olvia		
MATERILA	---	APPD: Ava		
FINISH	---		SCALE	SHEET
			N:A	1/1