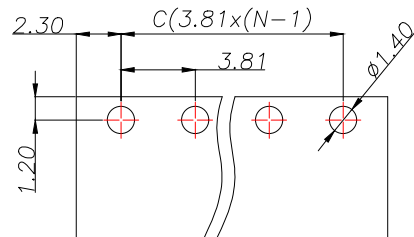
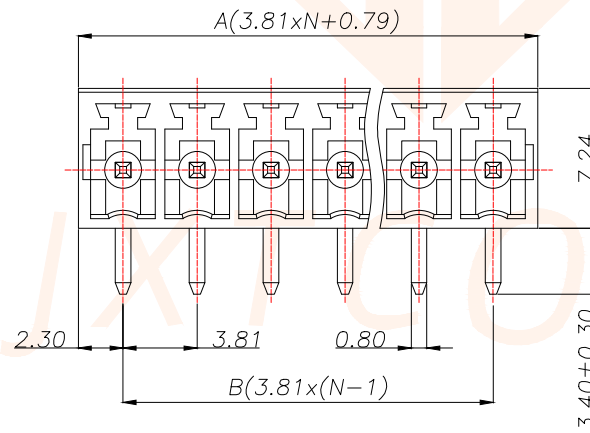
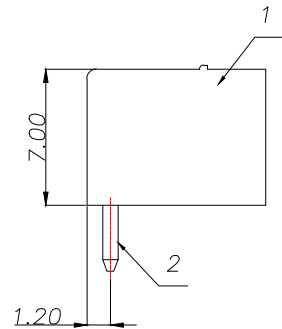
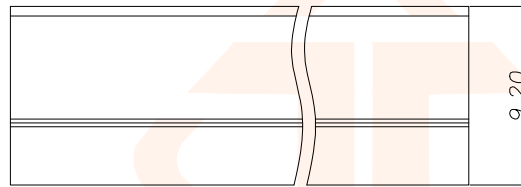


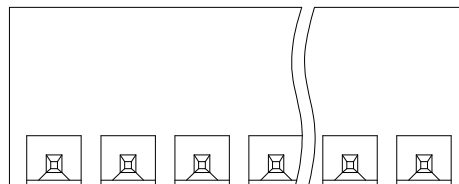


REV.	ECN NO.	DESCRIPTION	DATE	DESIGN

General tolerances(mm)														0.1/10 ±2°
0 ~ 3	>3 ~ 6	>6 ~ 10	>10 ~ 18	>18 ~ 30	>30 ~ 50	>50 ~ 80	>80 ~ 120	>120 ~ 180	>180 ~ 250	>250 ~ 315	>315 ~ 400	>400 ~ 500	>500	
±0.12	±0.18	±0.22	±0.26	±0.31	±0.37	±0.57	±0.80	±0.93	±1.05	±1.15	±1.60	±2.20	±2.50	



PCB LAYOUT



Product model	PIN	A	B	C
JXT-EDGRC-3.81-2P0G	2	8.41	3.81	3.81
JXT-EDGRC-3.81-3P0G	3	12.22	7.62	7.62
JXT-EDGRC-3.81-4P0G	4	16.03	11.43	11.43
JXT-EDGRC-3.81-5P0G	5	19.84	15.24	15.24
JXT-EDGRC-3.81-6P0G	6	23.65	19.05	19.05
JXT-EDGRC-3.81-7P0G	7	27.46	22.86	22.86
.....				
JXT-EDGRC-3.81-24P0G	24	3.81xN+0.79	3.81x(N-1)	3.81x(N-1)

UL Standard	B	C	D
Rated Voltage(V)/Current(A)	300/8	/	300/8
IEC Standard			
Overvoltage Category/Pollution Degree	II/2		
Rated Voltage(V)/Current(A)	250/7		
Rate Impulse Voltage(KV)	2.5		
Pitch(mm)/Poles	3.81/N=02~24P		
Operating temperature(°C)	-40~+105		
Conform to RoHS	Yes		

2	Pin	Copper alloy	Sn plated
1	Housing	PA66	UL94V-0
ITEM	NAME OF PART	MATERIAL	NOTES

TOLERANCE		深圳聚兴泰电子科技有限公司 Shenzhen Juxingtai Electronic Technology Co., Ltd			
		PART NO. JXT-EDGRC-3.81-NP0G	TITLE: Socket-type terminal		
UNITS	mm	DESIGN: Ethan	MANUFACTURE DWG		
MATERILA	---	CHED: Olvia			
FINISH	---	APPD: Ava		SCALE N:A	SHEET 1/1