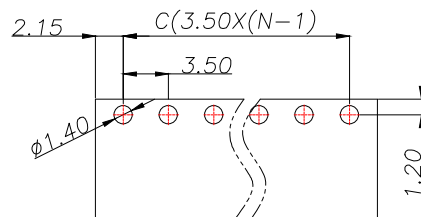
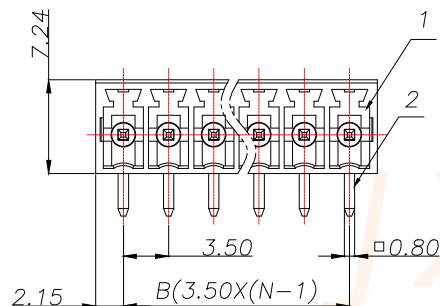
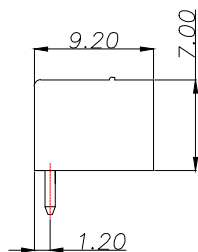
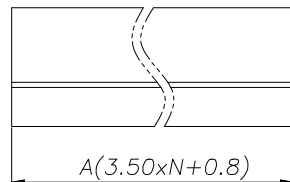




General tolerances(mm)														0.1/10 ±2°
0 ~ 3	>3 ~ 6	>6 ~ 10	>10 ~ 18	>18 ~ 30	>30 ~ 50	>50 ~ 80	>80 ~ 120	>120 ~ 180	>180 ~ 250	>250 ~ 315	>315 ~ 400	>400 ~ 500	>500	
±0.12	±0.18	±0.22	±0.26	±0.31	±0.37	±0.57	±0.80	±0.93	±1.05	±1.15	±1.60	±2.20	±2.50	



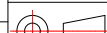
PCB LAYOUT

REV.	ECN NO.	DESCRIPTION	DATE	DESIGN

Product model	PIN	A	B	C
JXT-EDGRC-3.5-2P0G	2	7.80	3.50	3.50
JXT-EDGRC-3.5-3P0G	3	11.30	7.00	7.00
JXT-EDGRC-3.5-4P0G	4	14.80	10.50	10.50
JXT-EDGRC-3.5-5P0G	5	18.30	14.00	14.00
JXT-EDGRC-3.5-6P0G	6	21.80	17.50	17.50
JXT-EDGRC-3.5-7P0G	7	25.30	21.00	21.00
.....				
JXT-EDGRC-3.5-24P0G	24	3.5xN+0.8	3.50x(N-1)	3.50x(N-1)

UL Standard	B	C	D
Rated Voltage(V)/Current(A)	300/8	/	300/8
IEC Standard			
Overvoltage Category/Pollution Degree	II/2		
Rated Voltage(V)/Current(A)	250/7		
Rate Impulse Voltage(KV)	2.5		
Pitch(mm)/Poles	3.50/N=02~24P		
Operating temperature(*C)	-40C°~+105C°		
Conform to RoHS	Yes		

2	Pin	Copper Alloy	Sn Plated
1	Housing	PA66	UL94V-0
ITEM	NAME OF PART	MATERIAL	NOTES

TOLERANCE		 深圳聚兴泰电子科技有限公司 Shenzhen Juxingtai Electronic Technology Co., Ltd			
		PART NO.	TITLE: Socket-type terminal		
		JXT-EDGRC-3.5-NP0G			
UNITS	mm	DESIGN: Ethan	MANUFACTURE DWG		
MATERILA	— — —	CHED: Olvia			
FINISH	— — —	APPD: Ava			
				SCALE N:A	SHEET 1/1