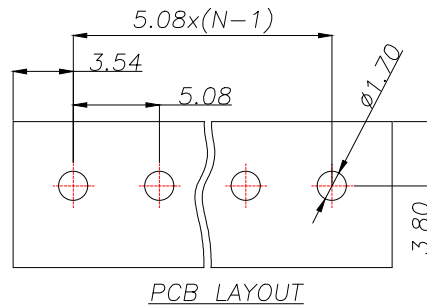
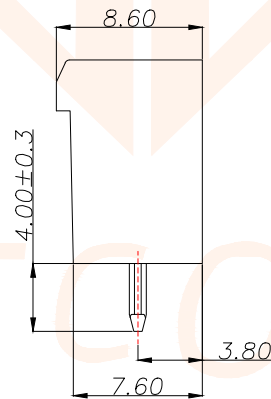
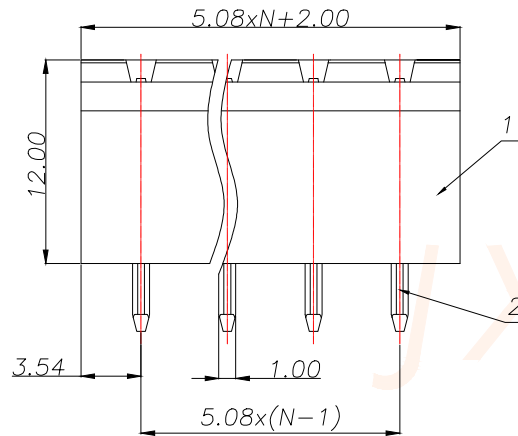
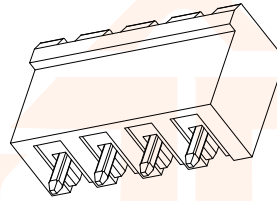
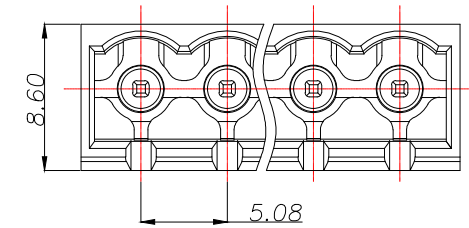




General tolerances(mm)													$\bigcirc$ 0.1/10
0 ~ 3	>3 ~ 6	>6 ~ 10	>10 ~ 18	>18 ~ 30	>30 ~ 50	>50 ~ 80	>80 ~ 120	>120 ~ 180	>180 ~ 250	>250 ~ 315	>315 ~ 400	>400 ~ 500	>500
±0.12	±0.18	±0.22	±0.26	±0.31	±0.37	±0.57	±0.80	±0.93	±1.05	±1.15	±1.60	±2.20	±2.50



PCB LAYOUT

REV.	ECN NO.	DESCRIPTION	DATE	DESIGN

Product model	PIN	A	B	C
JXT-EDGVC-5.08-2POG	2	12.16	5.08	5.08
JXT-EDGVC-5.08-3POG	3	17.24	10.16	10.16
JXT-EDGVC-5.08-4POG	4	22.32	15.24	15.24
JXT-EDGVC-5.08-5POG	5	27.40	20.32	20.32
JXT-EDGVC-5.08-6POG	6	32.48	25.40	25.40
JXT-EDGVC-5.08-7POG	7	37.56	30.48	30.48
.....				
JXT-EDGVC-5.08-24POG	24	5.08xN+2	5.08x(N-1)	5.08x(N-1)

UL Standard		B	C	D
Rated Voltage(V)/Current(A)		300/15	/	300/10
IEC Standard				
Overvoltage Category/Pollution Degree		Ⅲ/3	Ⅲ/2	Ⅱ/2
Rated Voltage(V)/Current(A)		/	320/20	/
Rate Impulse Voltage(KV)		/	4	/
Pitch(mm)/Poles		5.08/N=02~24P		
Operating temperature(°C)		-40°C~+105°C		
Conform to RoHS		Yes		

2	Pin	Copper	Sn Plated
1	Housing	PA66	UL94V-0
ITEM	NAME OF PART	MATERIAL	NOTES

TOLERANCE		 深圳聚兴泰电子科技有限公司 Shenzhen Juxingtai Electronic Technology Co., Ltd			
		PART NO.		TITLE:	
		JXT-EDGVC-5.08-NPOG		Socket-type terminal	
		DESIGN: Ethan		MANUFACTURE DWG	
UNITS	mm	CHED: Olvia			SCALE
MATERILA	— — —	APPD: Ava			SHEET
FINISH	— — —				N:A