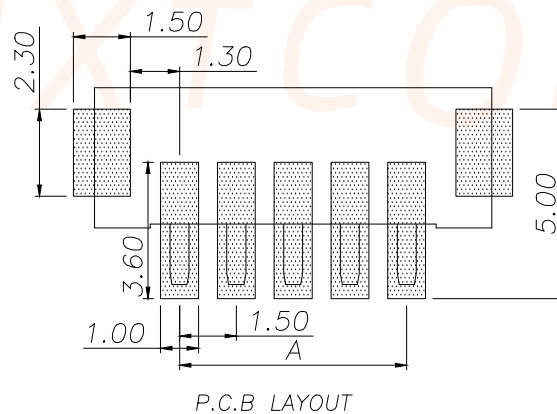
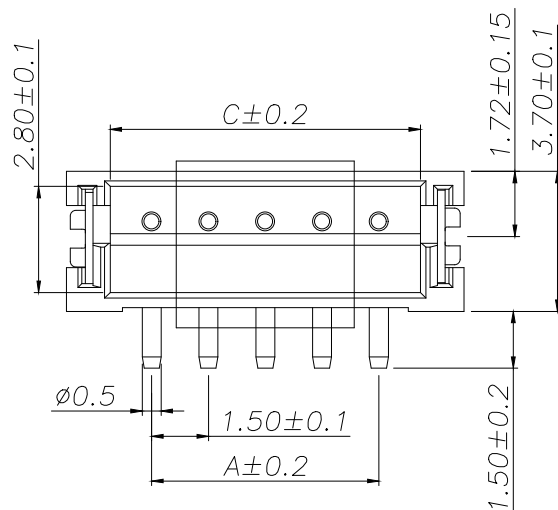
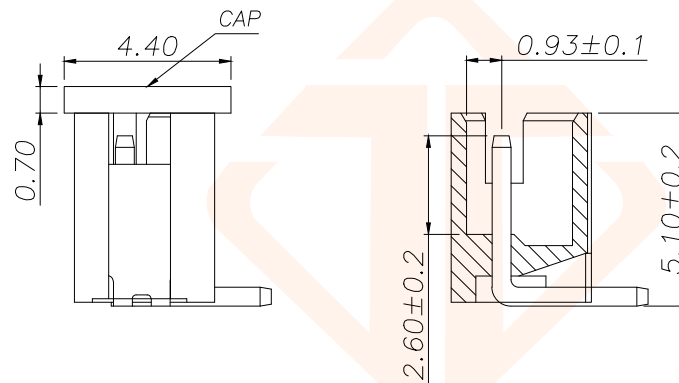
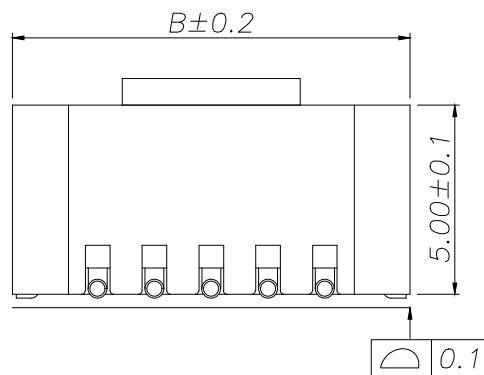
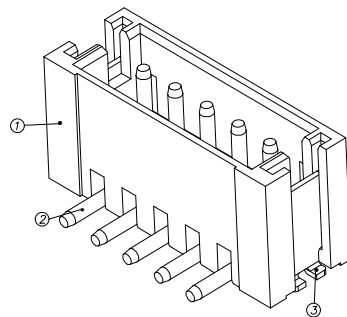




REV.	ECN NO.	DESCRIPTION	DATE	DESIGN

NOTES:

- 1.BASE: LCP
- 2.PIN: Brass
- 3.Solder Tab: Brass

SPECIFICATIONS:

- 1.Spacing: 1.5
- 2.Voltage Rating: 250V AC DC
- 3.Current Rating: 1A AC DC
- 4.Insulation Resistance: 500MΩ Min
- 5.Contact Resistance: 20mΩ Max
- 6.Withstanding voltage: 500V AC 1/Minute
- 7.Temperatuer Range: -25℃ ~+ 85℃

Part No	Pin	A	B	C
JXT-ZH1.5-2PLB-P43	2	1.50	6.00	3.65
JXT-ZH1.5-3PLB-P43	3	3.00	7.50	5.15
JXT-ZH1.5-4PLB-P43	4	4.50	9.00	6.65
JXT-ZH1.5-5PLB-P43	5	6.00	10.50	8.15
JXT-ZH1.5-6PLB-P43	6	7.50	12.00	9.65
JXT-ZH1.5-7PLB-P43	7	9.00	13.50	11.15
JXT-ZH1.5-8PLB-P43	8	10.50	15.00	12.65
JXT-ZH1.5-9PLB-P43	9	12.00	16.50	14.15
JXT-ZH1.5-10PLB-P43	10	13.50	18.00	15.65
JXT-ZH1.5-11PLB-P43	11	15.00	19.50	17.15
JXT-ZH1.5-12PLB-P43	12	16.50	21.00	18.65
JXT-ZH1.5-13PLB-P43	13	18.00	22.50	20.15
JXT-ZH1.5-14PLB-P43	14	19.50	24.00	21.65
JXT-ZH1.5-15PLB-P43	15	21.00	25.50	23.15
JXT-ZH1.5-16PLB-P43	16	22.50	27.00	24.65

TOLERANCE		深圳聚兴泰电子科技有限公司 Shenzhen Juxingtai Electronic Technology Co., Ltd		
X.X ±0.30	X.* ±5°	PART NO. JXT-ZH1.5-NPLB-P43	TITLE: WAFER 1.50 立贴	
.XX ±0.20	X.X* ±3°		MANUFACTURE DWG	
.XXX ±0.10	X.XX* ±1°			
UNITS	mm	DESIGN: Ethan		SCALE N:A
MATERILA	---	CHED: Olvia		
FINISH	---	APPD: Ava		