

General Description

This Power MOSFET is produced using Cmos's advanced planar stripe DMOS technology. These devices are well suited for high efficiency switched mode power supplies, active power factor correction, electronic lamp ballasts based on half bridge topology.

Features

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- RoHS Compliant

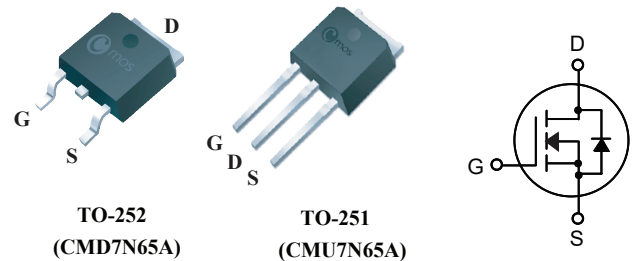
Product Summary

BVDSS	R _{DS(on)} max.	ID
650V	1.3Ω	7A

Applications

- Adaptor
- Power Supply
- High Current, High Speed Switching

TO-252/251 Pin Configuration



Absolute Maximum Ratings (T_c=25°C unless otherwise noted)

Symbol	Parameter	Value	Units
V _{DSS}	Drain-Source Voltage	650	V
I _D	Drain Current - Continuous (T _C = 25°C)	7	A
	- Continuous (T _C = 100°C)	4.2	A
I _{DM}	Drain Current - Pulsed ¹	28	A
V _{GSS}	Gate-Source Voltage	± 30	V
dv/dt	Peak Diode Recovery dv/dt ²	4.5	V/ns
P _D	Power Dissipation (T _C = 25°C)	100	W
E _{AS}	Avalanche energy ³	735	mJ
T _J , T _{STG}	Operating and Storage Temperature Range	-50 to +150	°C
T _L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	°C

Thermal Characteristics

Symbol	Parameter	Value	Units
R _{θJC}	Thermal Resistance, Junction-to-Case	1.25	°C/W
R _{θCS}	Thermal Resistance, Case-to-Sink Typ.	0.5	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

Electrical Characteristic (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
V_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	650	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250\text{ }\mu\text{A}$, Referenced to 25°C	--	0.8	--	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	μA
		$V_{DS} = 520\text{ V}, T_C = 125^\circ\text{C}$	--	--	10	
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2	--	4	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 3.5\text{A}$	--	1.1	1.3	Ω
R_g	Gate Resistance	$V_{DS}=0\text{V} , V_{GS}=0\text{V} , f=1\text{MHz}$	--	3	--	Ω
g_{fs}	Forward Transconductance	$V_{Ds}=20\text{V}, I_D=3.5\text{A}$	--	8	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	1100	--	pF
C_{oss}	Output Capacitance		--	105	--	pF
C_{rss}	Reverse Transfer Capacitance		--	10	--	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 325\text{ V}, I_D = 7\text{A}$ $R_G = 25\Omega$ (note:4,5)	--	20	--	ns
t_r	Turn-On Rise Time		--	50	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	90	--	ns
t_f	Turn-Off Fall Time		--	55	--	ns
Q_g	Total Gate Charge	$V_{DS} = 520\text{ V}, I_D = 7\text{A}$ $V_{GS} = 10\text{ V}$ (note:4,5)	--	24	--	nC
Q_{gs}	Gate-Source Charge		--	5	--	nC
Q_{gd}	Gate-Drain Charge		--	9	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	7	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	28	A
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = 7\text{A}$	--	0.87	1.4	V

note:

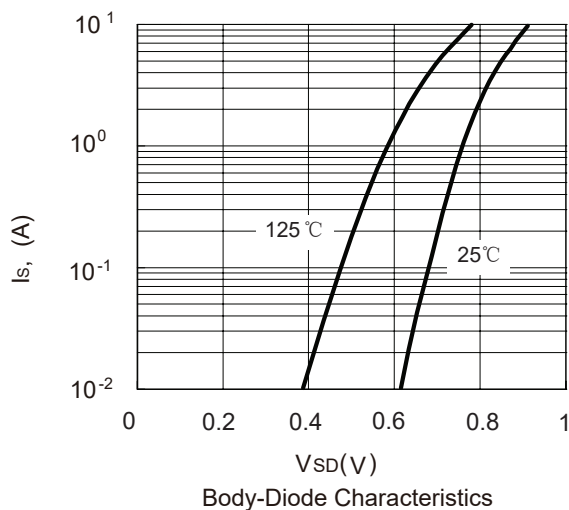
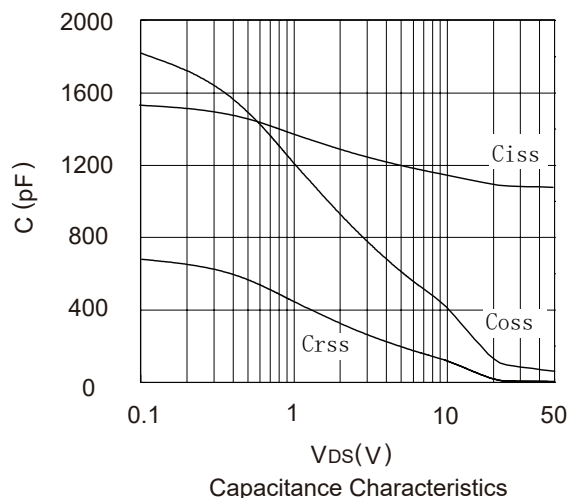
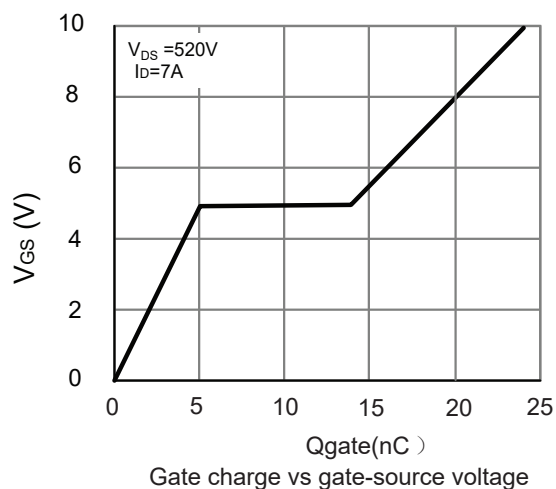
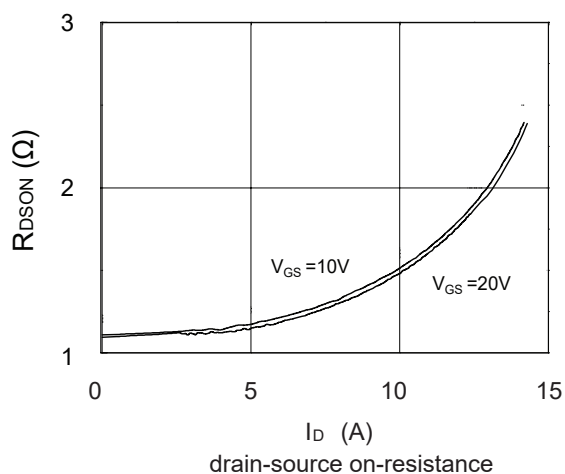
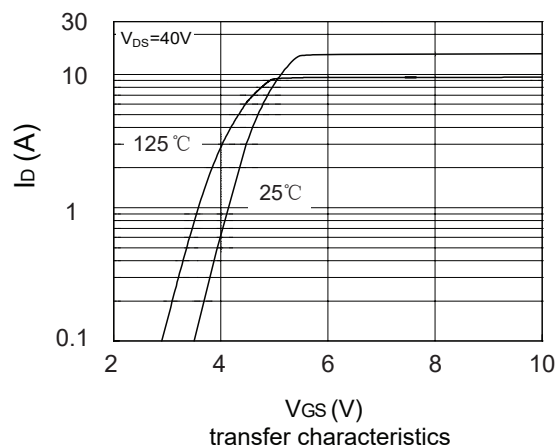
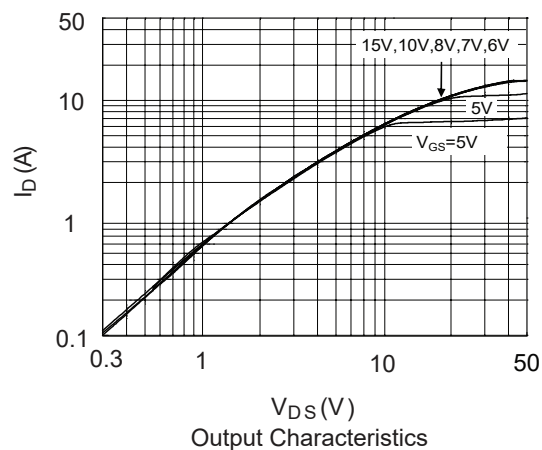
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. $I_{SD} \leq 7\text{ A}$, $di/dt \leq 200\text{ A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$.
3. The EAS data shows Max. rating . The test condition is $V_{DD}=80\text{ V}$, $V_{GS}=10\text{ V}$, $L=30\text{ mH}$, $I_{AS}=7\text{ A}$.
4. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.
5. Essentially independent of operating temperature.

This product has been designed and qualified for the consumer market.

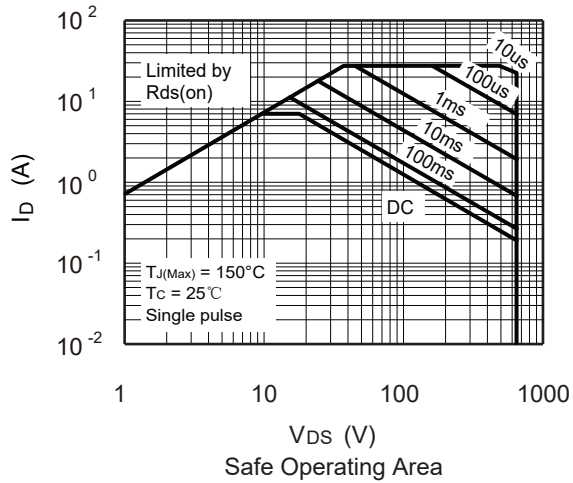
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Cmos reserves the right to improve product design, functions and reliability without notice. Please refer to the latest version of specification.

Typical Characteristics



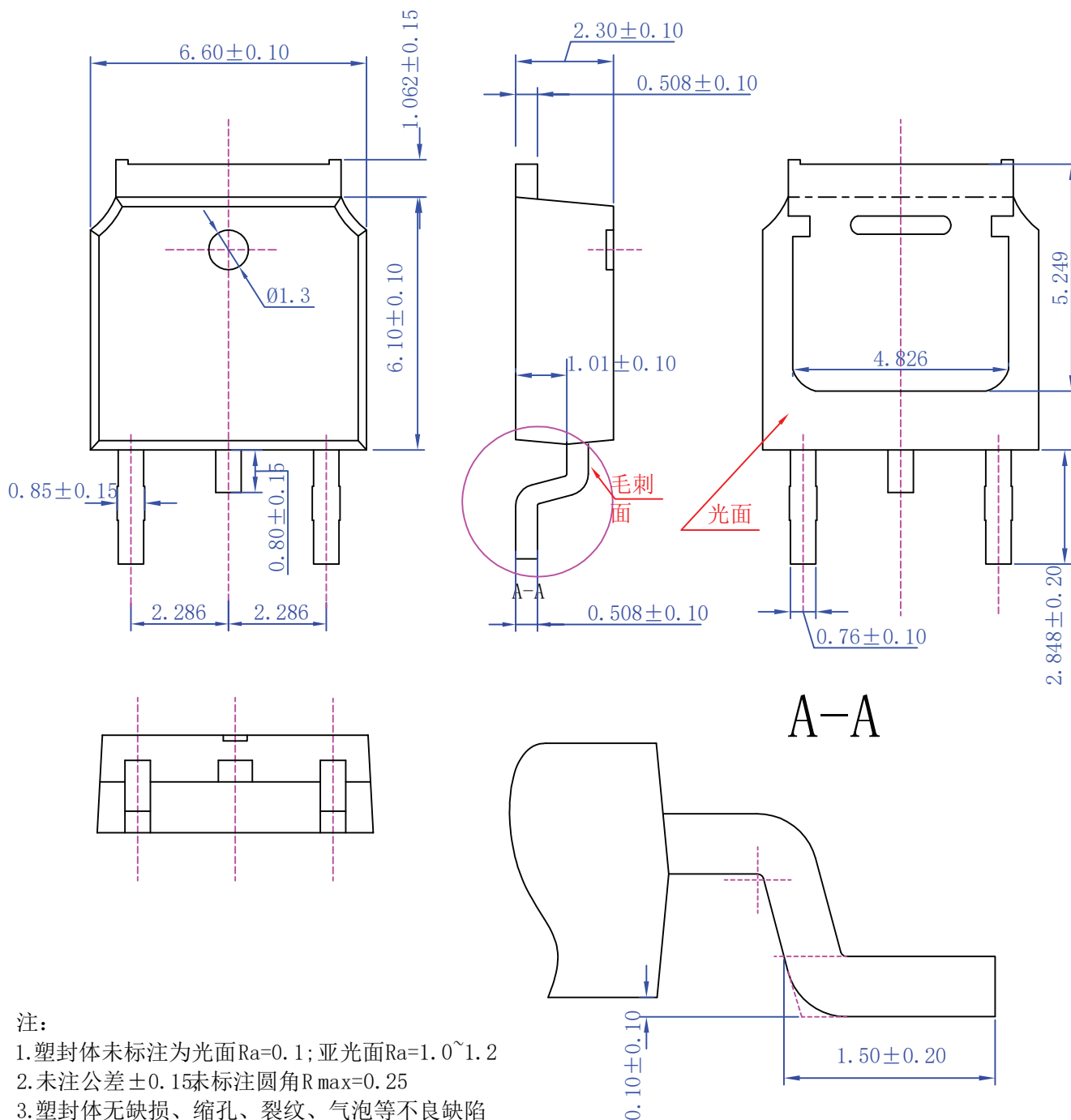
Characteristics



Package Dimension

TO-252

Unit :mm



Package Dimension

TO-251A

Unit :mm

