TECH PUBLIC
台舟电子

SN74AUC1G126DCKR-TP

SINGLE BUS BUFFER GATEWITH 3-STATE OUTPUT

WWW.TECHPUBLIC.COM

Features

- Wide supply voltage range from 0.8V to 2.7V
- Inputs accept voltages up to 3.6V
- I_{OFF} supports partial-power-down mode
- Low static power consumption; I_{CC}=0.5μA (Max.)
- ESD Protection Exceeds JESD 22
  -2000-V Human-Body Model (A114-A)
  -200-V Machine Model (A115-A)
  -1000-V Charged-Device Model (C101)

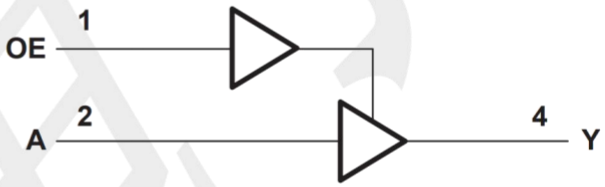
Applications

- AV Receiver
- Audio Dock:Portable
- Blu-ray Player and Home Theater
- Embedded PC
- Personal Digital Assistant(PDA)
- Power:Telecom/Server AC/DC Supply:Single Controller:Analog and Digital
- Solid State Drive(SSD):Client and Enterprise
- Wireless Headset,Keyboard,and Mouse

General Description

The is a single bus buffer/line driver with 3-state output. When the output enable (OE) is high the output will be disabled. In contrast, when the OE is low, true data will pass from A input to the Y output.

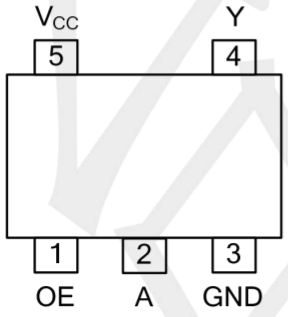
Logic Diagram



Ordering Information

ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION
SN74AUC1G126DCKR-TP	SOT353	Tape and Reel,3000

Pin Configuratio (TOP VIEW)



SOT353

Function Table (each gate)

INPUT(OE)	INPUT(A)	OUTPUT(Y)
H	H	H
H	L	L
L	X	Z

H: HIGH voltage level; L: LOW voltage level;
X=don't care; Z=high-impedance OFF-state.

Absolute Maximum Ratings

PARAMETER	SYMBOL	CONDITIONS	RATINGS	UNIT
Supply Voltage	V_{CC}		-0.5 ~ +3.6	V
Input Voltage	V_{IN}		-0.5 ~ +3.6	V
Output Voltage	V_{OUT}	Output in the high or low state	-0.5 ~ +V_{CC}+0.5V	V
		Output in the power-off state	-0.5 ~ +3.6	V
VCC or GND Current	I_{CC}		±50	mA
Continuous Output Current	I_{OUT}	VOUT=0~VCC	±20	mA
Input Clamp Current	I_{IK}	VIN<0	-50	mA
Output Clamp Current	I_{OK}	VOUT<0	-50	mA
Storage Temperature Range	T_{STG}		-65 ~ +150	℃
Junction to Ambient	θ_{JA}	SOT353	280	℃/W

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Recommended Operating Conditions

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V_{CC}	Operating	0.8	--	2.7	V
Input Voltage	V_{IN}		0	--	2.7	V
Output Voltage	V_{OUT}	High or low state	0	--	V_{CC}	V
Input Transition Rise or Fall Rate	$\Delta t/\Delta v$	V_{CC}=0.8V ~ 2.7V	--	--	200	ns/V
Operating Temperature	T_A		-40	--	+125	°C

Electrical Characteristics (T_A=25°C ,unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
High-level Input Voltage	V_{IH}	V_{CC}=0.8V		0.7×V_{CC}	--	--	V
		V_{CC}=0.9V ~ 1.95V		0.65×V_{CC}	--	--	V
		V_{CC}=2.3V ~ 2.7V		1.6	--	--	V
Low-level Input Voltage	V_{IL}	V_{CC}=0.8V		--	--	0.3×V_{CC}	V
		V_{CC}=1.1V ~ 1.95V		--	--	0.35×V_{CC}	V
		V_{CC}=2.3V ~ 2.7V		--	--	0.7	V
High-Level Output Voltage	V_{OH}	V_{CC}=0.8 ~ 2.7V, I_{OH}=-20μA		V_{CC}-0.1	--	--	V
		V_{CC}=1.1V, I_{OH}=-1.1mA		0.75×V_{CC}	--	--	V
		V_{CC}=1.4V, I_{OH}=-1.7mA		1.11	--	--	V
		V_{CC}=1.65V, I_{OH}=-1.9mA		1.32	--	--	V
		V_{CC}=2.7V	I_{OH}=-2.3mA	2.05	--	--	V
			I_{OH}=-3.1mA	1.9	--	--	V
Low-Level Output Voltage	V_{OL}	V_{CC}=0.8 ~ 2.7V, I_{OL}=20μA		--	--	0.1	V
		V_{CC}=1.1V, I_{OL}=1.1mA		--	--	0.3×V_{CC}	V
		V_{CC}=1.4V, I_{OL}=1.7mA		--	--	0.31	V
		V_{CC}=1.65V, I_{OL}=1.9mA		--	--	0.31	V
		V_{CC}=2.7V	I_{OL}=2.3mA	--	--	0.31	V
			I_{OL}=3.1mA	--	--	0.44	V
Input Leakage Current	$I_{I(LEAK)}$	V_{CC}=0 ~ 2.7V, V_{IN}=GND ~ 2.7V		--	--	±0.1	μA
Power OFF Leakage Current	I_{off}	V_{CC}=0 V, V_{IN} or V_{OUT}=0 ~ 2.7V		--	--	±0.2	μA
Additional Power OFF Leakage Current	ΔI_{off}	V_{CC}=0 V~0.2V,V_{IN} or V_{OUT}=0 ~ 2.7V		--	--	±0.2	μA
Quiescent Supply Current	I_{CC}	V_{CC}=0.8 ~2.7V, V_{IN}=V_{CC} or GND,I_{OUT}=0		--	--	0.5	μA
Additional Quiescent Supply Current Per Input Pin	ΔI_{CC}	V_{CC}=2.7 V, V_{IN} =V_{CC}-0.6V, I_{OUT}=0		--	--	40	μA
Input Capacitance	CI	V_{CC}=0V-2.7V, V_{IN}=V_{CC} or GND		--	1.5	--	pF
Output Capacitance	C_{OUT}	V_{CC} =0V, V_{OUT}=GND		--	3.0	--	pF

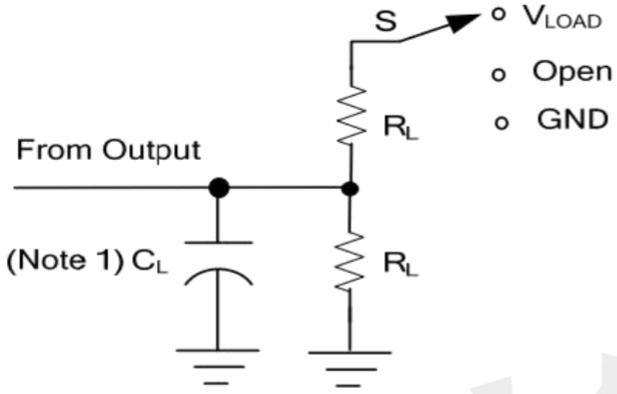
OPERATING CHARACTERISTICS (f=10MHz, TA =25°C , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	C_{PD}	V_{CC}=0.8V	--	3.8	--	pF
		V_{CC}=1.2±0.1V	--	3.7	--	pF
		V_{CC}=1.5±0.1V	--	3.7	--	pF
		V_{CC}=1.8±0.15V	--	3.7	--	pF
		V_{CC}=2.5±0.2V	--	3.9	--	pF

SWITCHING CHARACTERISTICS (TA =25°C，unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Propagation delay from inputs (A) to output(Y)	t_{PD}	CL=5pF, RL=1MΩ	V_{CC}=0.8V	--	18.1	--	ns
			V_{CC}=1.2±0.1V	4.3	7.4	--	ns
			V_{CC}=1.5±0.1V	3.3	5.2	--	ns
			V_{CC}=1.8±0.15V	2.6	4.1	--	ns
			V_{CC}=2.5±0.2V	2	2.9	--	ns
		CL=10pF, RL=1MΩ	V_{CC}=0.8V	--	20.5	--	ns
			V_{CC}=1.2±0.1V	4.6	8.4	--	ns
			V_{CC}=1.5±0.1V	3.5	5.9	--	ns
			V_{CC}=1.8±0.15V	3.9	4.7	--	ns
			V_{CC}=2.5±0.2V	2.3	3.4	--	ns
		CL=15pF, RL=1MΩ	V_{CC}=0.8V	--	22.5	--	ns
			V_{CC}=1.2±0.1V	5.8	9.3	--	ns
			V_{CC}=1.5±0.1V	4.4	6.6	--	ns
			V_{CC}=1.8±0.15V	3.5	5.3	--	ns
			V_{CC}=2.5±0.2V	2.7	3.9	--	ns
		CL=30pF, RL=1MΩ	V_{CC}=0.8V	--	29	--	ns
			V_{CC}=1.2±0.1V	7.4	12	--	ns
			V_{CC}=1.5±0.1V	5.7	8.6	--	ns
			V_{CC}=1.8±0.15V	4.8	6.9	--	ns
			V_{CC}=2.5±0.2V	3.9	5.1	--	ns

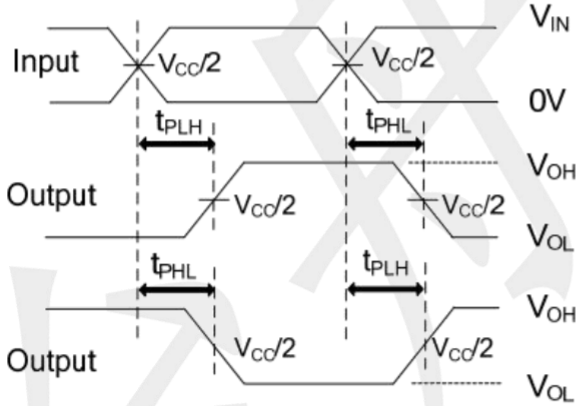
TEST CIRCUIT AND WAVEFORMS



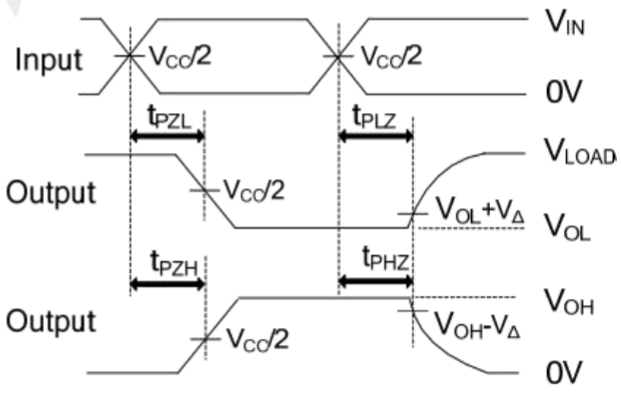
TEST	S
t_{PLH}/t_{PHL}	Open
t_{PHZ}/t_{PZH}	GND
t_{PLZ}/t_{PZL}	2×V_{CC}

TEST CIRCUIT

V_{CC}	V_{IN}	t_R / t_F	V_M	V_{LOAD}	C_L	R_L	V_Δ
0.8	V_{CC}	3ns	$V_{CC}/2$	2×V_{CC}	5,10,15,30pF	5kΩ	0.1V
1.2±0.1V	V_{CC}	3ns	$V_{CC}/2$	2×V_{CC}	5,10,15,30pF	5kΩ	0.1V
1.5±0.1V	V_{CC}	3ns	$V_{CC}/2$	2×V_{CC}	5,10,15,30pF	5kΩ	0.1V
1.8±0.15V	V_{CC}	3ns	$V_{CC}/2$	2×V_{CC}	5,10,15,30pF	5kΩ	0.15V
2.5±0.2V	V_{CC}	3ns	$V_{CC}/2$	2×V_{CC}	5,10,15,30pF	5kΩ	0.15V



PROPAGATION DELAY TIMES



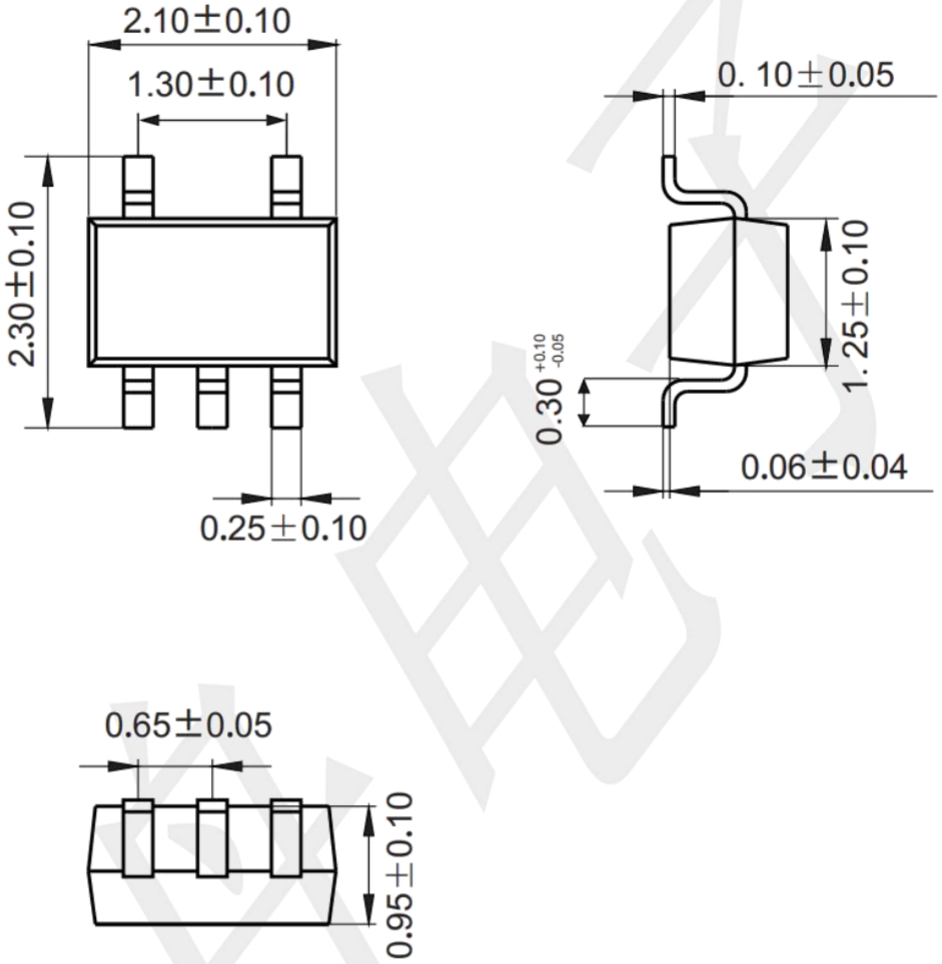
ENABLE AND DISABLE TIMES

Notes:

1. CL includes probe and jig capacitance.
2. All input pulses are supplied by generators having the following characteristics: PRR ≤10MHz, ZO = 50Ω.

Package information (Unit: mm)

SOT353



Mounting Pad Layout (unit: mm)