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TPM1003NS3LG
N-Channel MOSFET

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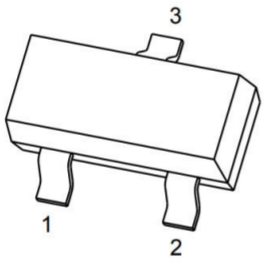
Product Summary

- V_{DS} 100 V
- I_{DS} (V_{GS}=10V) 3.0 A
- $R_{DS(ON)}$ (V_{GS}=10V) ≤85mΩ

Application

- Interfacing Switching
- Load Switch
- Portable equipment and battery

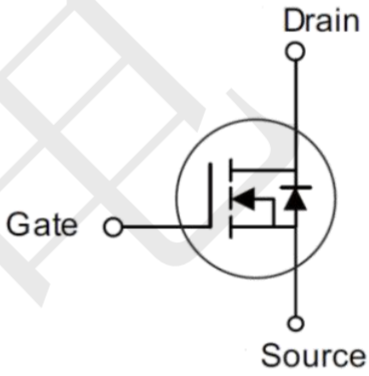
Package and Pin Configuration



1. GATE
2. SOURCE
3. DRAIN

SOT-23-3L

Circuit diagram



Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current TA=25°C		I_D	3.0	A
Continuous Drain Current TA=70°C		I_D	2.4	A
Pulsed Drain Current (t = 100 μs)		I_{DM}	20	A
Maximum Power Dissipation	TA=25℃	P_D	1.4	W
	TA=70℃		0.8	W
Operating Junction Temperature Range		T_J	-55 to +150	℃
Storage Temperature Range		T_{stg}	-55 to +150	℃

Thermal Characteristic

PARAMETER		Symbol	Value	Unit
Thermal Resistance from Junction to Ambient(t≤10s)	PCB Mount (Note)	$R_{\theta JA}$	90	°C/W

Note : When mounted on 1" square PCB (FR4 material).

Electrical Characteristics (T_A=25℃unless otherwise noted)

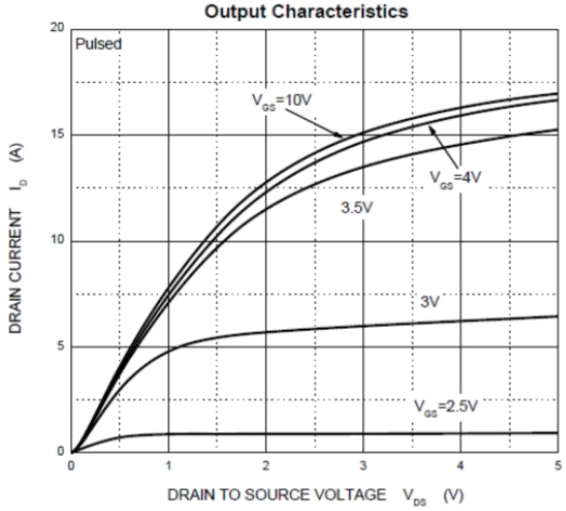
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Static						
Drain-Source Breakdown Voltage	V_{GS}=0V, I_D= 10μA	BV_{DSS}	100	--	--	V
Gate-Source Threshold Voltage	V_{DS}=V_{GS} , I_D= 250μA	$V_{GS(th)}$	1.3	1.8	2.4	V
Gate-Source Leakage	V_{DS}=0V, V_{GS}= ±20V	I_{GSS}	--	--	±100	nA
Zero Gate Voltage Drain Current	V_{DS}= 120V, V_{GS}=0V	I_{DSS}	--	--	1.0	μA
Drain-Source On-State Resistance (Note 1)	V_{GS}= 10V, I_D= 3.0A	R_{DS}(on)	--	67	85	mΩ
	V_{GS}= 4.5V, I_D= 1.5A		--	85	105	
Forward Transconductance (Note 2)	V_{DS}= 5V, I_D= 1.0A	g_{fs}	--	32	--	S
Dynamic (Note 2)						
Input Capacitance	V_{DS} = 25V, V_{GS} = 0V, F= 1.0MHz	C_{iss}	--	690	--	pF
Output Capacitance		C_{oss}	--	120	--	
Reverse Transfer Capacitance		C_{rss}	--	90	--	
Switching						
Turn-On Delay Time (Note 3)	V_{DS} = 30V, V_{Gs}= 10V, I_D = 2A, R_G = 2.5Ω.	$t_{d(on)}$	--	11	--	nS
Rise Time (Note 3)		t_r	--	7.4	--	
Turn-Off Delay Time (Note 3)		$t_{d(off)}$	--	35	--	
Fall Time (Note 3)		t_f	--	9.1	--	
Total Gate Charge	V_{DS} = 30V, I_D= 3A, V_{GS}= 10V	Qg	--	16	--	nC
Gate Source Charge		Qgs	--	3.2	--	
Gate Drain Charge		Qgd	--	4.7	--	
Source-Drain Diode Ratings and Characteristics (Note 2)						
Forward Voltage	V_{GS} = 0V, I_F = 1A	V_{SD}	--	0.8	1.2	V
Continuous Source Current	Integral reverse diode in the MOSFET	I_S	--	--	3.0	A
Pulsed Current (Note 1)		I_{SM}	--	--	20	A

Notes:

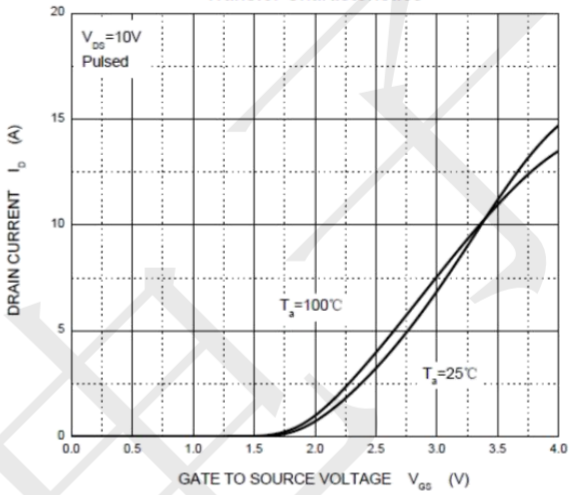
1. Pulse test; pulse width ≤ 300 μS, duty cycle ≤ 2%.
2. Guaranteed by design, not subject to production testing.
3. Independent of operating temperature

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

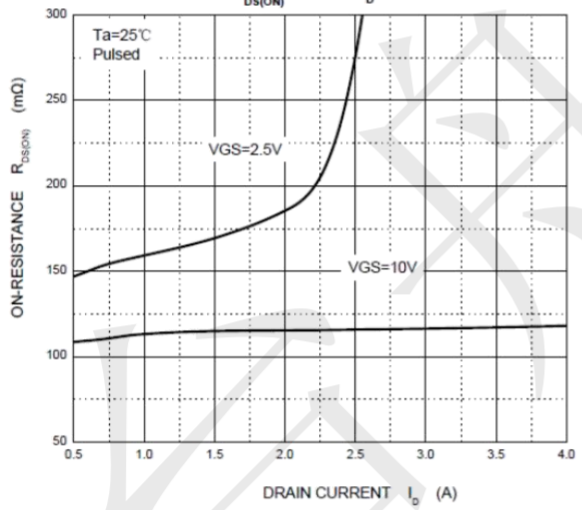
Output Characteristics



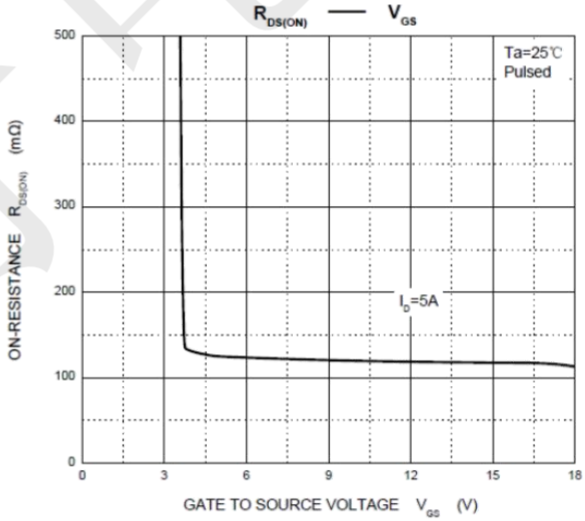
Transfer Characteristics



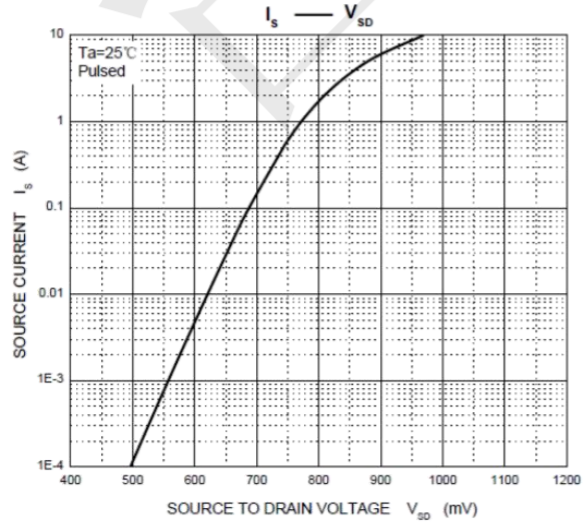
$R_{DS(ON)}$ — I_D



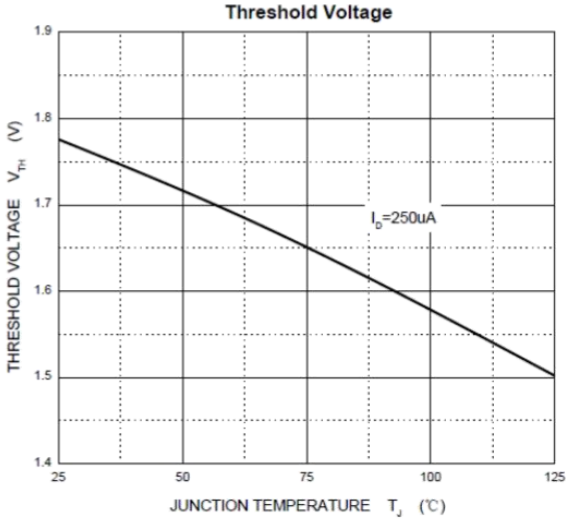
$R_{DS(ON)}$ — V_{GS}



I_S — V_{SD}

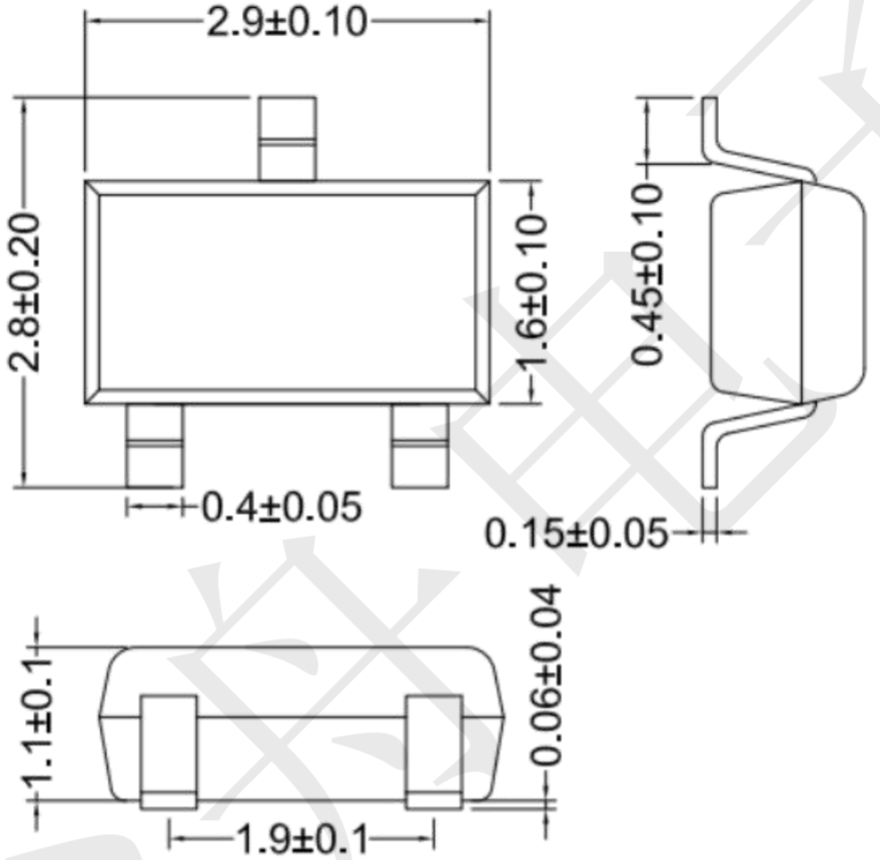


Threshold Voltage



Package Outline Dimensions (unit: mm)

SOT-23-3L



Mounting Pad Layout (unit: mm)

