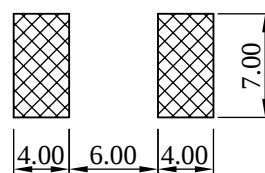
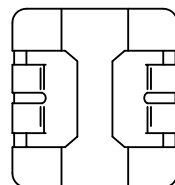
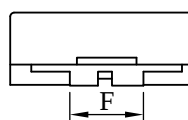
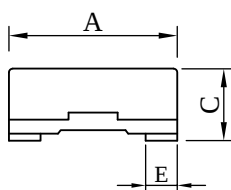
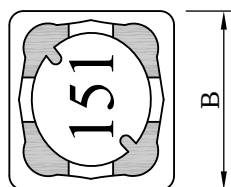


SPECIFICATION FOR APPROVAL

REF :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.		ASS1206151K2B	
		REV.	20140319-C	PAGE	1

I . Configuration and dimensions :



(PCB Pattern)

Unit : m/m

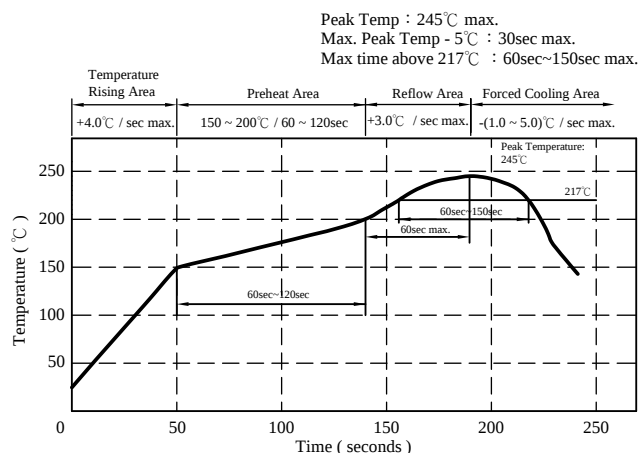
A	B	C	E	F
12.70 ±0.3	12.70 ±0.3	6.00 ±0.5	2.30 ±0.2	5.00 ±0.2

II . Description :

- a . Ferrite drum core construction.
- b . Magnetically shielded.
- c . Enamelled copper wire : H class
- d . Product weight : 3.10 g (ref.)
- e . Moisture sensitivity Level 1
- f . Products comply with RoHS' requirements
- g . Halogen free available.

III . General specification :

- a . Storage temp. : -40°C ----+125°C
- b . Operating temp. : -40°C ----+155°C
(Temp. rise included)
- c . Resistance to solder heat : 245°C .10 secs.
- d . Qualification to AEC-Q200 , available for automotive application on driver assistant , entertainment & lighting system.



AR-001C


千如電子集團
 ABC ELECTRONICS GROUP

SPECIFICATION FOR APPROVAL

REF :

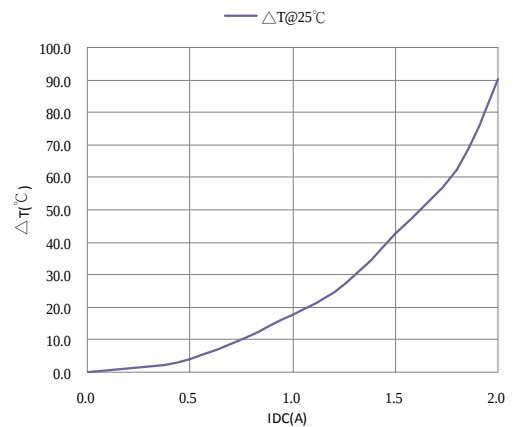
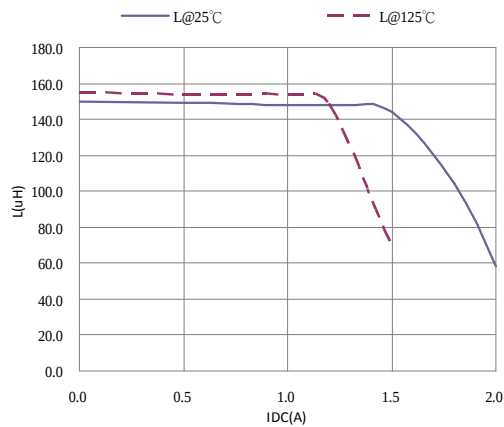
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	ASS1206151K2B		
		REV.	20140319-C	PAGE	2

IV . Electrical characteristics :

KEY

DWG No.	Inductance (μ H)	Test Freq. (Hz)	RDC ($m\Omega$) max	IDC (A) max
ASS1206151K2B	150.0 $\pm$ 10%	1K	320.0	0.90

- 1). Electrical specifications at 25°C
- 2). Inductance Test Freq. : 1KHz /1V
- 3). IDC base on Temp. rise 40°C max. & ΔL / L0A=10% max.



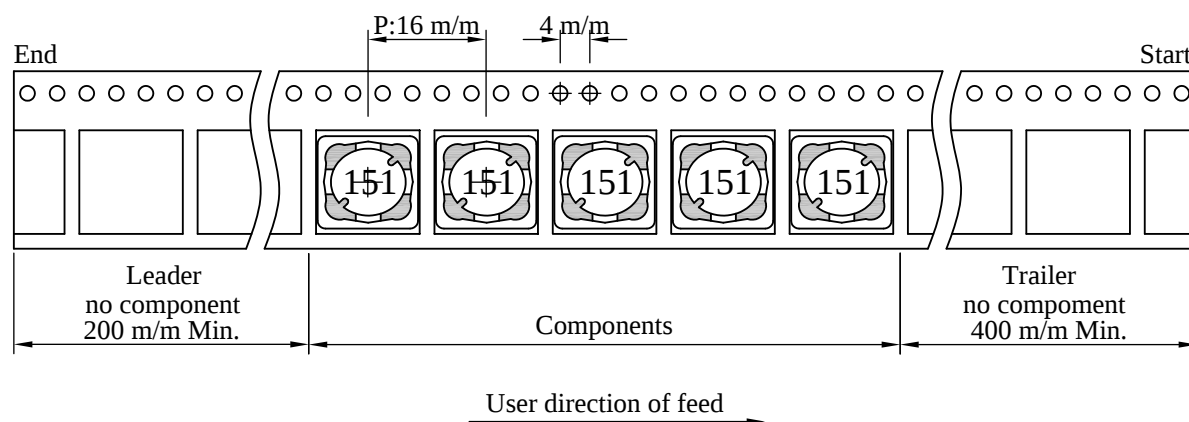
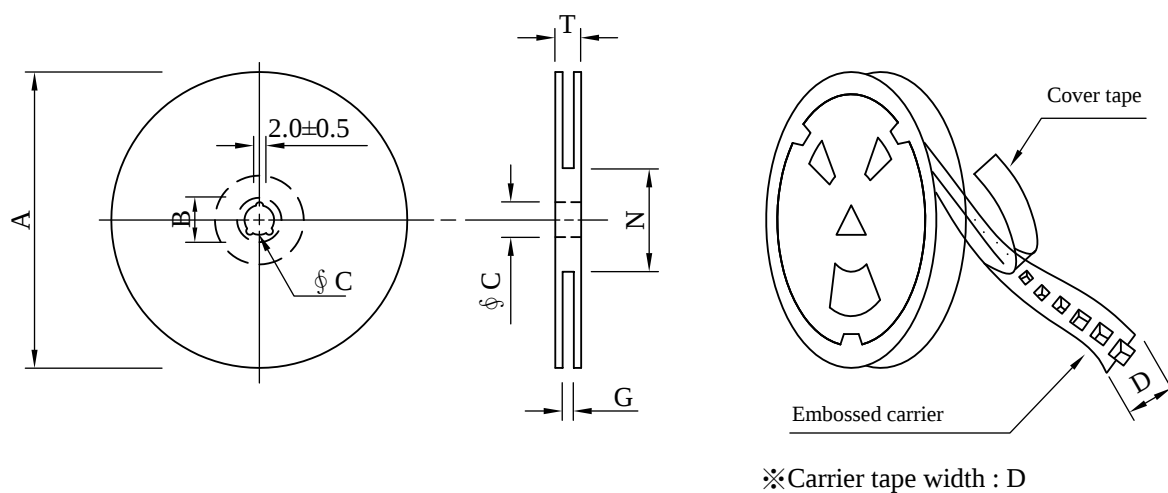
SPECIFICATION FOR APPROVAL

REF :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.		ASS1206151K2B	
		REV.	20140319-C	PAGE	3

V . Packaging information :

(1) Configuration



(2) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
13 - 24	330	21±0.8	13	24	26 ⁺⁰	50 ⁻⁰	30.4

(3) Q'TY & G.W. Per package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (Kg)	Size (cm)
B	600	2,200	13 - 24	2,400	11.0	38 x 37 x 22

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SPECIFICATION FOR APPROVAL

REF :

PROD.	Shielded SMD Power Inductor	ABC'S DWG NO.	ASS1206151K2B		
NAME		REV.	20140319-C	PAGE	4

VI . Reliability test :

Item	Reference documents	AEC-Q200 Test Condition	Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125℃ 2.Time:1000 hours.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
2.Temperature Cycling	JESD22 Method JA-104	1.Temperature: -40℃ ~ 125℃ 2.Number of cycle:1000 cycle 3.Dwell time:30 minutes	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
3.Biased Humidity Test	MIL-STD-202 Method 204	1.Temperature:85±5 ℃ 2.Time:1000 Hours 3.Humidity: 85±5% RH.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
4.Operational Life	User Spec.	1.Temperature: 155℃ 2.Time:1000 hours. 3.Rated current : 0.90A	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
5.Exeternal Visual	MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22 Method JB-100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for cycles.	1.No body change in apperarence. 2.No marking blurred. 3.Inductance shall not change more than ±20%.
8.Mechanical Shock	MIL-STD-202 Method 213	1.Peak acceleration100g`s 2.Duration of pulse: 6ms 3.Waveform : Half-sine 4.Velocity change: 12.3ft/sec 5.Direction : X, Y, Z (3axes / 3times)	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
9.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitud : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
10.Resistance To Soldering Heat Test	MIL-STD-202 Method 210	1.Highest temperature : 245±5℃. 2.Time (temp. ≥ 217℃) : 60~150 Second. 3.IR reflow times : 3 times.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
11.ESD	AEC-Q200-002 or ISO/DIS 10605	1.DSE Voltage : 15kV 2.Mode 1 : 150 pF / 330 Ω 3.Mode 2 : 150 pF / 2000 Ω 4.Discharge times and polarity : 3 times pos. / 3 times eng. for each condition	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
12.Solderability Test	J-STD-002	1.Baking in pre-testing : 155±5℃ / 16Hours±30 min. 2.Peak temperature : 240±5℃ 3.Time (temp. ≥ 217℃) : 60~150 second. 4.IR reflow times : 1 times.	The terminal shall be at least 95% covered with fresh solder.
13.Electrical Characteriazation	User Spec.	1.Operating temperature : -40℃~155℃ 2.Room temperature : 25℃.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
14.Flammability			
15.Board Flex	JIS-C-6429	1.Deflection speed : 1 mm/ sec 2.Amount of deflection : 2 mm 3.Span : 90 mm 4.Direction for test : Bottom of PCB 5.Holding time : 60 sec	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±20%.
16.Terminal Strength Test	JIS-C-6429	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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