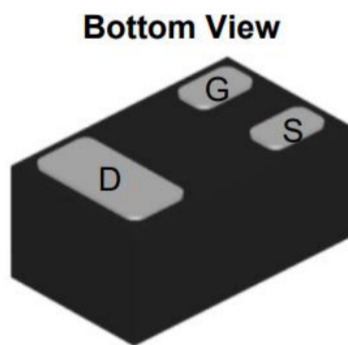


Product Summary

- V_{DS} 20 V
- I_{DS} ($V_{GS}=4.5V$) 2.5 A
- $R_{DS(ON)}$ ($V_{GS}=4.5V$) $\leq 100m\Omega$

Package and Pin Configuration

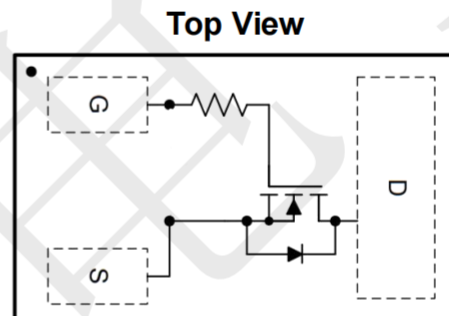


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Application

- Interfacing Switching
- DC-DC Converters
- Power management functions

Circuit diagram



Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current $T_A=25^\circ C$	I_D	2.5	A
Continuous Drain Current $T_A=70^\circ C$	I_D	1.8	A
Pulsed Drain Current ($t = 100 \mu s$)	I_{DM}	10	A
Maximum Power Dissipation $T_A=25^\circ C$	P_D	0.715	W
Operating Junction Temperature Range	T_J	-55 to +150	$^\circ C$
Storage Temperature Range	T_{stg}	-55 to +150	$^\circ C$

Thermal Characteristic

PARAMETER	Symbol	Value	Unit
Thermal Resistance from Junction to Ambient($t \leq 10s$)	$R_{\theta JA}$	175	$^\circ C/W$

Note : When mounted on 1" square PCB (FR4 material).

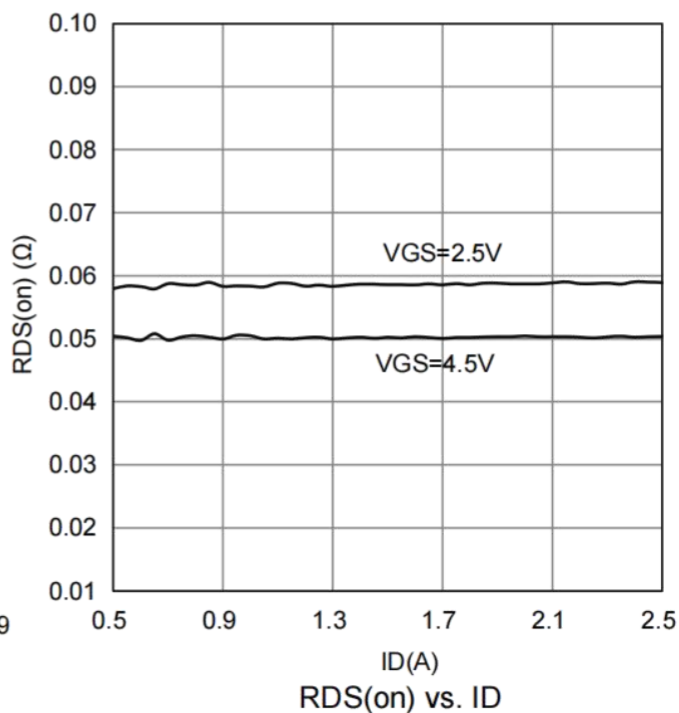
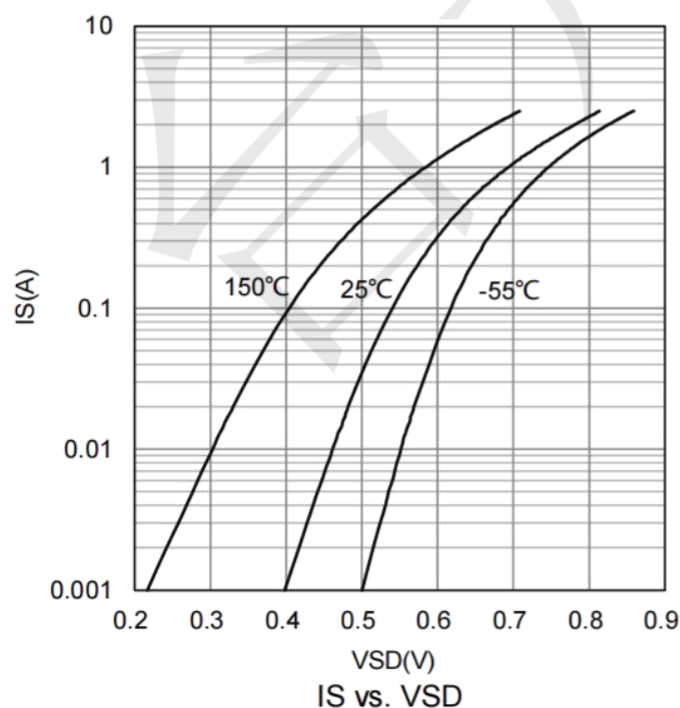
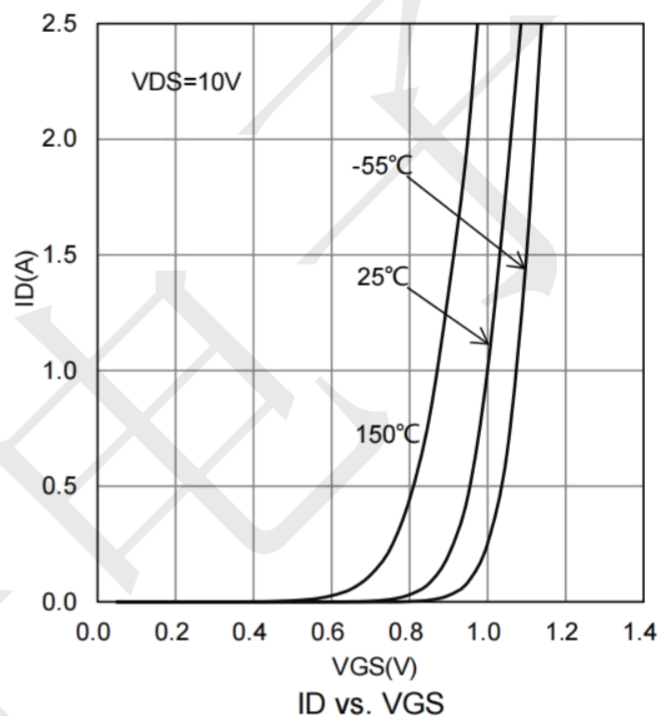
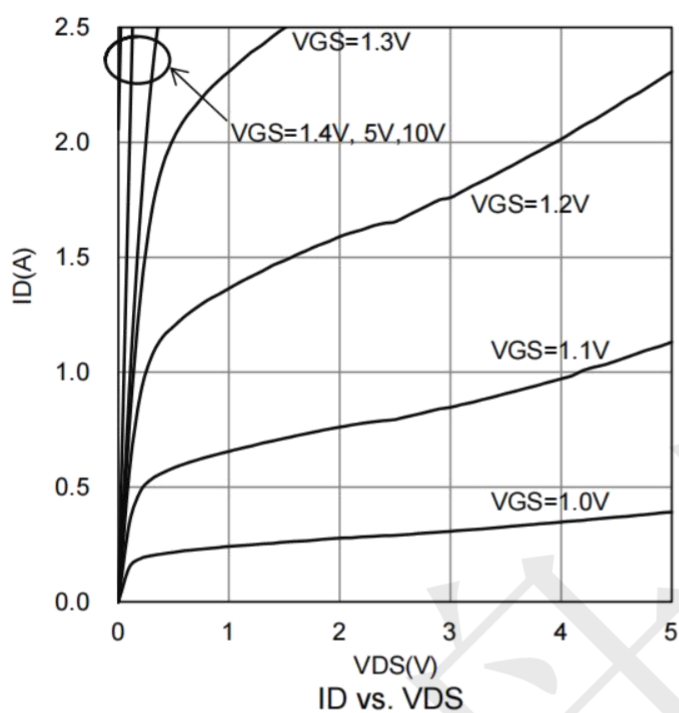
Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Characteristics						
Drain-Source Breakdown Voltage	V _{GS} =0V, I _D = 10μA	BV _{DSS}	20	--	--	V
Gate-Source Threshold Voltage	V _{DS} =V _{GS} , I _D = 250μA	V _{GS(th)}	0.45	0.75	0.9	V
Gate-Source Leakage	V _{DS} =0V, V _{GS} = ±12V	I _{GSS}	--	--	±100	nA
Zero Gate Voltage Drain Current	V _{DS} = 20V, V _{GS} =0V	I _{DSS}	--	--	1.0	μA
Drain-Source On-State Resistance (Note 1)	V _{GS} = 4.5V, I _D =1A	R _{DS(on)}	--	88	100	mΩ
	V _{GS} = 2.5V, I _D =1A		--	95	110	
Forward Transconductance (Note 2)	V _{DS} = 2V, I _D =1A	g _{fs}	--	6	--	S
Dynamic (Note 2)						
Input Capacitance	V _{DS} = 10V, V _{GS} = 0V, F = 1.0MHz	C _{iss}	--	135	--	pF
Output Capacitance		C _{oss}	--	16	--	
Reverse Transfer Capacitance		C _{rss}	--	19	--	
Switching						
Turn-On Delay Time (Note 3)	V _{DS} = 10V, V _{GS} = 4.5V, I _D = 1A, R _G = 6Ω.	t _{d(on)}	--	9.0	--	nS
Rise Time (Note 3)		t _r	--	23	--	
Turn-Off Delay Time (Note 3)		t _{d(off)}	--	38	--	
Fall Time (Note 3)		t _f	--	3.0	--	
Total Gate Charge	V _{DS} = 10V, I _D = 1A, V _{GS} = 4.5V	Q _g	--	2.2	--	nC
Gate Source Charge		Q _{gs}	--	0.18	--	
Gate Drain Charge		Q _{gd}	--	0.6	--	
Source-Drain Diode Ratings and Characteristics (Note 2)						
Forward Voltage	V _{GS} = 0V, I _F = 1A	V _{SD}	--	0.8	1.3	V
Continuous Source Current	Integral reverse diode in the MOSFET	I _S	--	--	2.5	A
Pulsed Current (Note 1)		I _{SM}	--	--	10	A

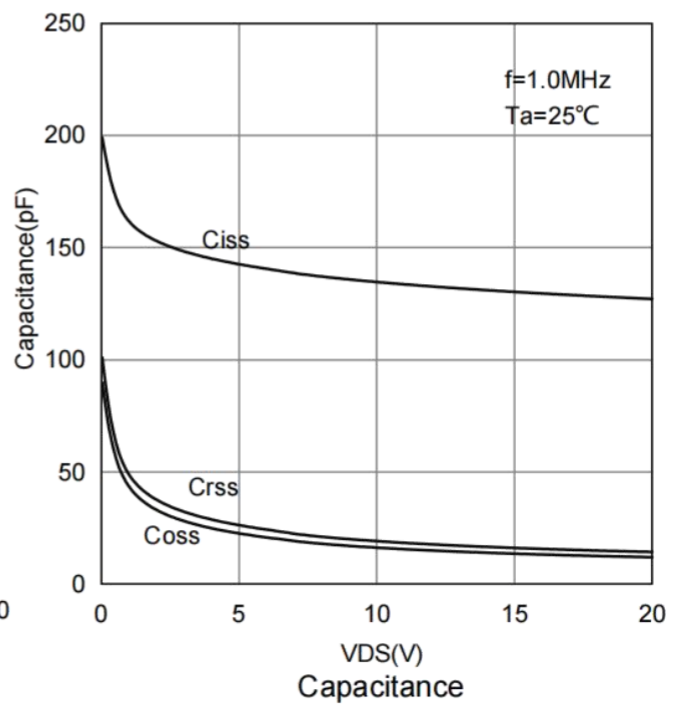
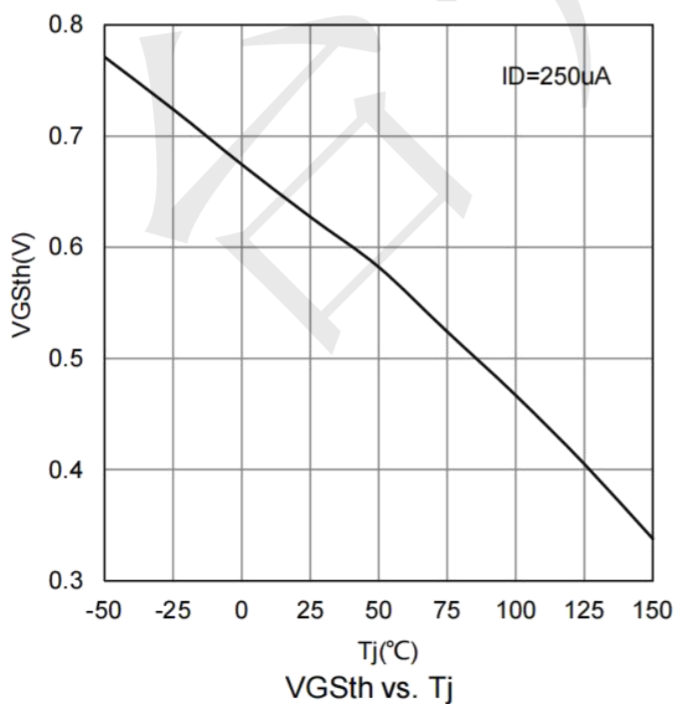
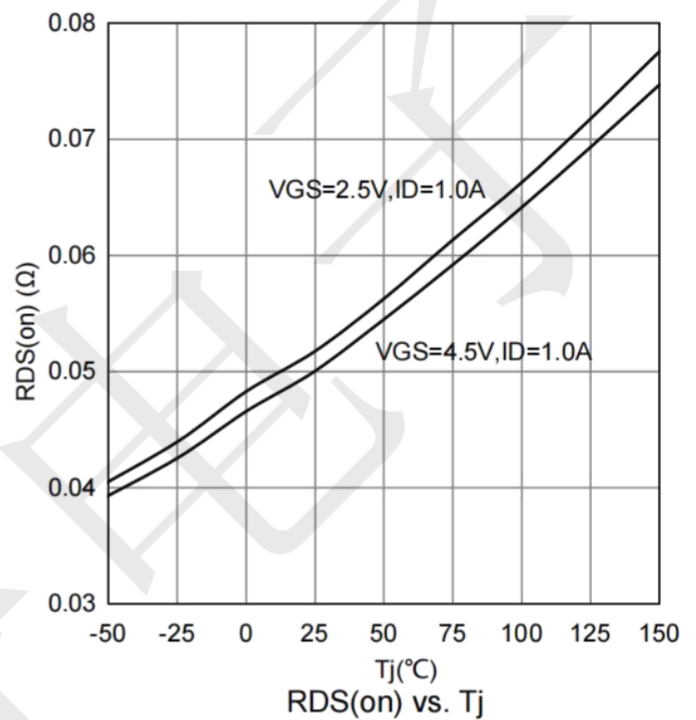
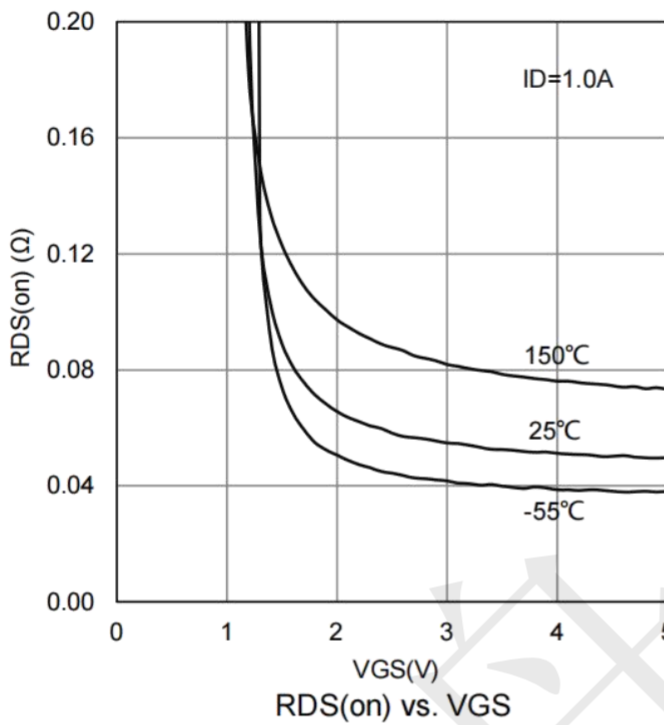
Notes:

1. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
2. Guaranteed by design, not subject to production testing.
3. Independent of operating temperature

TYPICAL CHARACTERISTICS

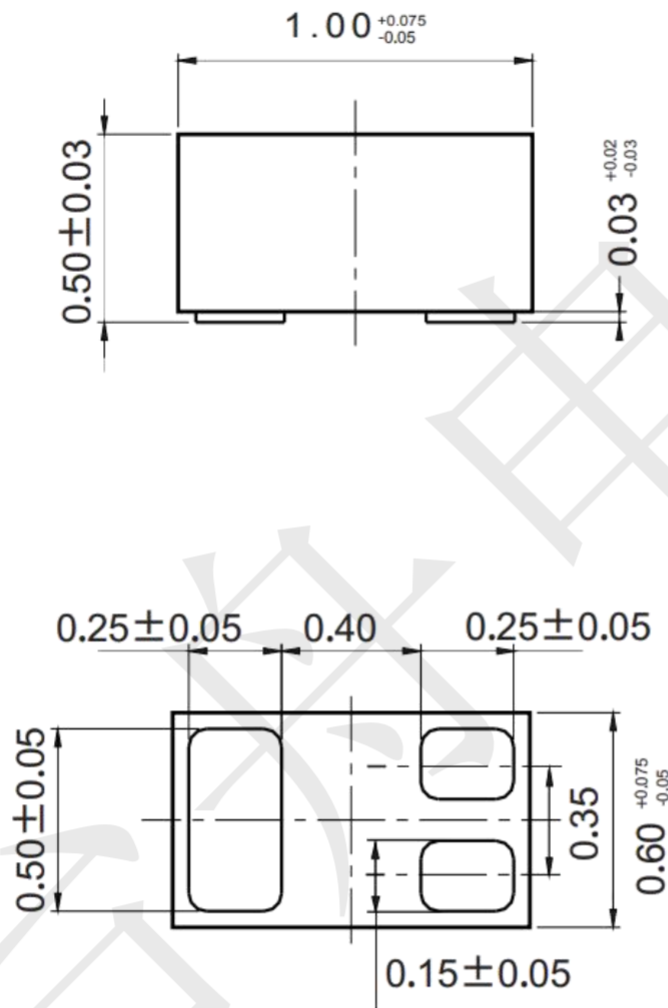


TYPICAL CHARACTERISTICS



Package Outline Dimensions (unit: mm)

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Mounting Pad Layout (unit: mm)

