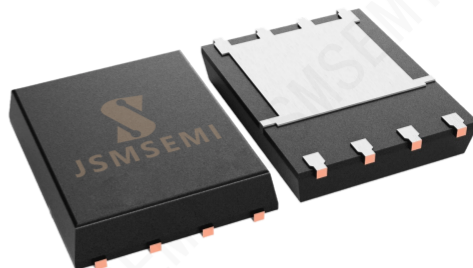


Product Summary

- V_{DS} 100V
- I_D 100A
- $R_{DS(ON)}$ (at $V_{GS}=10V$) $<4.75m\Omega$
- 100% EAS Tested
- 100% ∇V_{DS} Tested

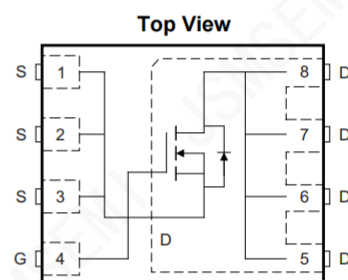


General Description

- Trench Power MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Power switching application
- Uninterruptible power supply
- DC-DC convertor
- Motor drivers



■ Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V_{DS}	100	V
Gate-source Voltage		V_{GS}	± 20	V
Drain Current	$T_C=25^\circ C$	I_D	100	A
	$T_C=100^\circ C$		80	
Pulsed Drain Current ^A		I_{DM}	400	A
Avalanche energy ^B		EAS	580	mJ
Total Power Dissipation ^C	$T_C=25^\circ C$	P_D	154	W
	$T_C=100^\circ C$		100	
Junction and Storage Temperature Range		T_J, T_{STG}	$-55 \sim +150$	$^\circ C$

■ Thermal resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient ^D	Steady-State	$R_{\theta JA}$	45		$^\circ C/W$
Thermal Resistance Junction-to-Case	Steady-State	$R_{\theta JC}$	1.0		

Ordering Information

Order number	Package	Marking	Operation Temperature Range	MSL Grade	Ship, Quantity	Green
CSD19532Q5B-JSM	DFN5060-8L	CSD19532	-55 to $150^\circ C$	1	T&R, 5000	RoHS

■ Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =250μA	100	-	-	V
		V _{GS} = 0V, I _D =1mA	100	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V	-	-	1	μA
		V _{DS} =100V, V _{GS} =0V, T _j =150°C	-	-	100	
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250μA	2.0	-	4.0	V
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D =20A	-	4.1	4.75	mΩ
Diode Forward Voltage	V _{SD}	I _S =50A, V _{GS} =0V	-	-	1.2	V
Gate resistance	R _G	f=1MHz	-	1.7	-	Ω
Maximum Body-Diode Continuous Current	I _S		-	-	100	A
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{DS} =50V, V _{GS} =0V, f=1MHz	-	3920	-	pF
Output Capacitance	C _{oss}		-	590	-	
Reverse Transfer Capacitance	C _{rss}		-	20	-	
Switching Parameters						
Total Gate Charge	Q _g	V _{GS} =10V, V _{DS} =80V, I _D =20A	-	59	-	nC
Gate-Source Charge	Q _{gs}		-	18	-	
Gate-Drain Charge	Q _{gd}		-	15	-	
Reverse Recovery Charge	Q _{rr}	I _F =1A, di/dt=100A/us	-	53	-	nC
Reverse Recovery Time	t _{rr}		-	55	-	ns
Turn-on Delay Time	t _{D(on)}	V _{GS} =10V, V _{DD} =50V, I _D =50A R _{GEN} =10Ω	-	24	-	ns
Turn-on Rise Time	t _r		-	105	-	
Turn-off Delay Time	t _{D(off)}		-	57	-	
Turn-off fall Time	t _f		-	74	-	

A. Repetitive rating; pulse width limited by max. junction temperature.

 B. $T_J=25^{\circ}\text{C}$, $V_G=10\text{V}$, $R_G=25\Omega$, $L=0.5\text{mH}$

 C. P_d is based on max. junction temperature, using junction-case and junction-ambient thermal resistance.

■ Typical Electrical and Thermal Characteristics Diagrams

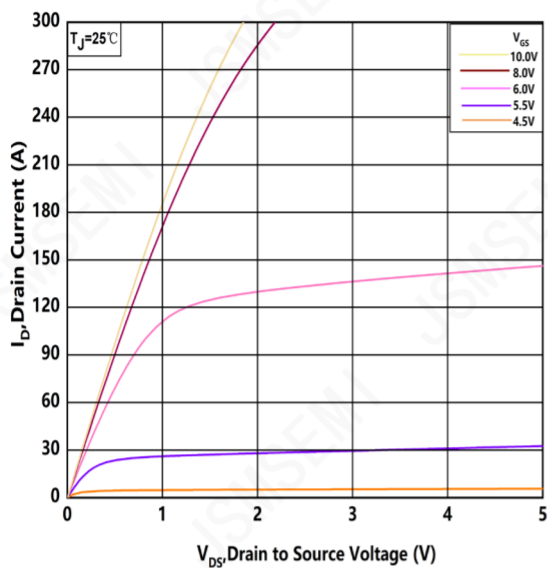


Figure.1 Output Characteristics $T_J = 25^\circ\text{C}$

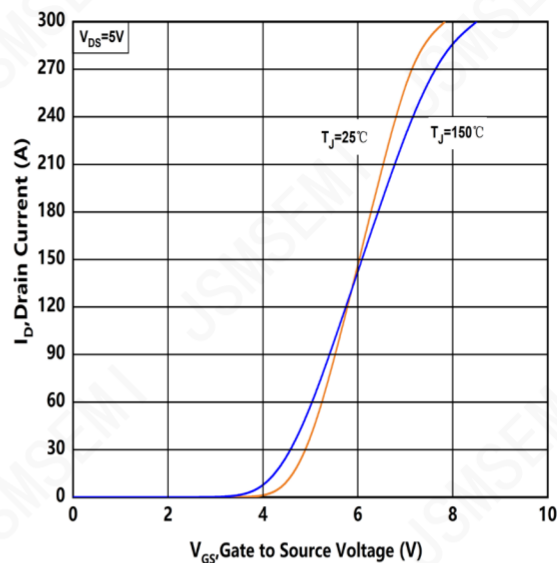


Figure.2 Transfer Characteristic for Various Junction Temperatures

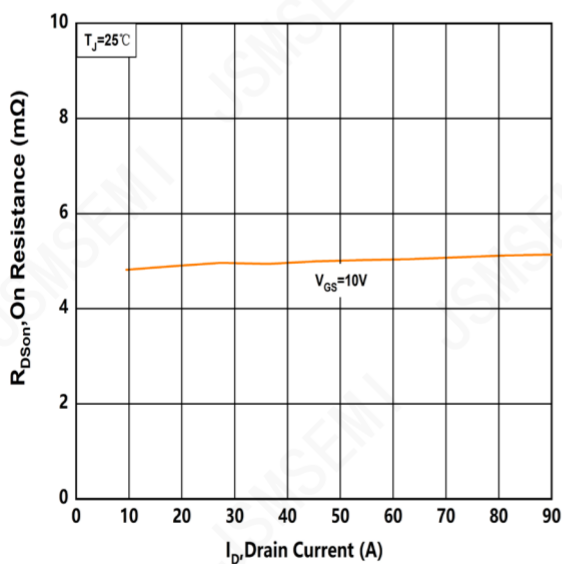


Figure.3 On-Resistance vs Drain Current For Various Gate Voltage

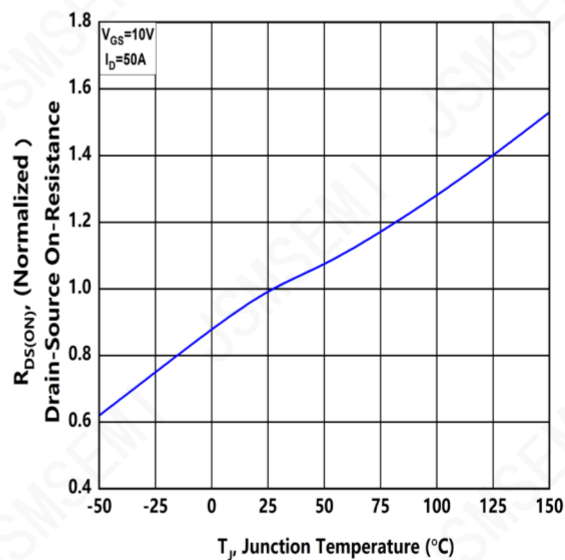


Figure.4 Typical On-Resistance vs Junction Temperature

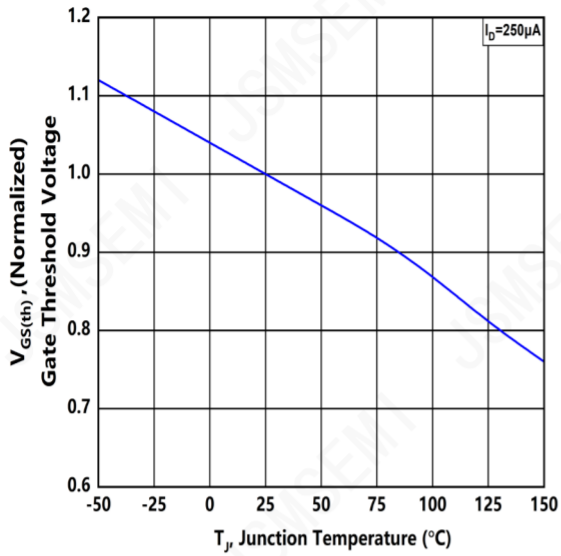


Figure.5 Typical Threshold Voltage vs Junction Temperature

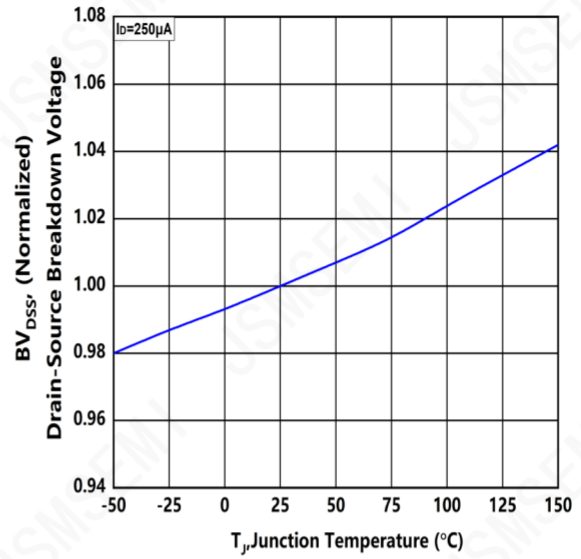


Figure.6 Typical Breakdown Voltage vs Junction Temperature

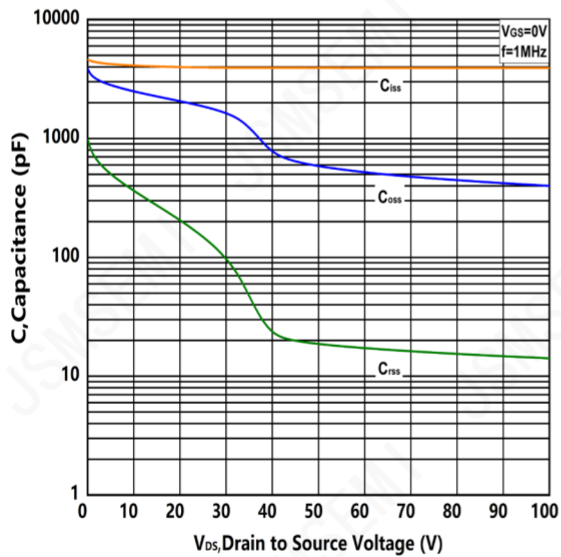


Figure.7 Typical Capacitance vs Drain to Source Voltage

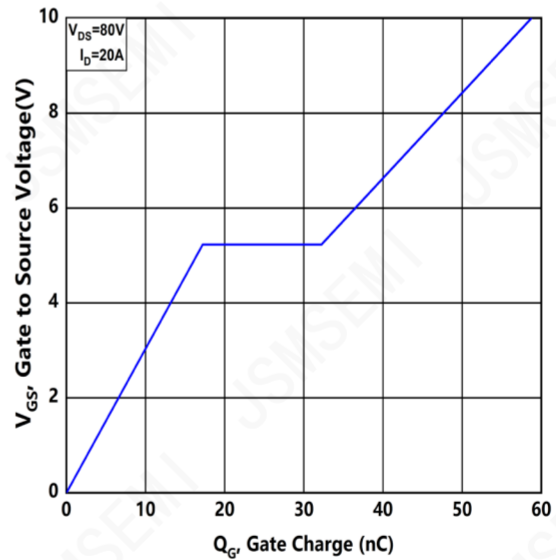


Figure.8 Typical Gate Charge vs Gate to Source Voltage

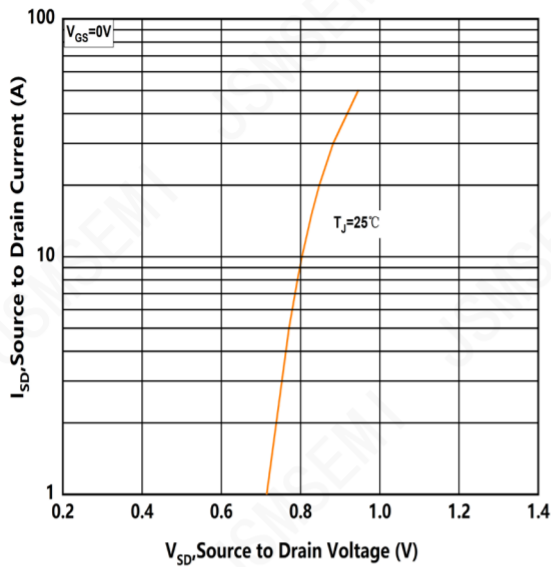


Figure.9 Typical Body Diode Characteristics

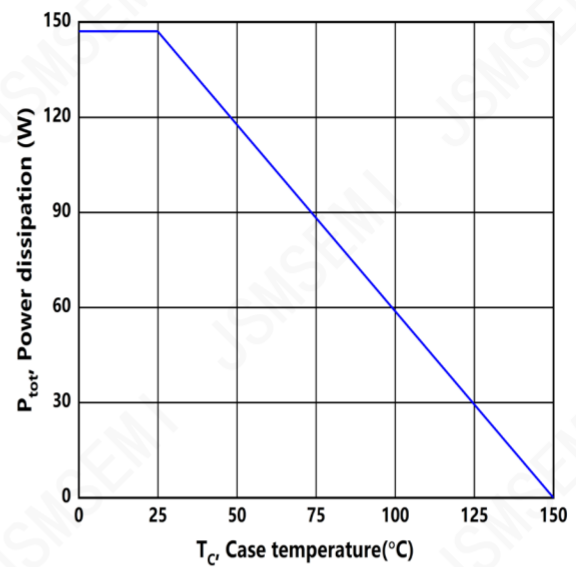


Figure.10 Maximum power Dissipation Derating vs Case Temperature

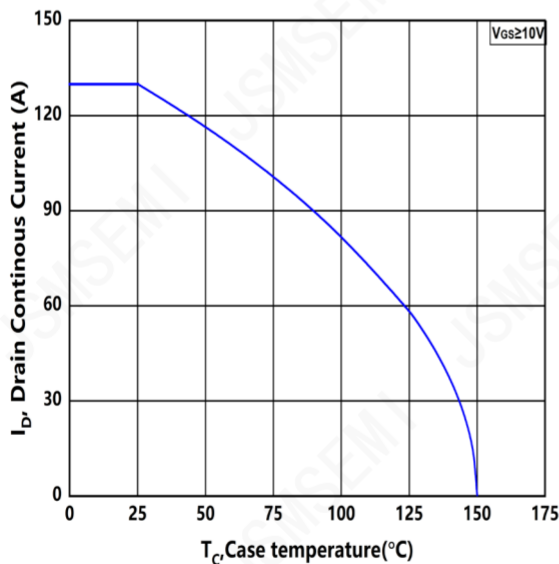


Figure.11 Continuous Drain Current Derating vs Case Temperature

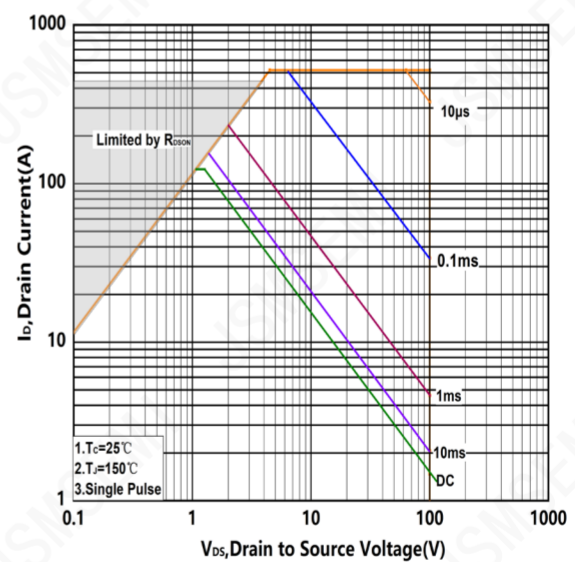


Figure.12 Safe Operating Area

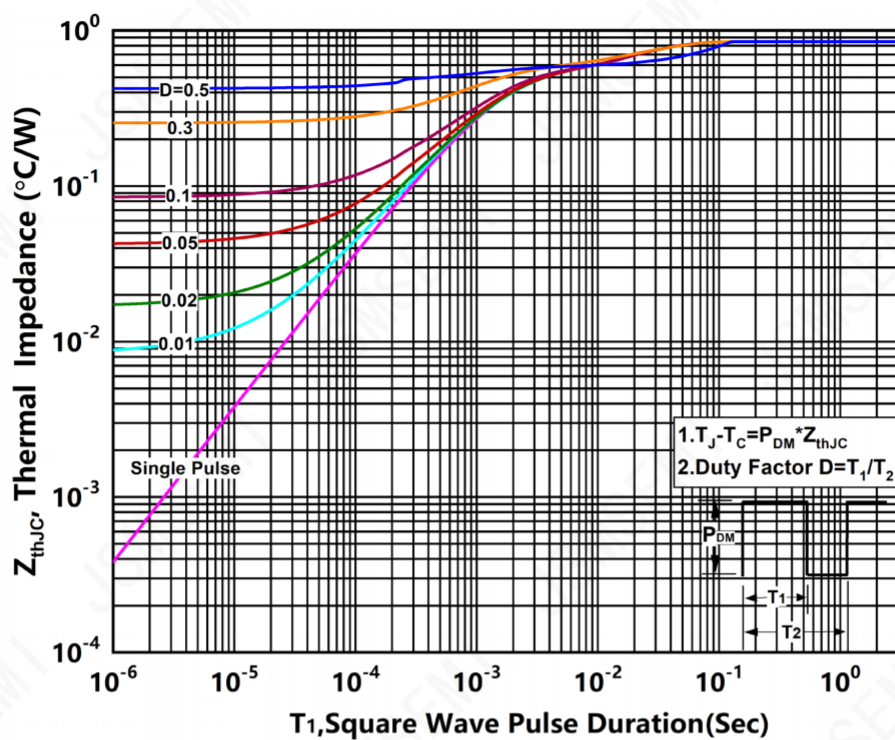
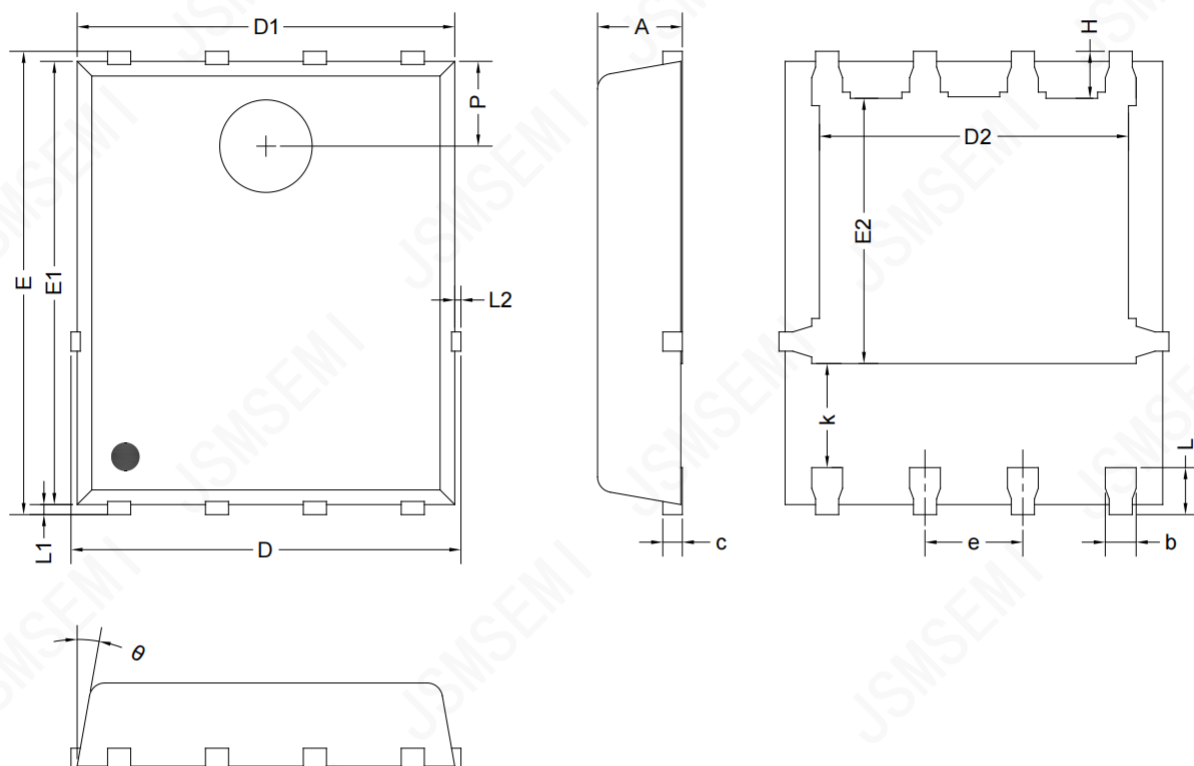


Figure.13 Transient Thermal Impedance (Junction - Case)

Package Information

DFN5060-8L



Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	1.000	1.100	1.200
b	0.350	0.400	0.450
c	0.210	0.250	0.340
D	4.800	-	5.100
D1	4.800	4.900	5.000
D2	3.910	4.010	4.110
E	5.900	6.000	6.100
E1	5.700	5.750	5.800
E2	3.340	3.440	3.540
e	1.270 BSC		
H	0.510	0.610	0.710
k	1.100	-	-
L	0.510	0.610	0.710
L1	0.060	0.130	0.200
L2	-	-	0.100
P	1.000	1.100	1.200
θ	8°	10°	12°

NOTE: This drawing is subject to change without notice.

Revision History

Rev.	Change	Date
V1.0	Initial version	6/27/2021

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