

SOP5, 1Mbit/s High Speed Transistor Photo Coupler**Description**

The TWSM501 series combine an AlGaAs infrared emitting diode as the emitter which is optically coupled to a silicon high speed photo transistor in a plastic SOP5 package.

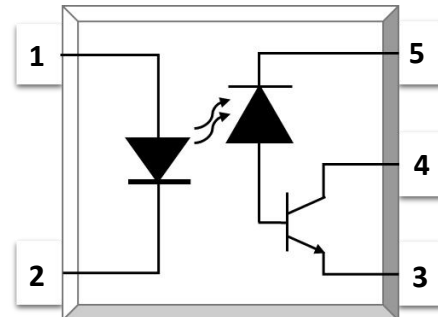
With the robust coplanar double mold structure, TWSM501 series provide the most stable isolation feature.

Features

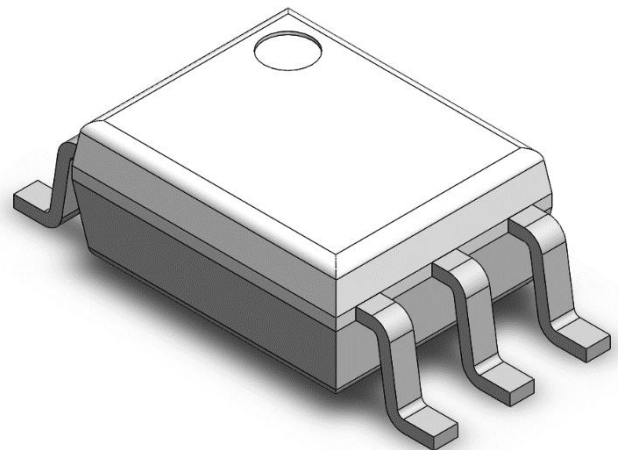
- High isolation 3750 VRMS
- DC input with high speed transistor
- Operating temperature range - 40 °C to 100 °C
- REACH compliance
- Halogen free
- MSL class 1
- Regulatory Approvals
 - UL - UL1577
 - VDE - EN60747-5-5(VDE0884-5)
 - CQC - GB4943.1, GB8898

Applications

- Line receivers
- Telecommunication equipment
- Out interface to CMOS-LSTTL-TTL
- Wide bandwidth analog coupling
- Pulse transformer replacement
- Computer-peripheral interface

SCHEMATIC**PIN DEFINITION**

1.Anode	5.VCC
2.Cathod	4.VO
	3.GND

PACKAGE OUTLINE

SOP5, 1Mbit/s High Speed Transistor Photo Coupler**ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	VALUE	UNIT	Note
INPUT				
Forward Current	I_F	25	mA	
Peak Forward Current	I_{FP}	50	mA	1
Peak Transient Current	$I_{F(trans)}$	1	A	2
Reverse Voltage	V_R	5	V	
Input Power Dissipation	P_I	100	mW	
OUTPUT				
Supply Voltage	V_{CC}	-0.5~30	V	
Output Voltage	V_O	-0.5~20	V	
Output Current	I_O	8	mA	
Peak Output Current	I_O	16	mA	
Output Power Dissipation	P_O	100	mW	
COMMON				
Total Power Dissipation	P_{tot}	200	mW	
Isolation Voltage	V_{iso}	3750	Vrms	3
Operating Temperature	T_{opr}	-40~100	°C	
Storage Temperature	T_{stg}	-55~125	°C	
Soldering Temperature	T_{sol}	260	°C	4

Note 1. 50% duty, 1ms P.W

Note 2. $\leq 1\mu s$ P.W, 300pps

Note 3. AC For 1 Minute, R.H. = 40 ~ 60%

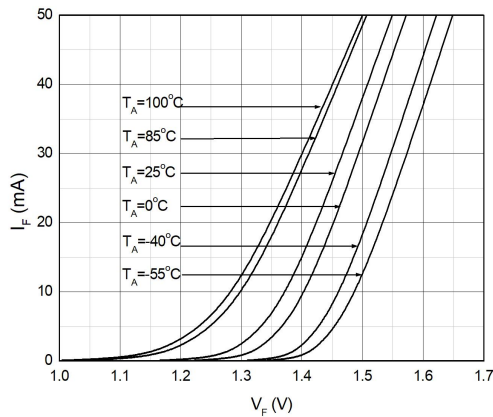
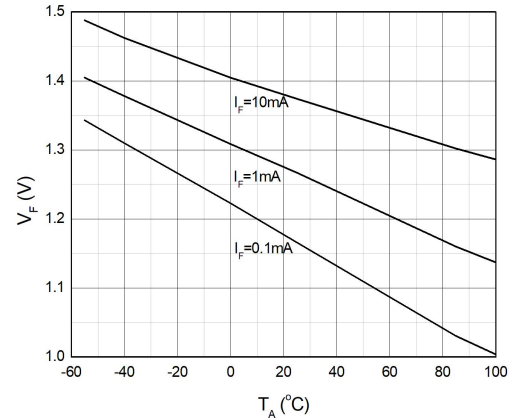
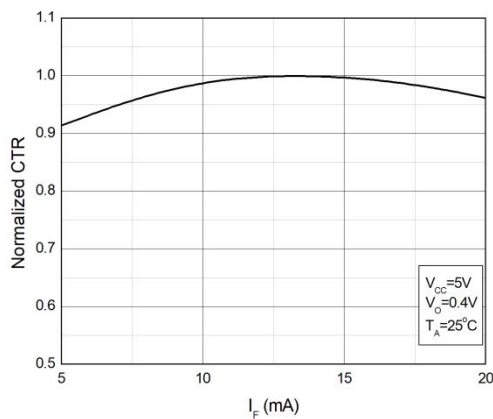
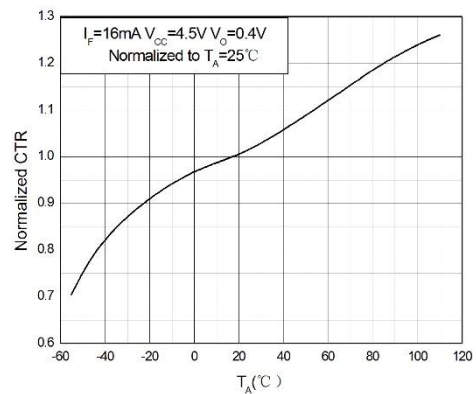
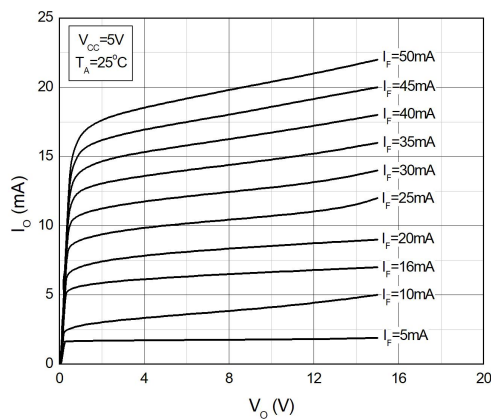
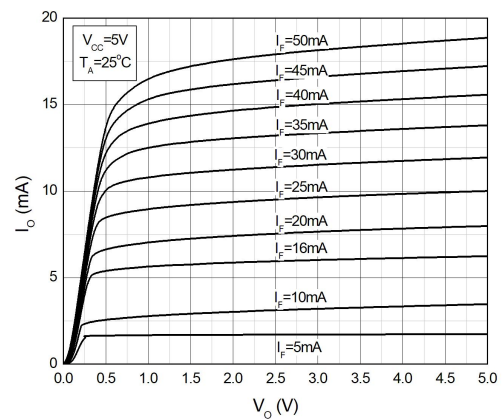
Note 4. For 10 seconds

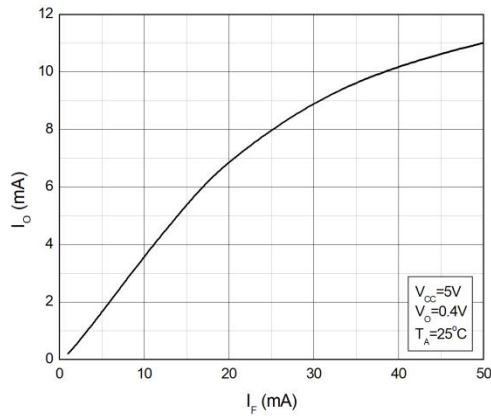
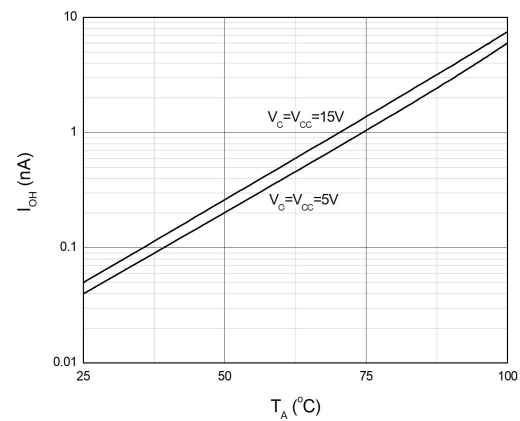
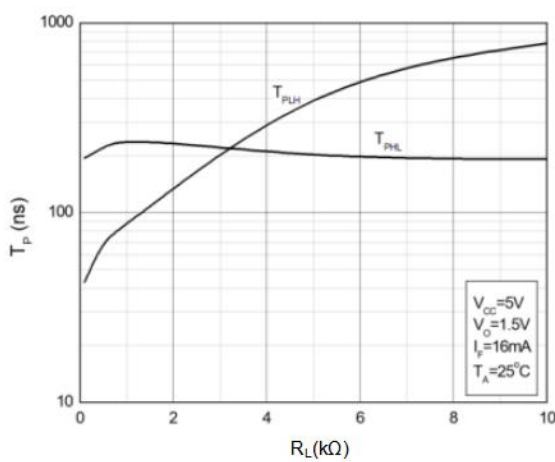
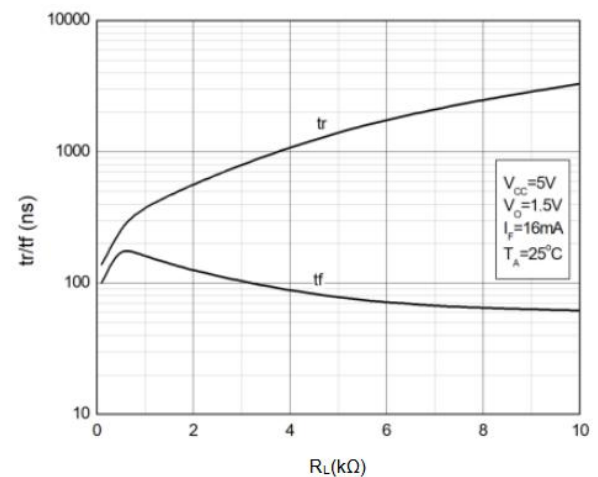
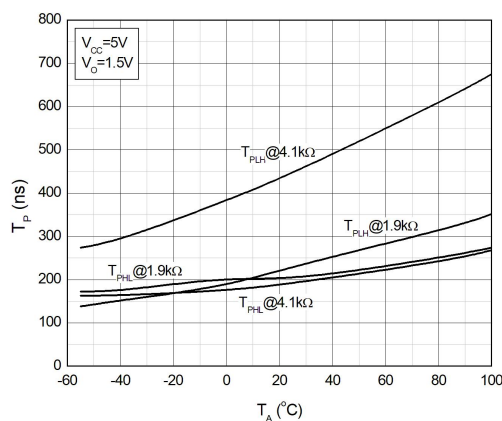
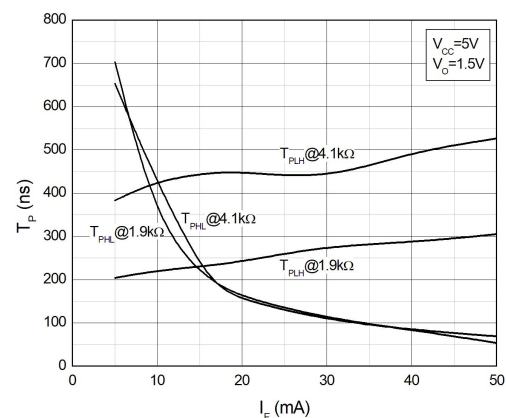
SOP5, 1Mbit/s High Speed Transistor Photo Coupler**ELECTRICAL OPTICAL CHARACTERISTICS at Ta=25°C**

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
INPUT							
Forward Voltage	V _F	-	1.45	1.8	V	I _F =16mA	
Reverse Current	I _R	-	-	10	μA	V _R =5V	
Input Capacitance	C _{in}	-	60	-	pF	V=0, f=1MHz	
OUTPUT							
High Level Supply Current	I _{CCH}	-	0.01	1	μA	I _F =0mA, V _O =Open, V _{CC} =15V, Ta=25°C	
		-	-	2	μA	I _F =0mA, V _O =Open, V _{CC} =15V	
Low Level Supply Current	I _{CCL}	-	200	-	μA	I _F =16mA, V _O =Open, V _{CC} =15V	
Logic High Output Current	I _{OH}	-	0.001	0.5	μA	I _F =0mA, V _O =V _{CC} =5.5V, Ta=25°C	
		-	0.01	1	μA	I _F =0mA, V _O =V _{CC} =15V, Ta=25°C	
		-	-	50	μA	I _F =0mA, V _O =V _{CC} =15V	
TRANSFER CHARACTERISTICS(at Ta=0 to 70°C , unless specified otherwise)							
Current Transfer Ratio	CTR	20	-	-	%	I _F = 16mA ,V _O = 0.4V, V _{CC} =4.5V, Ta=25°C	
		15	-	-		I _F = 16mA ,V _O = 0.5V, V _{CC} =4.5V	
Logic Low Output Voltage	V _{OL}	-	-	0.4	V	I _F = 16mA ,I _O = 3mA, V _{CC} =4.5V, Ta=25°C	
		-	-	0.5		I _F = 16mA ,I _O = 2.4mA, V _{CC} =4.5V	
Isolation Resistance	R _{iso}	10^12	10^14	-	Ω	DC500V, 40 ~ 60% R.H.	
Floating Capacitance	C _{IO}	-	0.3	-	pF	V=0, f=1MHz	

SOP5, 1Mbit/s High Speed Transistor Photo Coupler**ELECTRICAL OPTICAL CHARACTERISTICS**

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
SWITCHING CHARACTERISTICS(at Ta=0 to 70°C, I _F =16mA, V _{CC} =5V, unless specified otherwise)							
Propagation Delay Time to Logic Low	TPHL	-	0.4	0.8	μs	R _L =1.9kΩ, T _A =25°C	Fig.13
		-	-	1.0		R _L =1.9kΩ	
Propagation Delay Time to Logic High	TPLH	-	0.35	0.8	μs	R _L =1.9kΩ, T _A =25°C	Fig.13
		-	-	1.0		R _L =1.9kΩ	
Common Mode Transient Immunity at Logic High	CM _H	15	-	-	kV/μs	I _F = 0mA, V _{CM} =1500Vpp, R _L =1.9kΩ, T _A =25°C	Fig.15
Common Mode Transient Immunity at Logic Low	CM _L	15	-	-	kV/μs	I _F = 16mA, V _{CM} =1500Vpp, R _L =1.9kΩ, T _A =25°C	Fig.15

CHARACTERISTIC CURVES**Fig.1 Forward Current
vs. Forward Voltage****Fig.2 Forward Voltage
vs. Ambient Temperature****Fig.3 Normalized CTR
vs. Forward Current****Fig.4 Normalized CTR
vs. Ambient Temperature****Fig.5 Output Current
vs. Output Voltage(0~15V)****Fig.6 Output Current
vs. Output Voltage(0~5V)**

CHARACTERISTIC CURVES**Fig.7 Output Current
vs. Forward Current****Fig.8 Logic High Output Current
vs. Ambient Temperature****Fig.9 Propagation Delay
vs. Load Resistance****Fig.10 Switching Time
vs. Load Resistance****Fig.11 Propagation Delay
vs. Ambient Temperature****Fig.12 Propagation Delay
vs. Forward Current**

TEST CIRCUITS

Fig.13 Test Circuits for TPHL, TPLH, tr, tf

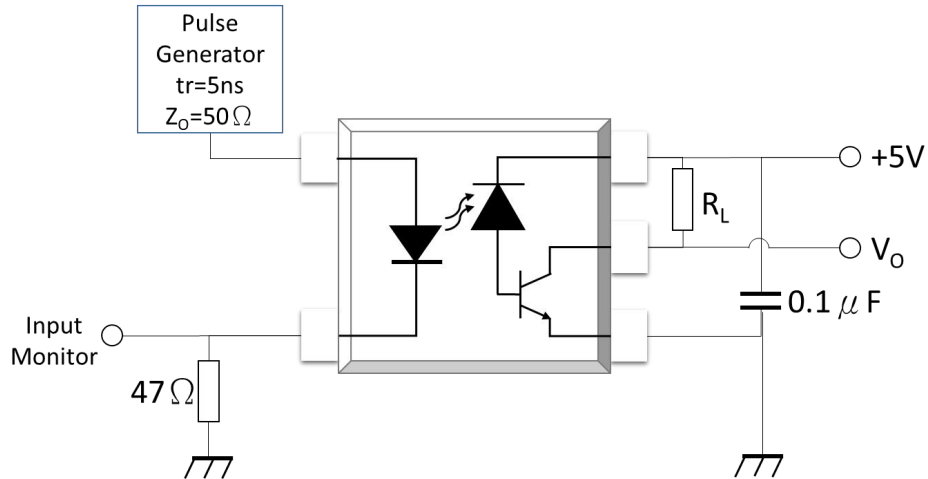
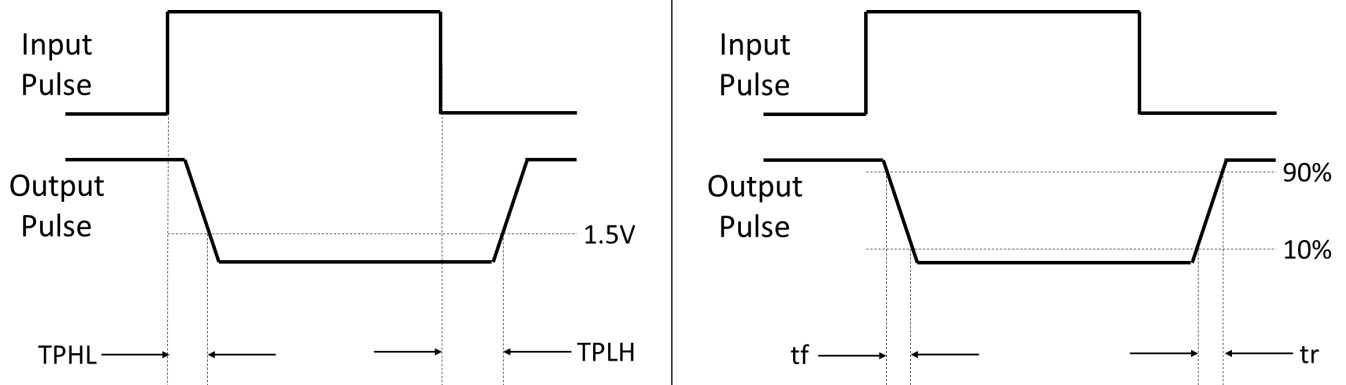


Fig.14 Waveforms of TPHL, TPLH, tr, tf



TEST CIRCUITS

Fig.15 Test Circuits for Common Mode Transient Immunity

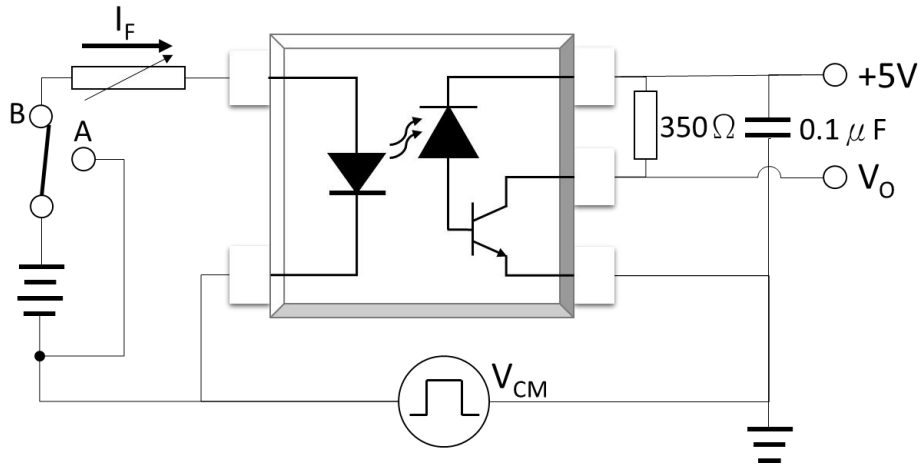
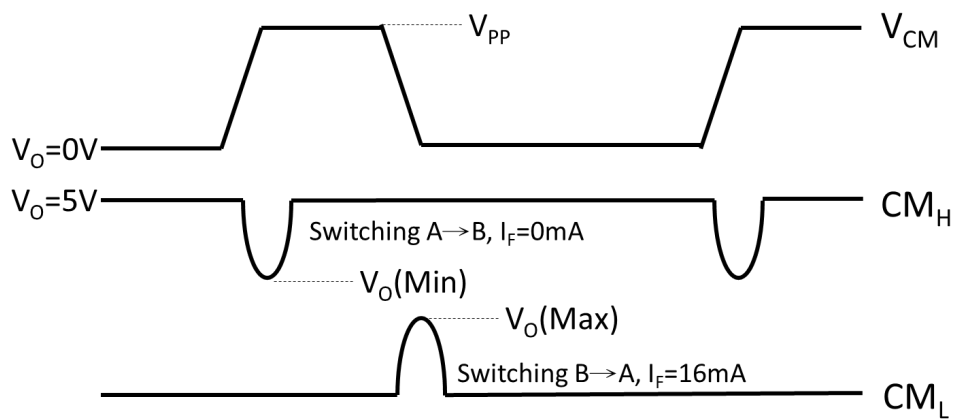
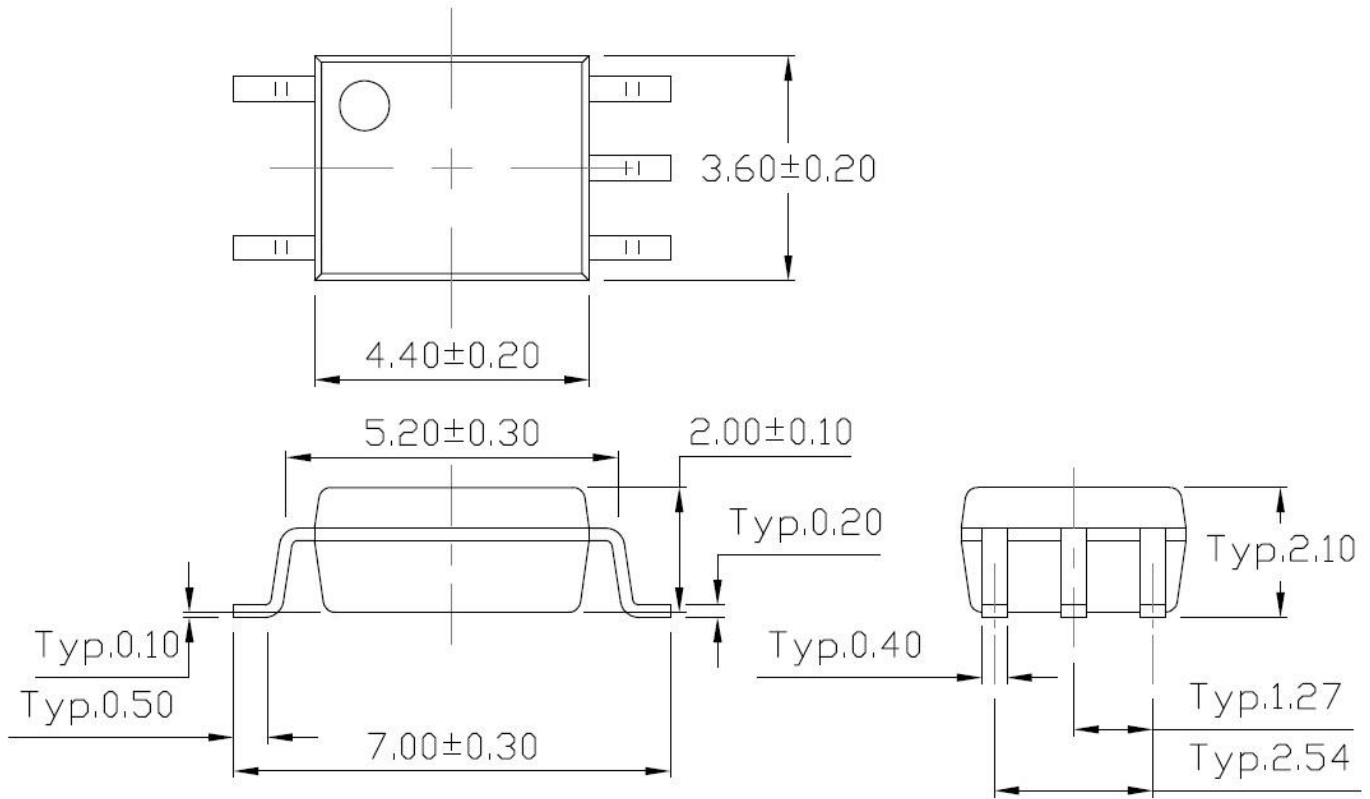


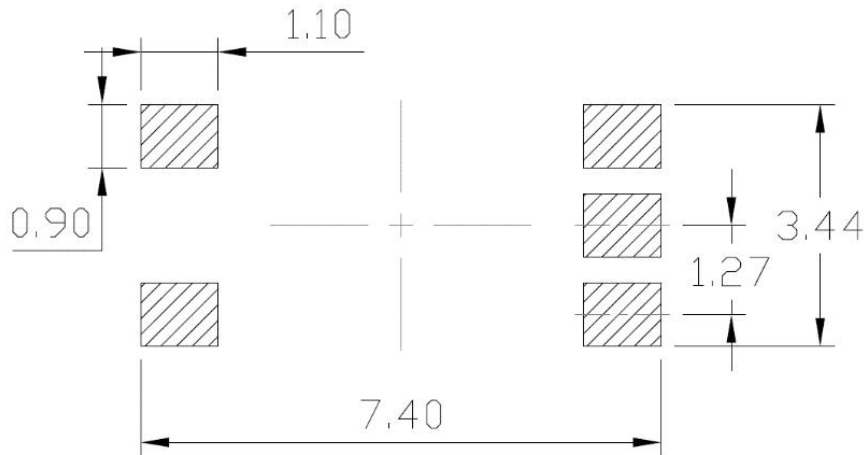
Fig.16 Wave forms of Common Mode Transient Immunity

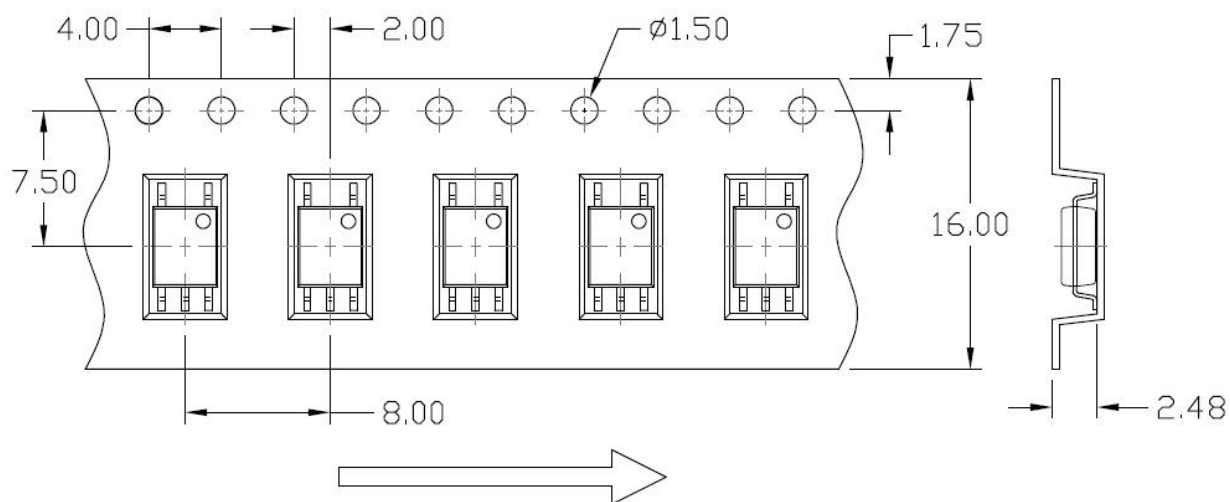
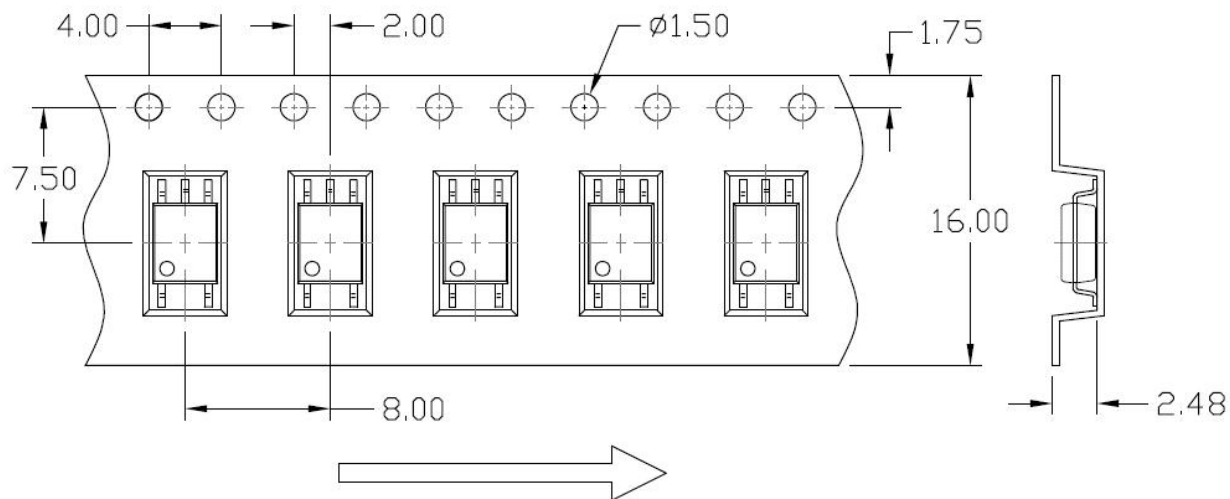


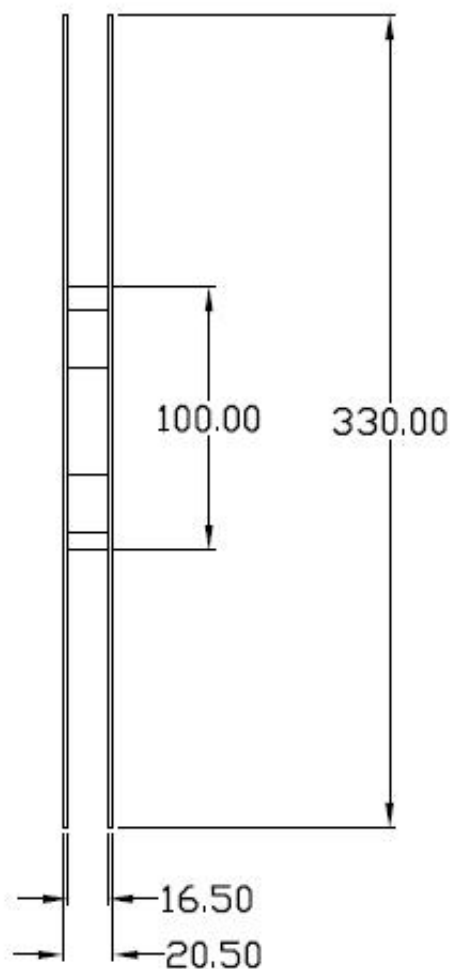
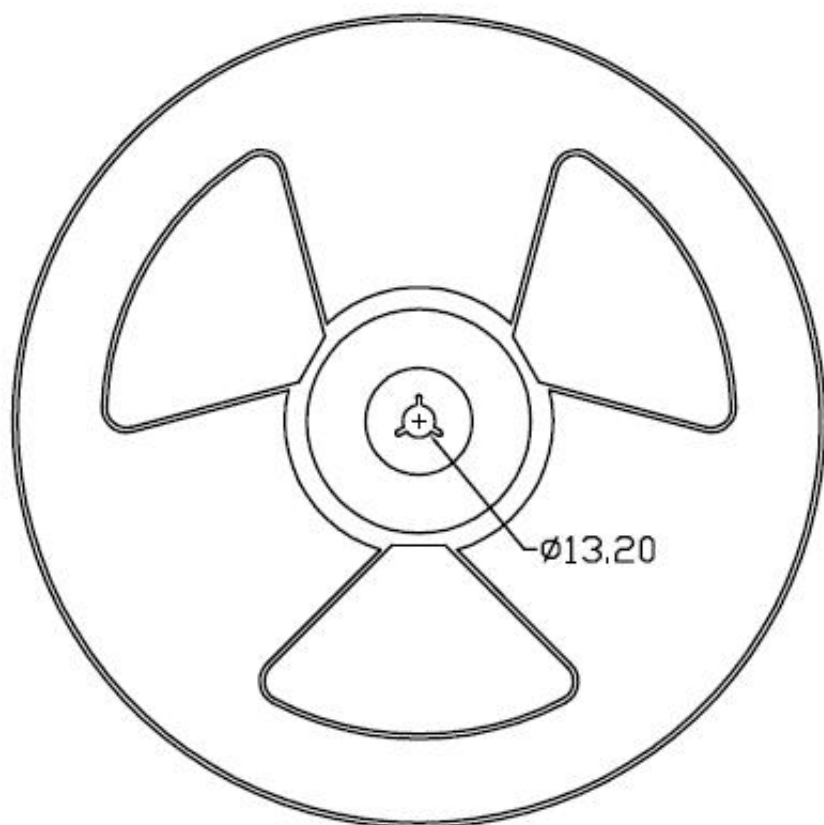
PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)



Recommended Solder Mask (Dimensions in mm unless otherwise stated)

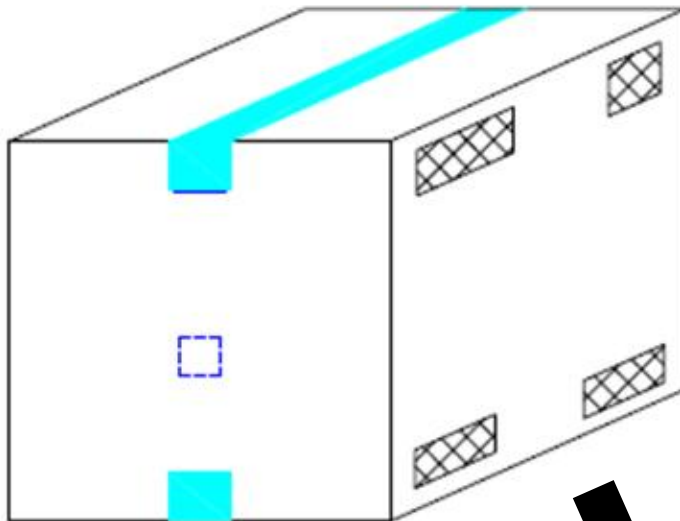


CARRIER TAPE SPECIFICATIONS (Dimensions in mm unless otherwise stated)**Option T1****Option T2**

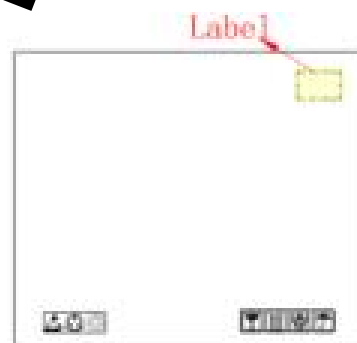
REEL SPECIFICATIONS (Dimensions in mm unless otherwise stated)**Option T1 & T2**

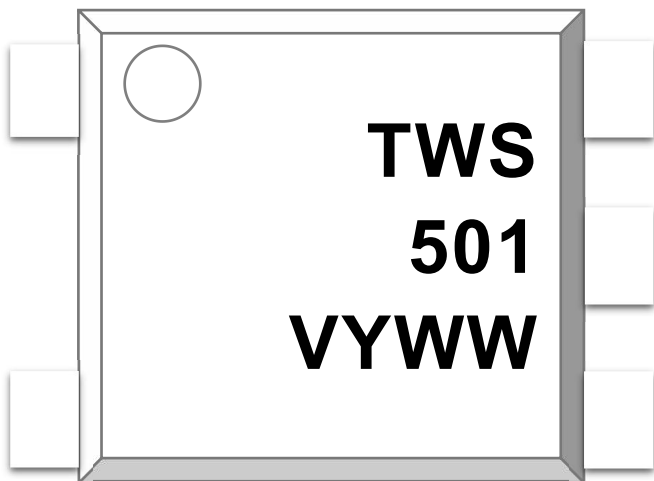
BOX SPECIFICATIONS (Reel Type)**Inner Box**

- L x W x H = 36cm x 36cm x 6.9cm

Outer Box

- Option1: L x W x H = 45cm x 38cm x 38cm
- Option2: L x W x H = 39cm x 38cm x 38cm

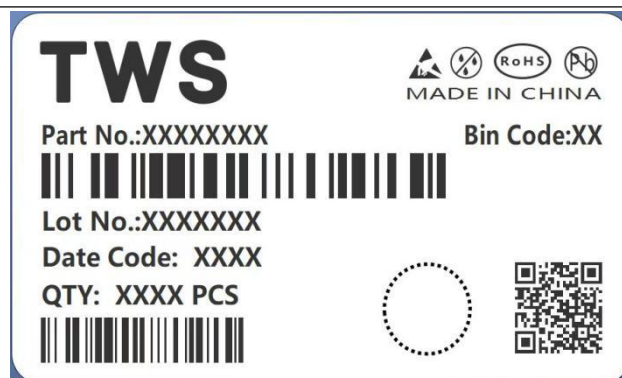


ORDERING AND MARKING INFORMATION
MARKING INFORMATION


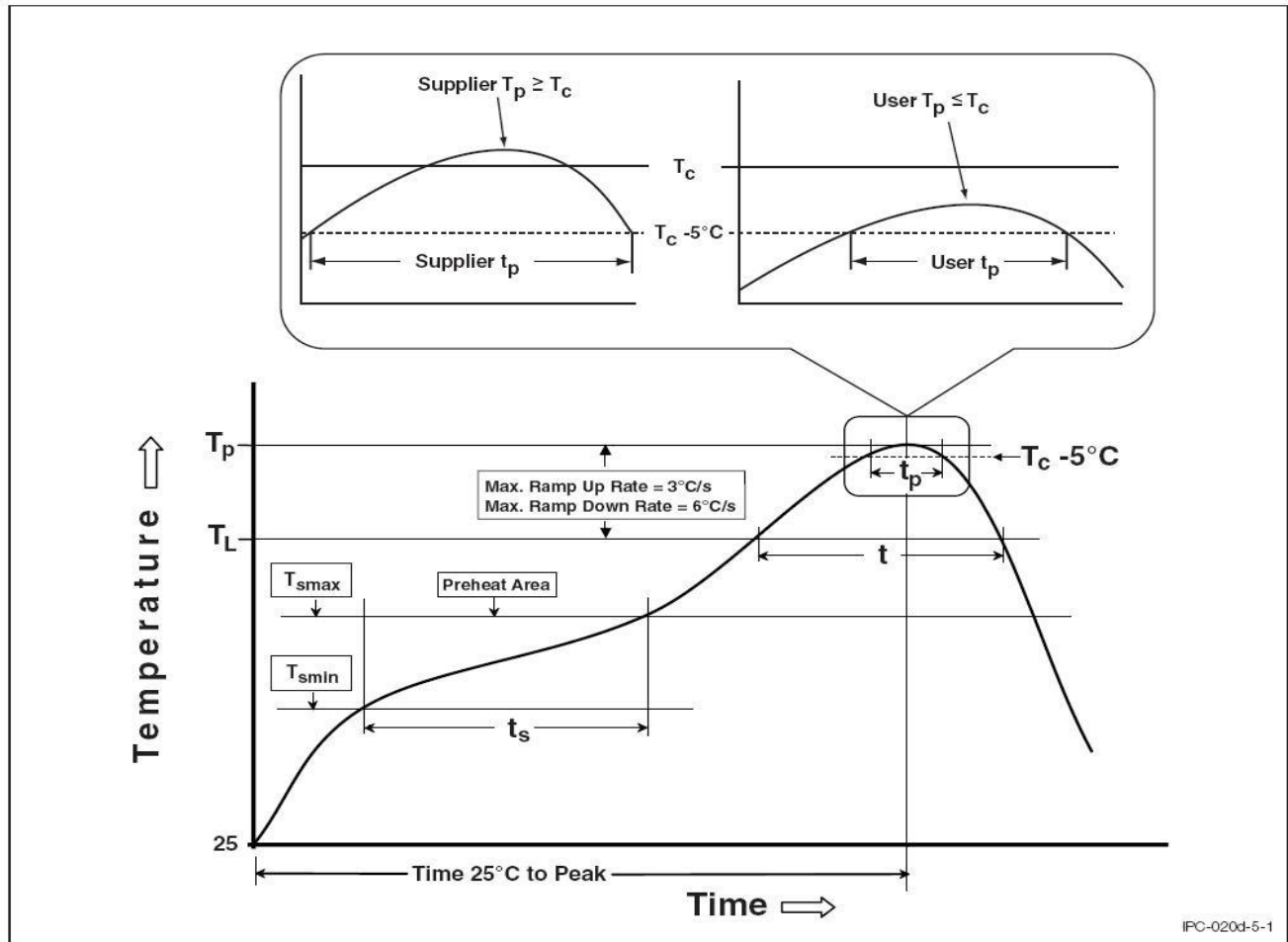
TWS : Company Abbr.
501 : Part Number
V : VDE Option
Y : Fiscal Year
WW : Work Week

ORDERING INFORMATION
TWSM501(Z)-GV

TWS – Company Abbr.
 M501 – Part Number
 Z – Tape and Reel Option (T1/T2)
 G – Green
 V – VDE Option (V or None)

LABEL INFORMATION

PACKING QUANTITY

Option	Quantity	Quantity – Inner box	Quantity – Outer box
T1	3000 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 45k Units
T2	3000 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 45k Units

REFLOW INFORMATION**REFLOW PROFILE**

Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	100	150°C
Temperature Max. (Tsmax)	150	200°C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds	60-120 seconds
Ramp-up Rate (tL to tP)	3°C/second max.	3°C/second max.
Liquidous Temperature (TL)	183°C	217°C
Time (tL) Maintained Above (TL)	60 – 150 seconds	60 – 150 seconds
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C
Time (tP) within 5°C of 260°C	20 seconds	30 seconds
Ramp-down Rate (TP to TL)	6°C/second max	6°C/second max
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

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- Please contact TWS sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
- Parameters provided in datasheets may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated in each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify TWS's terms and conditions of purchase, including but not limited to the warranty expressed therein.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.