



General Description

The IPG20N06S4L-26 use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics. This device is specially designed to get better ruggedness.

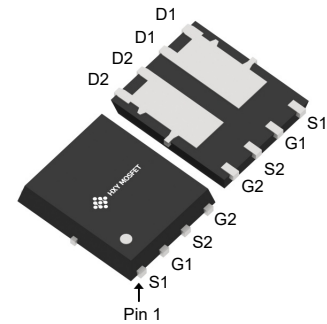
Feature

$V_{DS} = 60V$ $I_D = 35A$

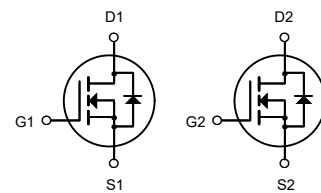
$R_{DS(ON)} < 14m\Omega @ V_{GS}=10V$

Applications

Consumer electronic power supply Motor control
Synchronous-rectification Isolated DC
Synchronous-rectification applications



DFN5X6B-8L



Dual N-Channel MOSFET

Ordering Information

| Product ID | Pack | Brand | Qty(PCS) |
|----------------|------------|------------|----------|
| IPG20N06S4L-26 | DFN5X6B-8L | HXY MOSFET | 5000 |

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise specified)

| Symbol | Parameter | Max. | Units |
|-----------------|---|---------------------|--------------|
| V_{DSS} | Drain-Source Voltage | 60 | V |
| V_{GSS} | Gate-Source Voltage | ± 20 | V |
| I_D | Continuous Drain Current | $T_C = 25^\circ C$ | 35 |
| | | $T_C = 100^\circ C$ | 26 |
| I_{DM} | Pulsed Drain Current ^{note1} | 180 | A |
| E_{AS} | Single Pulsed Avalanche Energy ^{note2} | 36 | mJ |
| P_D | Power Dissipation $T_C = 25^\circ C$ | 60 | W |
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case | 2.5 | $^\circ C/W$ |
| T_J, T_{STG} | Operating and Storage Temperature Range | -55 to +175 | $^\circ C$ |



Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Units |
|---------------|---|--|------|------|-----------|------------|
| $V_{(BR)DSS}$ | Drain-Source Breakdown Voltage | $V_{GS}=0V, I_D=250\mu A$ | 60 | - | - | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{DS}=60V, V_{GS}=0V,$ | - | - | 1.0 | μA |
| I_{GSS} | Gate to Body Leakage Current | $V_{DS}=0V, V_{GS}= \pm 20V$ | - | - | ± 100 | nA |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{DS}=V_{GS}, I_D=250\mu A$ | 1.0 | 1.6 | 2.5 | V |
| $R_{DS(on)}$ | Static Drain-Source on-Resistance <small>note3</small> | $V_{GS}=10V, I_D=20A$ | - | 11 | 14 | m Ω |
| | | $V_{GS}=4.5V, I_D=10A$ | - | 14 | 20 | |
| C_{iss} | Input Capacitance | $V_{DS}=25V, V_{GS}=0V,$ $f=1.0MHz$ | - | 930 | - | pF |
| C_{oss} | Output Capacitance | | - | 230 | - | pF |
| C_{rss} | Reverse Transfer Capacitance | | - | 8 | - | pF |
| Q_g | Total Gate Charge | $V_{DS}=30V, I_D=20A,$ $V_{GS}=10V$ | - | 22 | - | nC |
| Q_{gs} | Gate-Source Charge | | - | 4.5 | - | nC |
| Q_{gd} | Gate-Drain("Miller") Charge | | - | 3.5 | - | nC |
| $t_{d(on)}$ | Turn-on Delay Time | $V_{DD}=30V, I_D=20A,$ $R_G=1.6\Omega, V_{GS}=10V$ | - | 4.5 | - | ns |
| t_r | Turn-on Rise Time | | - | 2.7 | - | ns |
| $t_{d(off)}$ | Turn-off Delay Time | | - | 13.8 | - | ns |
| t_f | Turn-off Fall Time | | - | 2.7 | - | ns |
| I_S | Maximum Continuous Drain to Source Diode Forward Current | | - | - | 45 | A |
| I_{SM} | Maximum Pulsed Drain to Source Diode Forward Current | | - | - | 180 | A |
| V_{SD} | Drain to Source Diode Forward Voltage | $V_{GS}=0V, I_S=30A$ | - | - | 1.2 | V |
| t_{rr} | Body Diode Reverse Recovery Time | $T_J=25^\circ\text{C},$ $I_F=20A, di/dt=100A/\mu s$ | - | 18 | - | ns |
| Q_{rr} | Body Diode Reverse Recovery Charge | | - | 12 | - | nC |

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition: $T_J=25^\circ\text{C}, V_{DD}=30V, V_G=10V, R_G=25\Omega, L=0.5mH, I_{AS}=12A$

3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$



Typical Performance Characteristics

Figure 1: Output Characteristics

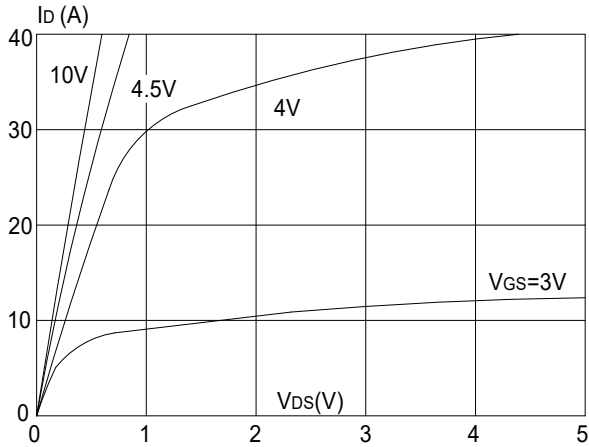


Figure 2: Typical Transfer Characteristics

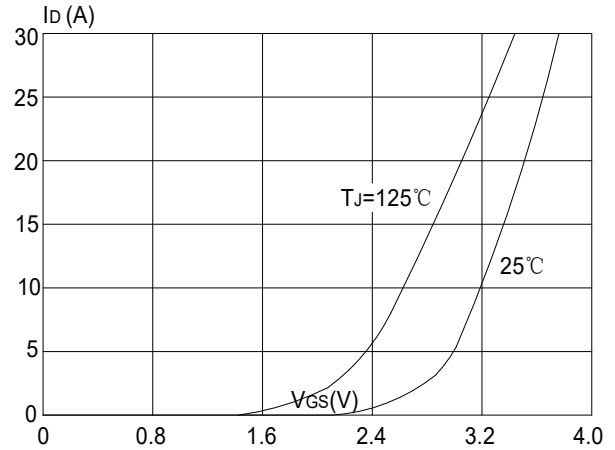


Figure 3: On-resistance vs. Drain Current

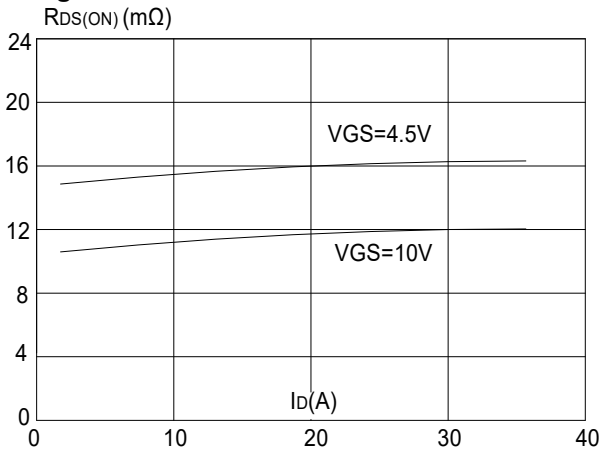


Figure 4: Body Diode Characteristics

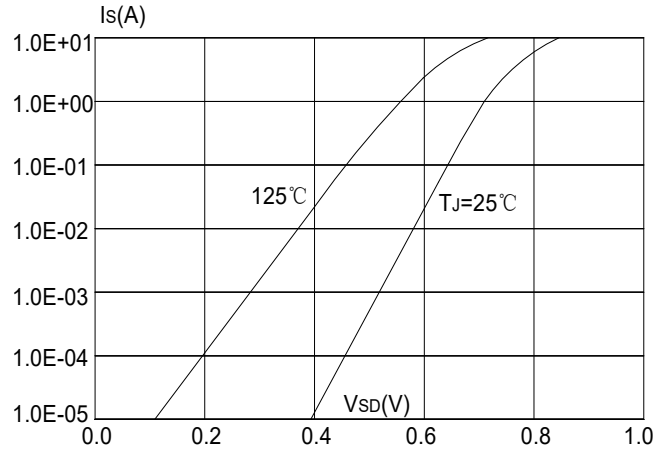


Figure 5: Gate Charge Characteristics

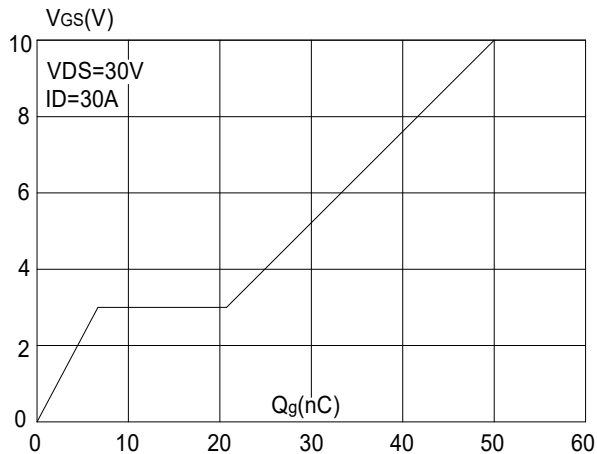


Figure 6: Capacitance Characteristics

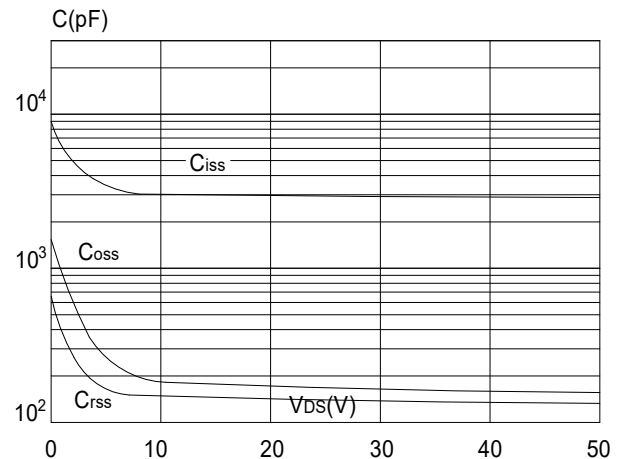




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

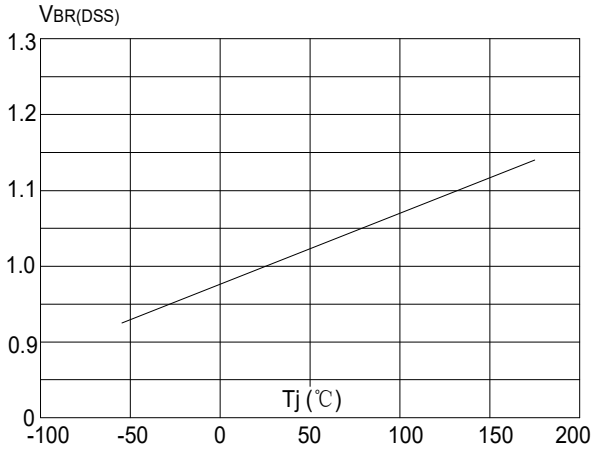


Figure 8: Normalized on Resistance vs. Junction Temperature

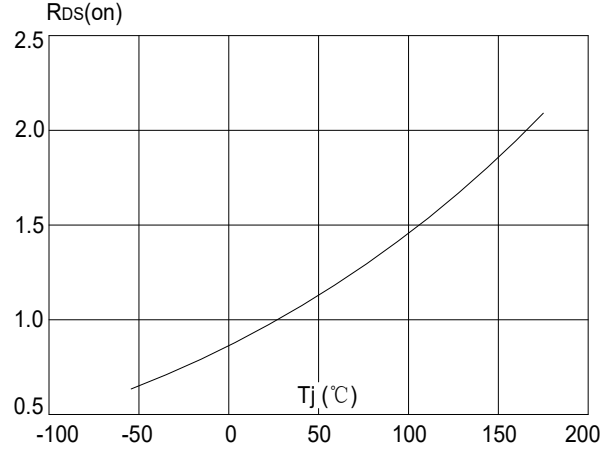


Figure 9: Maximum Safe Operating Area

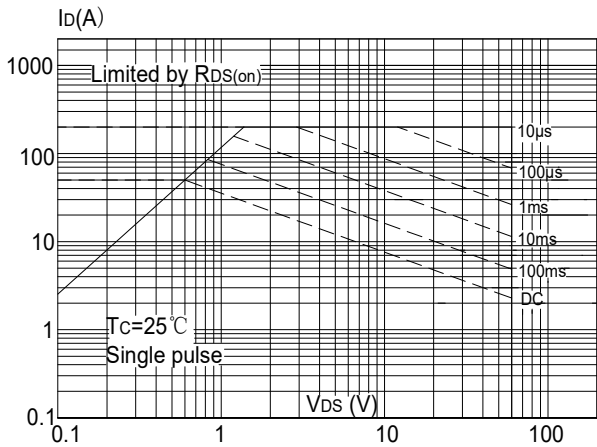


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

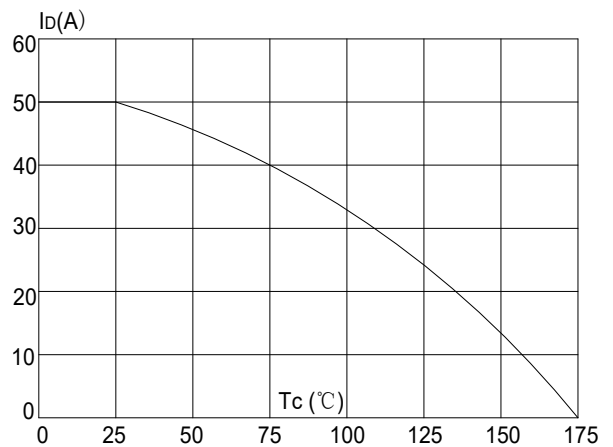
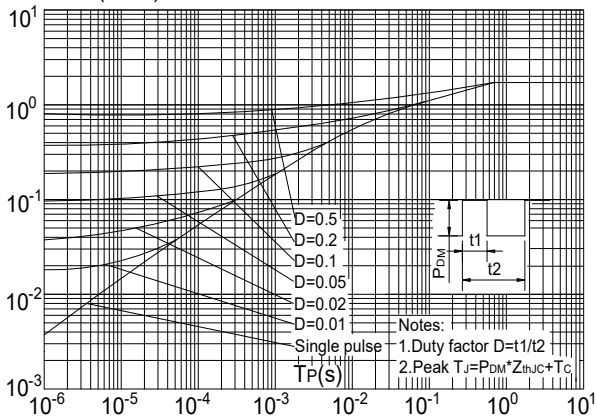
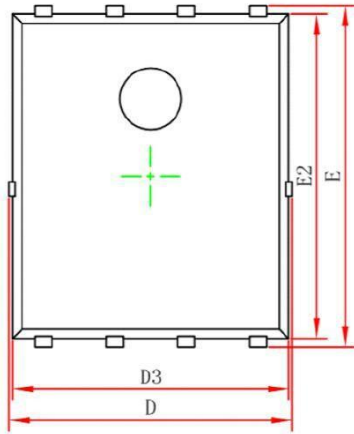


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case
 $Z_{thj-c} (^{\circ}C/W)$

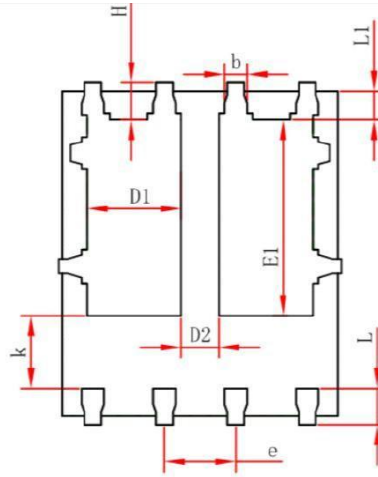




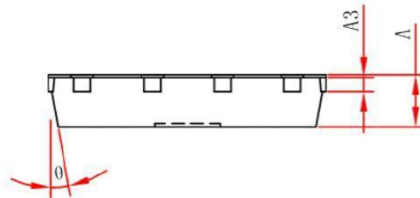
Package Mechanical Data-DFN5X6B-8L



Top View



Bottom View



Side View

| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 0.900 | 1.000 | 0.035 | 0.039 |
| A3 | 0.154REF. | | 0.006REF. | |
| D | 4.944 | 5.096 | 0.195 | 0.201 |
| E | 5.974 | 6.126 | 0.235 | 0.241 |
| D1 | 1.470 | 1.870 | 0.058 | 0.074 |
| D2 | 0.470 | 0.870 | 0.019 | 0.034 |
| E1 | 3.375 | 3.575 | 0.133 | 0.141 |
| D3 | 4.824 | 4.976 | 0.190 | 0.196 |
| E2 | 5.674 | 5.826 | 0.223 | 0.229 |
| k | 1.190 | 1.390 | 0.047 | 0.055 |
| b | 0.350 | 0.450 | 0.014 | 0.018 |
| e | 1.270TYP. | | 0.050TYP. | |
| L | 0.559 | 0.711 | 0.022 | 0.028 |
| L1 | 0.424 | 0.576 | 0.017 | 0.023 |
| H | 0.574 | 0.726 | 0.023 | 0.029 |
| θ | 10° | 12° | 10° | 12° |



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