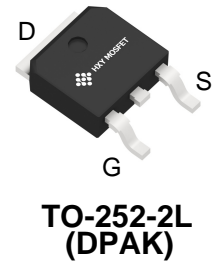




Description

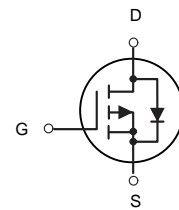
The CMD5950 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



General Features

$V_{DS} = -100V$ $I_D = -30A$

$R_{DS(ON)} < 57.5\ m\Omega @ V_{GS} = -10V$



P-Channel MOSFET

Application

Battery protection

Load switch

Uninterruptible power supply

Ordering Information

Product ID	Pack	Brand	Qty(PCS)
CMD5950	TO-252-2L(DPAK)	HXY MOSFET	2500

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-100	V
V _{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	-30	A
I _{DM}	Pulsed Drain Current ²	-120	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation ⁴	107	W
E _{AS}	Avalanche energy (Note 2)	361	mJ
T _{STG}	Storage Temperature Range	-55 to 175	°C
T _J	Operating Junction Temperature Range	-55 to 175	°C
R _{θJC}	Thermal Resistance Junction-Case ¹	1.4	°C/W



Electrical Characteristics (T = 25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =-250μA	-100	-127		V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-100V, V _{GS} =0V			-1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1	-1.8	-2.5	V
g _{FS}	Forward Transconductance	V _{DS} =-5V, I _D =-15A		50		S
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =-10V, I _D =-15A		46	57.5	mΩ
		V _{GS} =-4.5V, I _D =-10A		48	63	mΩ
C _{iss}	Input Capacitance	V _{DS} =-25V, V _{GS} =0V, f=1.0MHz		8056		pF
C _{oss}	Output Capacitance			195		pF
C _{rss}	Reverse Transfer Capacitance			70		pF
t _{d(on)}	Turn-on Delay Time	V _{GS} =-10V, V _{DS} =-50V, R _L =3.3Ω, R _{GEN} =9.1Ω		13		nS
t _r	Turn-on Rise Time			64		nS
t _{d(off)}	Turn-Off Delay Time			36		nS
t _f	Turn-Off Fall Time			52		nS
Q _g	Total Gate Charge	V _{GS} =-10V, V _{DS} =-50V, I _D =-10A		147		nC
Q _{gs}	Gate-Source Charge			17		nC
Q _{gd}	Gate-Drain Charge			31		nC
I _{SD}	Source-Drain Current (Body Diode)				-30	A
V _{SD}	Forward on Voltage ^(Note 3)	V _{GS} =0V, I _S =-15A			-1.2	V
t _{rr}	Reverse Recovery Time	I _F =-15A, di/dt=100A/μs		72		ns
Q _{rr}	Reverse Recovery Charge	I _F =-15A, di/dt=100A/μs		120		nC

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature.

Notes 2.EAS condition: T_j=25°C, V_{DD}=50V, V_G=-10V, R_g=25Ω, L=0.5mH.

Notes 3.Repetitive Rating: Pulse width limited by maximum junction temperature.



Typical Characteristics

Figure 1. Output Characteristics

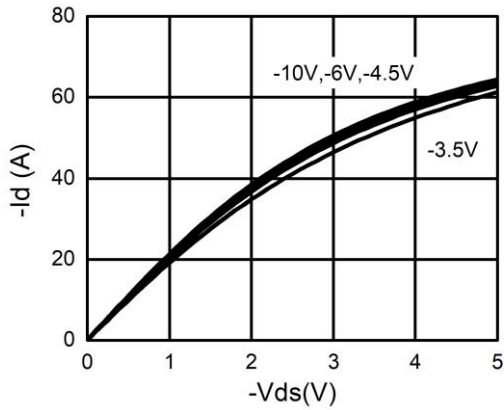


Figure 2. Transfer Characteristics

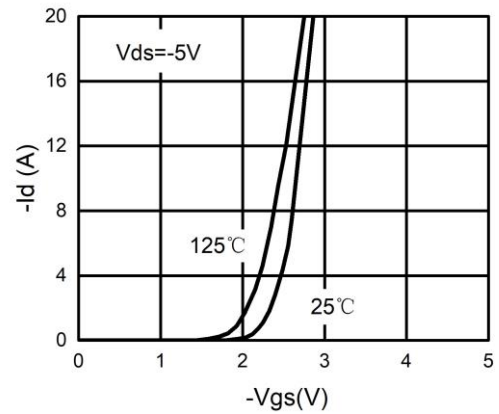


Figure 3. Power Dissipation

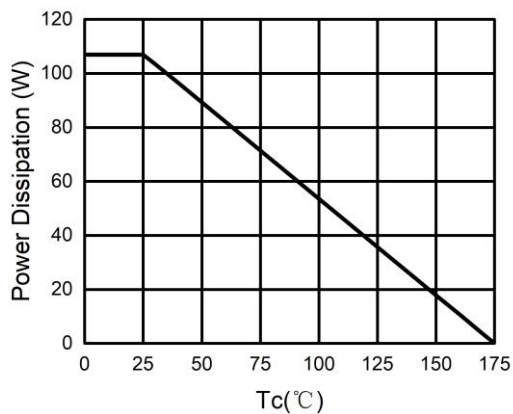


Figure 4. Drain Current

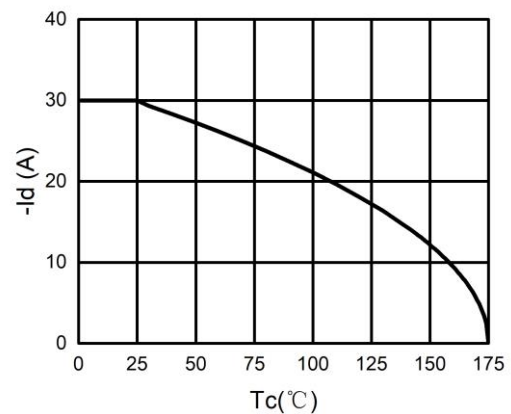


Figure 5. BV_{DSS} vs Junction Temperature

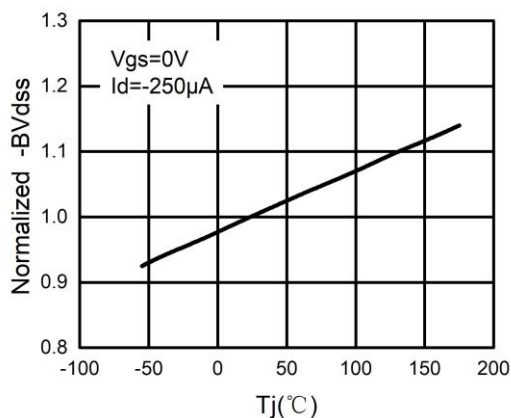


Figure 6. $R_{DS(ON)}$ vs Junction Temperature

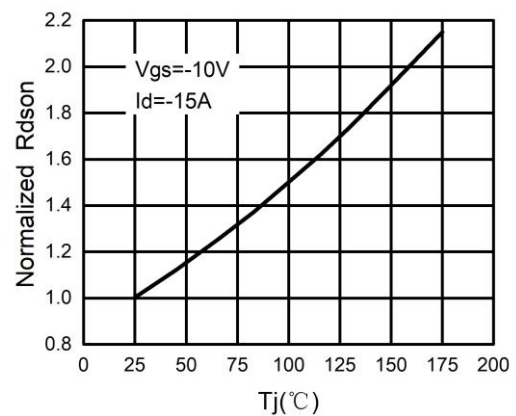




Figure 7. Gate Charge Waveforms

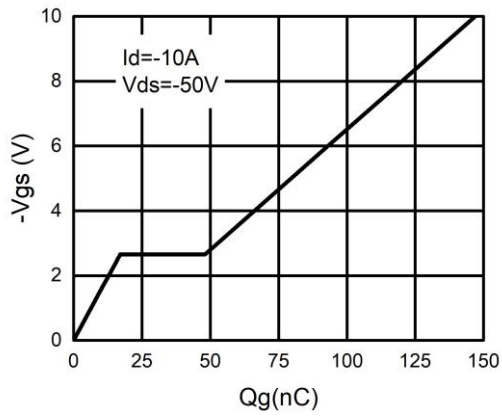


Figure 8. Capacitance

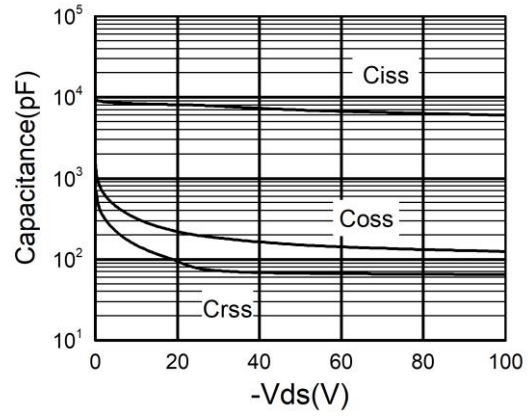


Figure 9. Body-Diode Characteristics

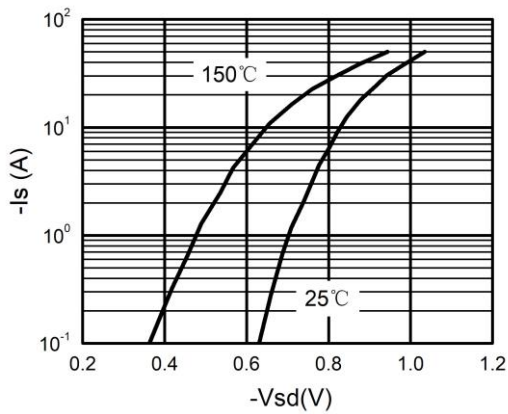
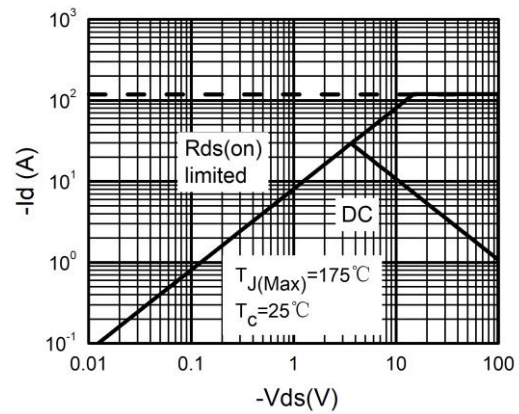


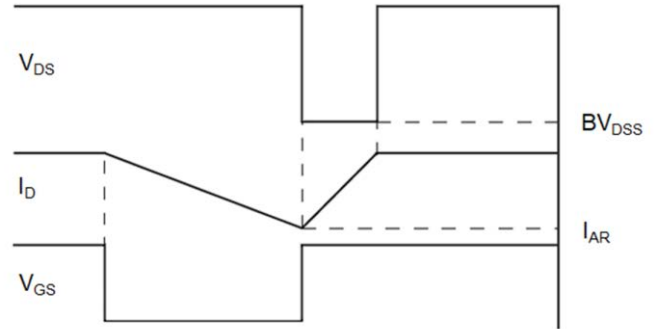
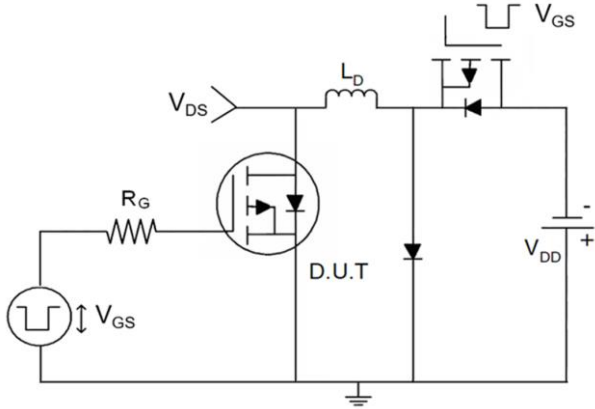
Figure 10. Maximum Safe Operating Area



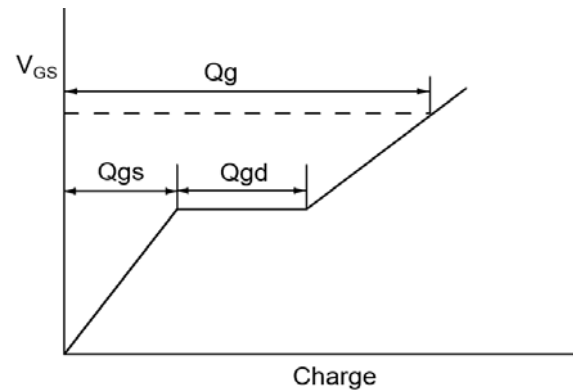
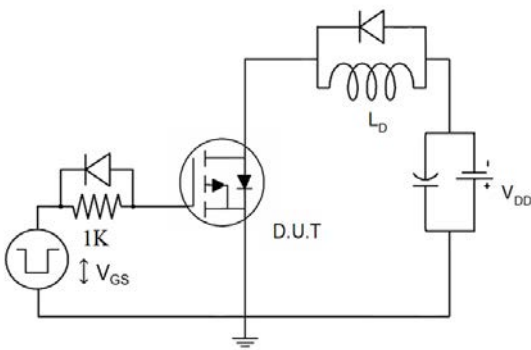


Test Circuit

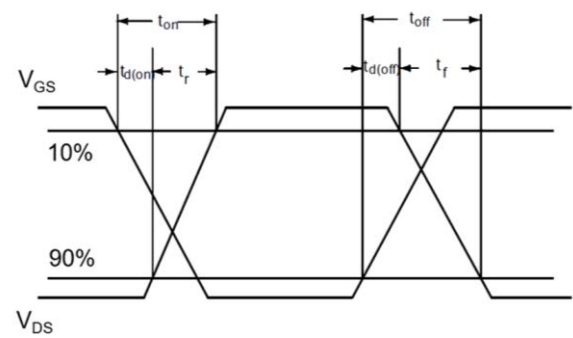
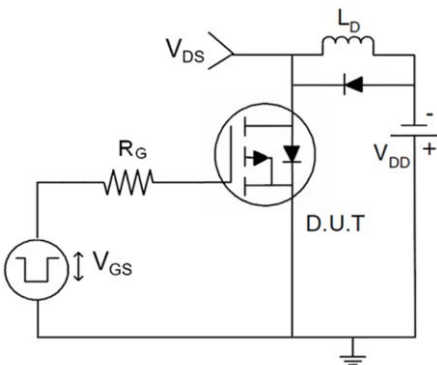
1) E_{AS} Test Circuits



2) Gate Charge Test Circuit

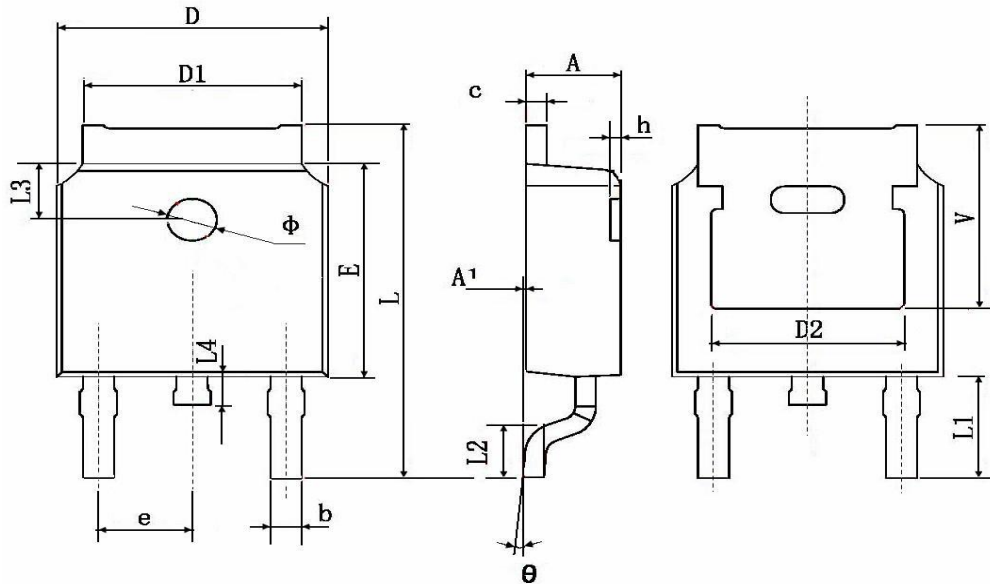


3) Switch Time Test Circuit





TO-252-2L(DPAK) Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	



Attention

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.