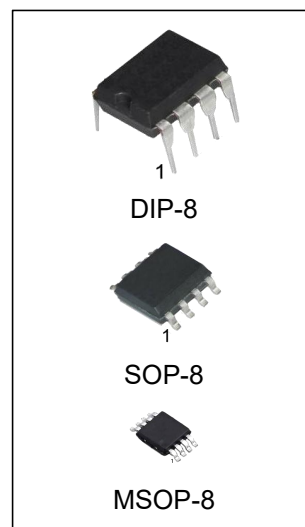


LM741 Operational Amplifier

General Description

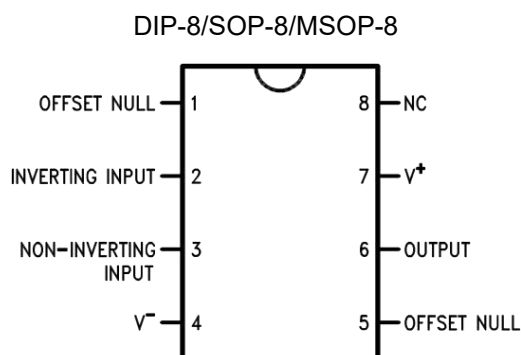
The LM741 series are general purpose operational amplifiers which feature improved performance over industry standards like the LM709. They are a direct, plug-in replacement for the 709C, LM201, MC1439 and 748 in most applications. The amplifiers offer many features which make their application nearly foolproof: overload protection on the input and output, no latch-up when the common mode range is exceeded, as well as freedom from oscillations.



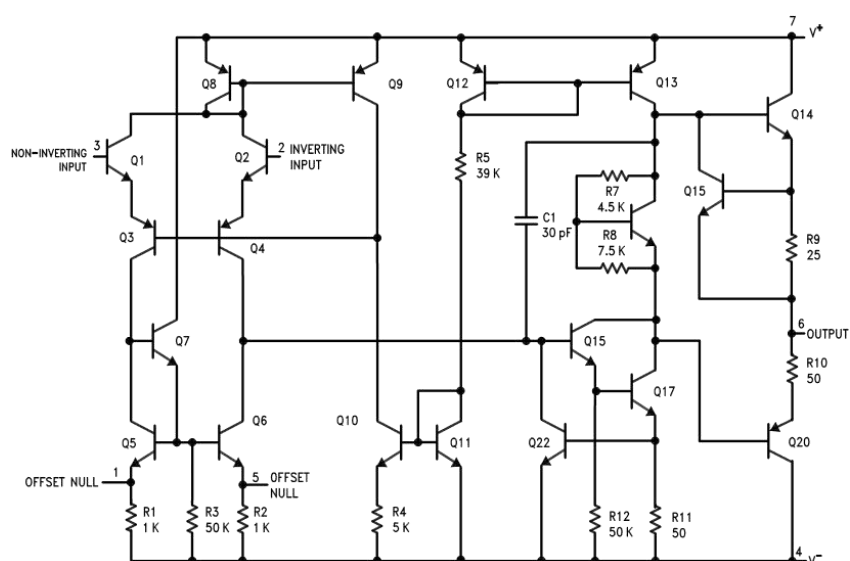
Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
LM741N	DIP-8	LM741,741	TUBE	2000pcs/Box
LM741AN	DIP-8	LM741A,741A	TUBE	2000pcs/Box
LM741M/TR	SOP-8	LM741,741C	REEL	2500pcs/Reel
LM741AM/TR	SOP-8	LM741A,741AC	REEL	2500pcs/Reel
LM741MM/TR	MSOP-8	LM741,741C	REEL	3000pcs/Reel
LM741AMM/TR	MSOP-8	LM741A,741AC	REEL	3000pcs/Reel

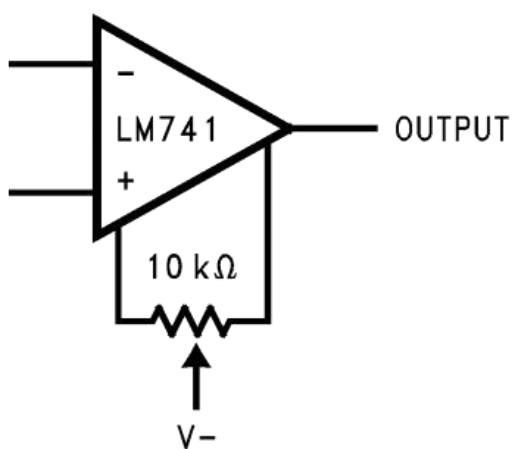
Connection Diagram



Schematic Diagram



Offset Nulling Circuit



Absolute Maximum Ratings ⁽¹⁾

CONDITION	LIMITS
Supply Voltage	± 18V
Power Dissipation ^(Note2)	500mW
Differential Input Voltage	± 30V
Input Voltage ^(Note3)	± 15V
Output Short Circuit Duration	Continuous
Operating Temperature Range	0°C to +70°C
Junction Temperature	150°C
Soldering Information (10 seconds)	260°C
Storage Temperature Range	-65°C to +150°C
ESD Tolerance	400V

Note1: "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.

Note 2: For operation at elevated temperatures, these devices must be derated based on thermal resistance, and T_j max. (listed under "Absolute Maximum Ratings"). $T_j = T_A + (\theta_{JA} P_D)$.

Note 3: For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

Thermal Resistance	DIP(N)	SOP-8(M)
θ_{JA} (Junction to Ambient)	100°C/W	195°C/W
θ_{JC} (Junction to Case)	N/A	N/A

Electrical Characteristics

Parameter	Conditions	LM741A			LM741			Units
		Min	Typ	Max	Min	Typ	Max	
Input Offset Voltage	$T_A=25^{\circ}\text{C}$							
	$R_S \leq 10\text{K}\Omega$					1.0	5.0	mW
	$R_S \leq 50\Omega$		0.8	3.0				mW
	$T_{AMIN} \leq T_A \leq T_{AMAX}$							
	$R_S \leq 50\Omega$			4.0				mW
	$R_S \leq 10\text{K}\Omega$						6.0	mW
Average Input Offset Voltage Drift				15				$\mu\text{V}/^{\circ}\text{C}$
Input Offset Voltage Adjustment Range	$T_A=25^{\circ}\text{C}, V_S=\pm 18\text{V}$	± 10				± 15		mW
Input Offset Current	$T_A=25^{\circ}\text{C}$		3.0	30		20	200	nA
	$T_{AMIN} \leq T_A \leq T_{AMAX}$			70		85	500	nA
Average Input Offset Current Drift				0.5				$\text{nA}/^{\circ}\text{C}$
Input Bias Current	$T_A=25^{\circ}\text{C}$		30	80		80	500	nA
	$T_{AMIN} \leq T_A \leq T_{AMAX}$			0.21			1.5	μA
Input Resistance	$T_A=25^{\circ}\text{C}, V_S=\pm 18\text{V}$	1.0	6.0		0.3	2.0		$\text{M}\Omega$
	$T_{AMIN} \leq T_A \leq T_{AMAX}, V_S=\pm 18\text{V}$	0.5						$\text{M}\Omega$
Input Voltage Range	$T_A=25^{\circ}\text{C}$							V
	$T_{AMIN} \leq T_A \leq T_{AMAX}$				± 12	± 13		V
Large Signal Voltage Gain	$T_A=25^{\circ}\text{C}, R_L \geq 2\text{K}\Omega$							
	$V_S=\pm 18\text{V}, V_O=\pm 15\text{V}$	50						V/mW
	$V_S=\pm 15\text{V}, V_O=\pm 10\text{V}$				50	200		V/mW
	$T_{AMIN} \leq T_A \leq T_{AMAX}$							
	$R_L \geq 2\text{K}\Omega$							
	$V_S=\pm 18\text{V}, V_O=\pm 15\text{V}$	32						V/mW
Output Voltage Swing	$V_S=\pm 15\text{V}, V_O=\pm 10\text{V}$				25			V/mW
	$V_S=\pm 5\text{V}, V_O=\pm 2\text{V}$	10						V/mW
	$V_S=\pm 18\text{V}$							
	$R_L \geq 10\text{K}\Omega$	± 16						V
Output Short Circuit Current	$R_L \geq 2\text{K}\Omega$	± 15						V
	$V_S=\pm 15\text{V}$				± 12	± 14		V
	$R_L \geq 10\text{K}\Omega$				± 10	± 13		V
	$R_L \geq 2\text{K}\Omega$							
Output Short Circuit Current	$T_A=25^{\circ}\text{C}$	10	25	35		25		mA
	$T_{AMIN} \leq T_A \leq T_{AMAX}$	10		40				mA

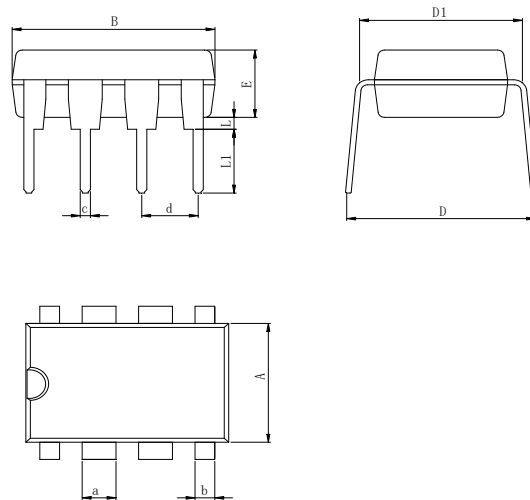
Common-Mode Rejection Ratio	$T_{AMIN} \leq T_A \leq T_{AMAX}$ $R_S \leq 10K\Omega, V_{CM} = \pm 12V$ $R_S \leq 50\Omega, V_{CM} = \pm 12V$	80	95	0.5	70	90		dB dB
Supply Voltage Rejection Ratio	$T_{AMIN} \leq T_A \leq T_{AMAX}$ $V_S = \pm 18V$ to $V_S = \pm 5V$ $R_S \leq 50\Omega$ $R_S \leq 10K\Omega$	86	96		77	96		dB dB
Transient Response Rise Time Overshoot	$T_A = 25^\circ C$, Unity Gain		0.25 6.0	0.8 20		0.3 5		μs %
Bandwidth ^(Note5)	$T_A = 25^\circ C$	0.437	1.5					MHz
Slew Rate	$T_A = 25^\circ C$, Unity Gain	0.3	0.7			0.5		V/ μs
Supply Current	$T_A = 25^\circ C$					1.7	2.8	mA
Power Consumption	$T_A = 25^\circ C$ $V_S = \pm 18V$ $V_S = \pm 15V$		80	150		50	85	mw mw

Note 4: Calculated value from: BW (MHz) = 0.35/Rise Time(μs).

Note 5: Human body model, 1.5 k Ω in series with 100 pF.

Physical Dimensions

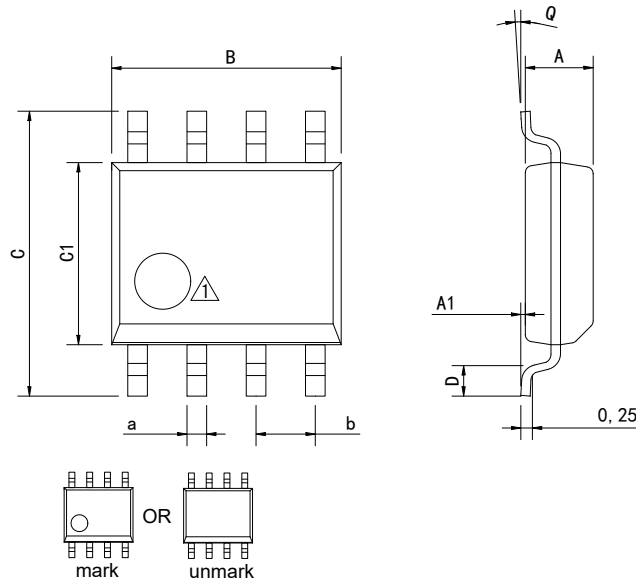
DIP-8



Dimensions In Millimeters(DIP-8)

Symbol:	A	B	D	D1	E	L	L1	a	b	c	d
Min:	6.10	9.00	8.10	7.42	3.10	0.50	3.00	1.50	0.85	0.40	2.54 BSC
Max:	6.68	9.50	10.9	7.82	3.55	0.70	3.60	1.55	0.90	0.50	

SOP-8

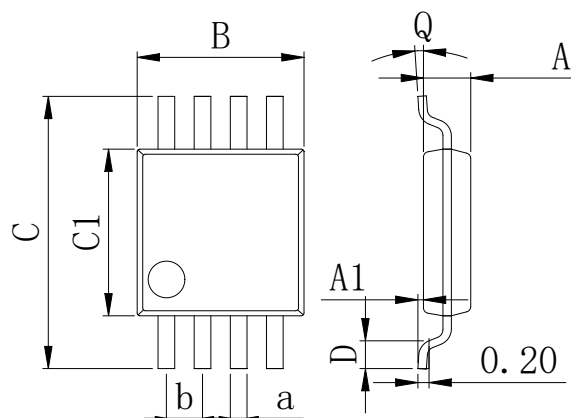


⚠ Package top mark may be in lower left corner or unmark

Dimensions In Millimeters(SOP-8)

Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	

MSOP-8



Dimensions In Millimeters(MSOP-8)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65 BSC
Max:	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	

Revision History

REVISION NUMBER	DATE	DESCRIPTION	PAGE
V1.0	2011-5	New	1-9
V1.1	2018-9	Update encapsulation type	1
V1.2	2022-4	Update Lead Temperature	3
V1.3	2024-12	Add a model marking name	1
V1.4	2025-12	Update important statements、Update sop-8 Dimension drawing	6、 9

IMPORTANT STATEMENT:

Huaguan Semiconductor reserves the right to change products and services offered without prior notice. Customers should obtain the latest relevant information before placing orders and verify that such information is current and complete. Huaguan Semiconductor assumes no responsibility or liability for altered documents.

Customers are responsible for complying with safety standards and implementing safety measures when using Huaguan Semiconductor products in system design and end-product manufacturing. You assume full responsibility for: selecting the appropriate Huaguan Semiconductor products for your application; designing, validating, and testing your application; and ensuring that your application complies with applicable standards and all other safety, security, or other requirements. This is to prevent potential risks that may lead to personal injury or property damage.

Huaguan Semiconductor products are not approved for use in life support, military, aerospace, or other high-risk applications. Huaguan products are neither intended nor warranted for use in such systems or equipment. Any failure or malfunction may lead to personal injury or severe property damage. Such applications are deemed "Unsafe Use." Unsafe Use includes, but is not limited to: surgical and medical equipment, nuclear energy control equipment, aircraft or spacecraft instruments, control or operation of vehicle power, braking, or safety systems, traffic signal instruments, all types of safety devices, and any other applications intended to support or sustain life. Huaguan Semiconductor shall not be liable for consequences resulting from Unsafe Use in these fields. Users must independently evaluate and assume all risks. Any issues, liabilities, or losses arising from the use of products beyond their approved applications shall be solely borne by the user. Users may not claim any compensation from Huaguan Semiconductor based on these terms. If any third party claims against Huaguan Semiconductor due to such Unsafe Use, the user shall compensate Huaguan Semiconductor for all resulting damages and liabilities.

Huaguan Semiconductor provides technical and reliability data (including datasheets), design resources (including reference designs), application or other design advice, web tools, safety information, and other resources for its semiconductor products. However, no guarantee is made that these resources are free from defects, and no express or implied warranties are provided. The use of testing and other quality control techniques is limited to Huaguan Semiconductor's quality assurance scope. Not all parameters of each device are tested.

Huaguan Semiconductor's documentation authorizes you to use these resources only for developing applications related to the products described herein. You are not granted rights to any other intellectual property of Huaguan Semiconductor or any third party. Any other reproduction or display of these resources is strictly prohibited. You shall fully indemnify Huaguan Semiconductor and its agents against any claims, damages, costs, losses, and liabilities arising from your use of these resources. Huaguan Semiconductor shall not be held responsible.