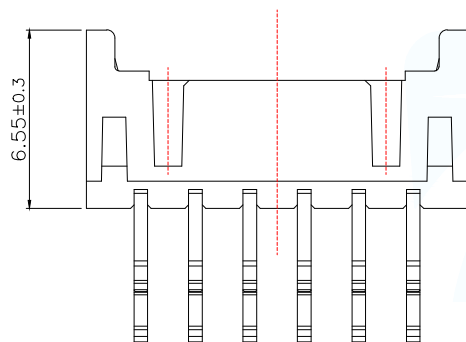
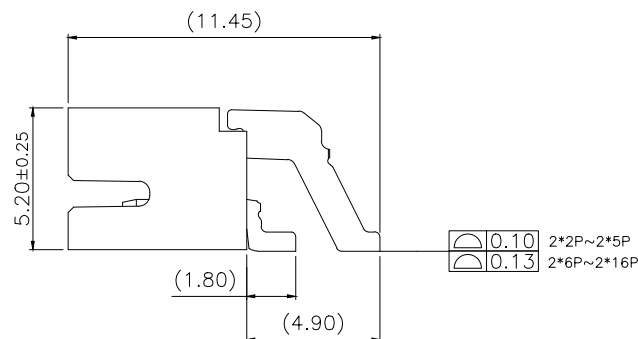
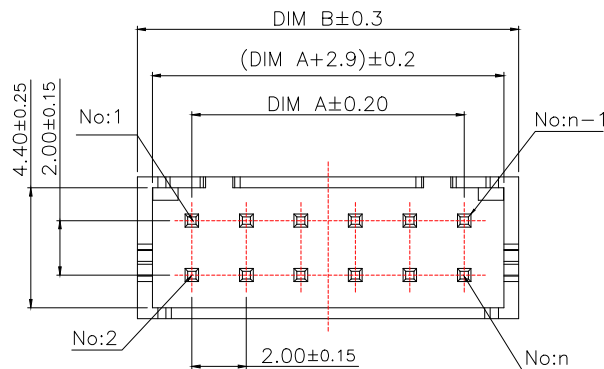
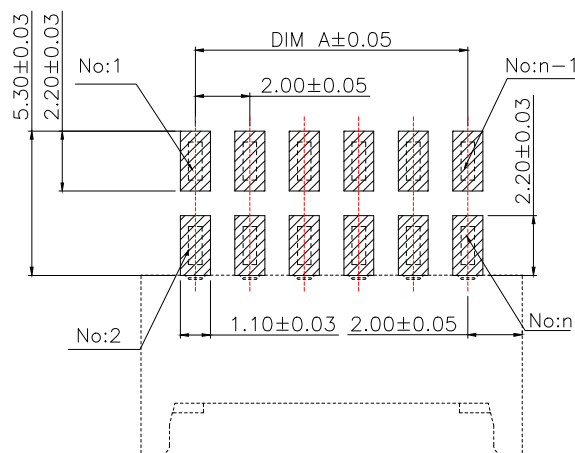
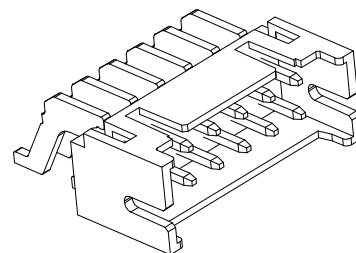
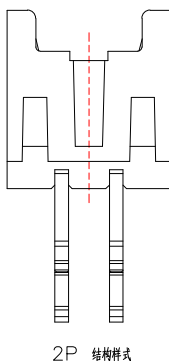




技术指标(Technical Index):

1. 工作温度(Temperature range): $-40^{\circ}\text{C} \sim +105^{\circ}\text{C}$.
2. 适用线规(Apply to gauge): AWG#28~#24
3. 额定电压(Rated voltage): 250V DC/AC(rms).
4. 额定电流(Rated current): 3.0A AC, DC.
5. 接触电阻(Contact resistance): $\leq 20\text{m}\Omega$.
6. 耐压值(Dielectric withstanding voltage): 800V AC/minute.
7. 绝缘电阻(Insulation resistance): $\geq 1000\text{M}\Omega$.

SUGGESTED PCB LAYOUT
(COMPONENT SIDE)

Part No	Pin	A	B
WAFER-PHD2.0-4PWB	2*2	2.00	6.00
WAFER-PHD2.0-6PWB	2*3	4.00	8.00
WAFER-PHD2.0-8PWB	2*4	6.00	10.00
WAFER-PHD2.0-10PWB	2*5	8.00	12.00
WAFER-PHD2.0-12PWB	2*6	10.00	14.00
WAFER-PHD2.0-14PWB	2*7	12.00	16.00
WAFER-PHD2.0-16PWB	2*8	14.00	18.00
WAFER-PHD2.0-18PWB	2*9	16.00	20.00
WAFER-PHD2.0-20PWB	2*10	18.00	22.00
WAFER-PHD2.0-22PWB	2*11	20.00	24.00
WAFER-PHD2.0-24PWB	2*12	22.00	26.00
WAFER-PHD2.0-26PWB	2*13	24.00	28.00
WAFER-PHD2.0-28PWB	2*14	26.00	30.00
WAFER-PHD2.0-30PWB	2*15	28.00	32.00
WAFER-PHD2.0-32PWB	2*16	30.00	34.00
WAFER-PHD2.0-36PWB	2*18	34.00	38.00
WAFER-PHD2.0-40PWB	2*20	38.00	42.00

序号	品名	材质
①	PHD卧贴WTB胶芯	LCP UL94V-0本色
②	PHD卧贴(连绕)端子	铜合金/表面镀锡

MANUFACTURE DWG		 东莞市讯普电子科技有限公司 DongGuan XunPu Electronics Co., Ltd	
UNLESS OTHERWISE SPECIFIED TOLERANCES		TITLE: 2.0 Pitch D型及排卧贴	
DECIMALS:		PAR	
0.X ± 0.25		DWN	
0.XX ± 0.12		CHKD	
0.XXX ± 0.05		APVD	
		SCALE: 1:1	
		UNIT: MM	
		SHEET: 1F1	
		REV: A	