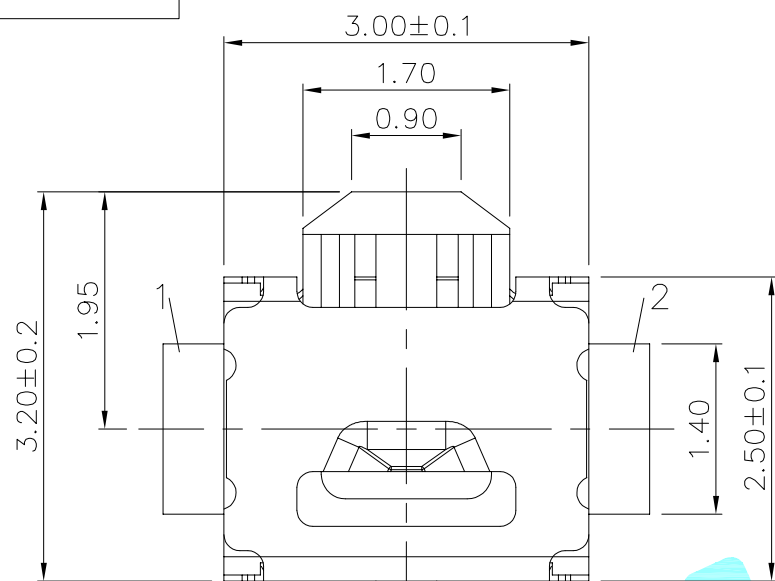
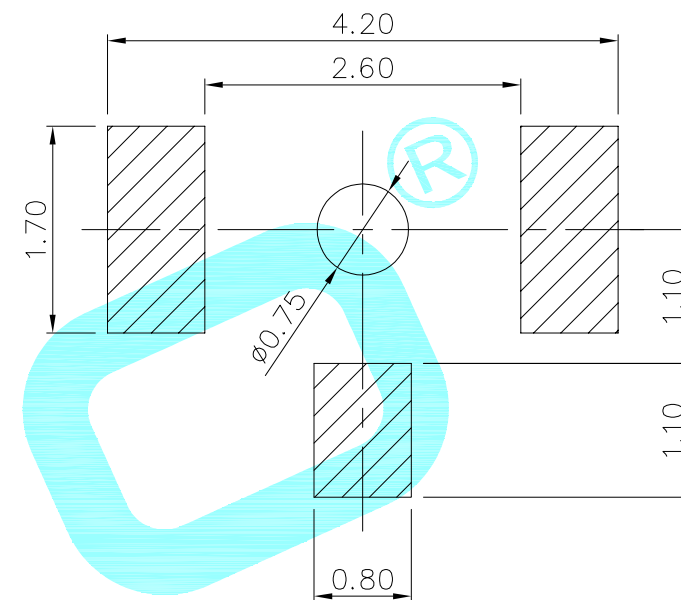


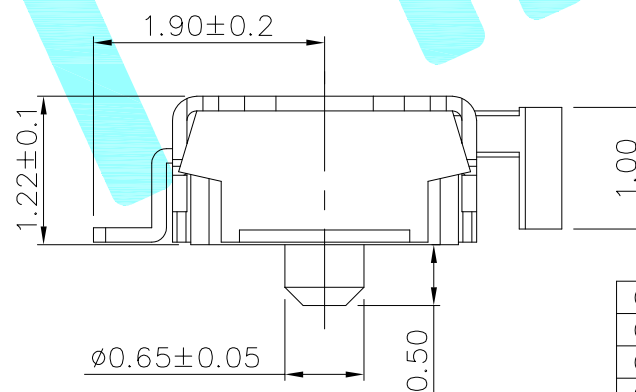
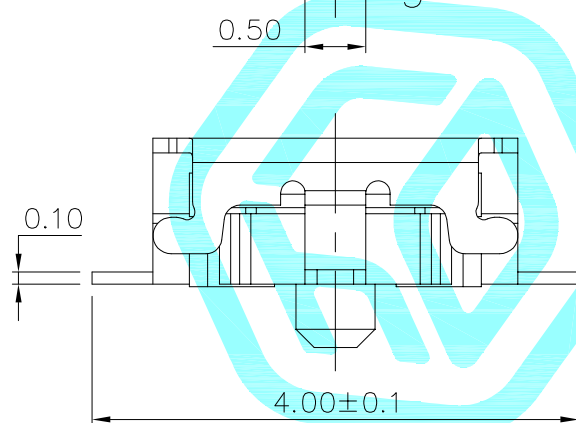
Circuit Diagram



PCB Layout

SPECIFICATION

- 1.Rating:DC 12V 50mA Max
- 2.Contact resistance:100mΩ MAX
- 3.Insulation resistance:100MΩ Min
- 4.Dielectric strength:250VAC/min
- 5.Operating force: 160gf±50gf
- 6.Operating life: 100,000 cycles



⑥	Dust cover	—	1	KAPTON FILM	—	—
⑤	Terminal	—	2	Phosphor Bronze	Plating silver	—
④	Contact	—	1	F.Ag/SUS	—	—
③	Base	—	1	LCP	White	—
②	Cover	—	1	SUS	—	—
①	Keystoke	—	1	LCP	Black	—
ITEM	PART NAME	TER'NO.	QTY.	MATERIAL	FINISHING	REMARK

APPROVALS			DATE				
DRAWN	LIANG	2026.03.17					
CHECKED				TITLE:	TACT SWITCHES		
APPROVALS				PART NO.	K2-1823SD		
TOLERANCES ARE		30~以上	±0.30	ANGLE	UNIT: mm	SCALE: 1:1	PROJ: 
		10~30	±0.20				
		5~10	±0.15				
		~ 5	±0.10				
		±2'		DRAWING NO.			SHEET 1 OF 1

—	A	—	NEW	—	—	—
ECN NO.	REV.	DATE.	DESCRIPTION.	CHANGE.	CHECK.	APPRO.