

Features

- 4 Bi-directional Channels ESD protection
- Typically Used at High Speed Ports
- Low operating voltage: 60V
- Low Channel Input Capacitance of 15pF
Typical for I/Os (Typ)
- -IEC 61000-4-2 (ESD) immunity test
Air discharge: $\pm 8\text{kV}$
Contact discharge: $\pm 8\text{kV}$
- -IEC61000-4-4 (EFT) 40A (5/50ns)
- Totally Lead-Free & Fully RoHS Compliant
- Halogen and Antimony Free. "Green" Device

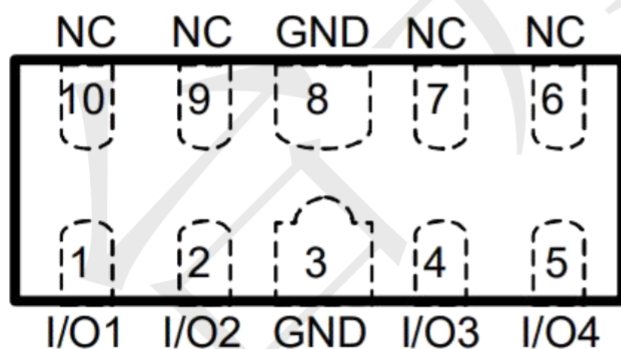
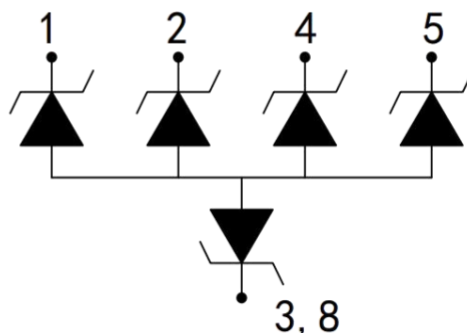
Mechanical Characteristics

- Package: DFN2510-10L
- Lead Finish: Matte Tin
- Case Material: "Green" Molding Compound
- Terminal Connections: See Diagram Below
- -IEC 61000-4-2 (ESD) immunity test

Applications

- Set-top boxes
- LCD televisions
- VBus protections
- Typically used at chip-on-glass (COG) panels

Dimensions and Pin Configuration



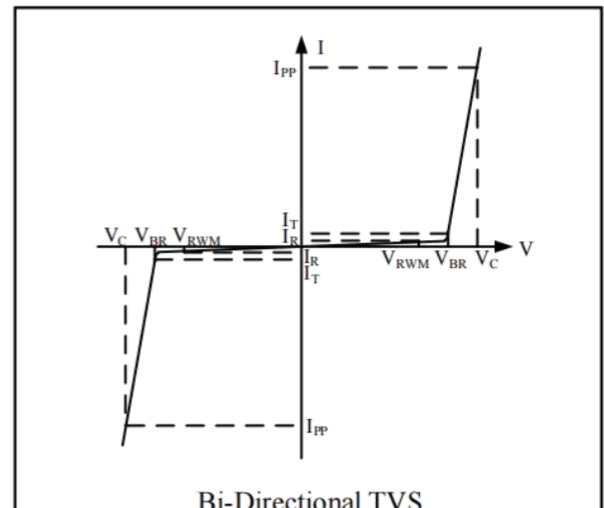
Absolute Maximum Ratings (T_{amb}=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	P _{pp}	200	W
IEC 61000-4-2 Spec Current at 60 ns (A)	I _{pp}	2	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V _{ESD}	±8 ±8	KV
Operating Temperature Range	T _J	-55 to +125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	V _{RWM}	--	--	60	V	
Breakdown Voltage	V _{BR}	65	--	85	V	I _T = 1mA
Reverse Leakage Current	I _R	--	--	100	nA	V _{RWM} =60V
Clamping Voltage	V _C	--	90	--	V	I _{pp} =1A(8x 20us pulse)
Clamping Voltage	V _C	--	--	100	V	I _{pp} =2A(8x 20us pulse)
Junction Capacitance	C _J	--	--	15	pF	V _R = 0V, f = 1MHz, Any I/O pin to GND
Junction Capacitance	C _J	--	8	--	pF	V _R = 0V, f = 1MHz, Between any I/O pin

Symbol	Parameter
V _{RWM}	Nominal Reverse Working Voltage
I _R	Reverse Leakage Current @ V _{RWM}
V _{BR}	Reverse Breakdown Voltage @ I _T
I _T	Test Current for Reverse Breakdown
V _C	Clamping Voltage @ I _{pp}
I _{pp}	Maximum Peak Pulse Current
C _{ESD}	Parasitic Capacitance
V _R	Reverse Voltage
f	Small Signal Frequency



Characteristic Curves

Fig1. 8/20 μ s Pulse Waveform

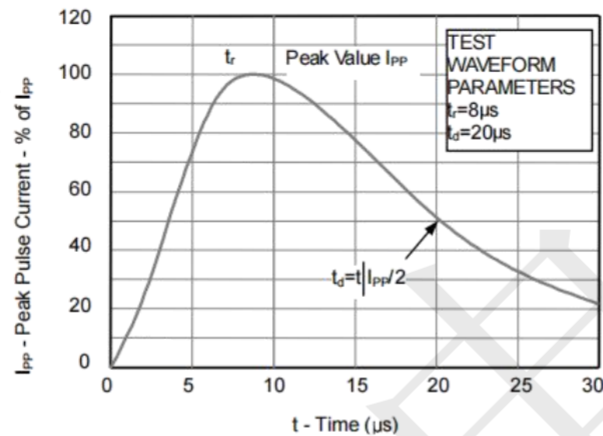


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)

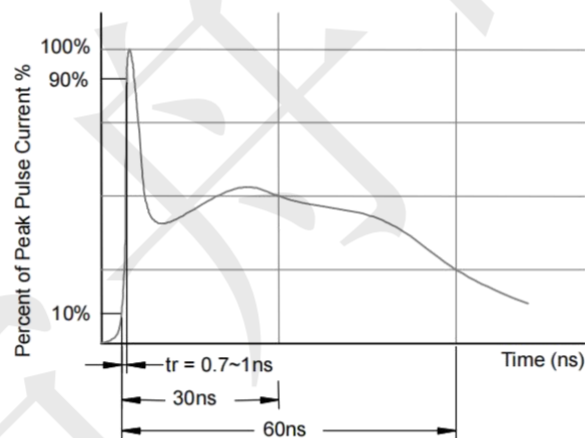
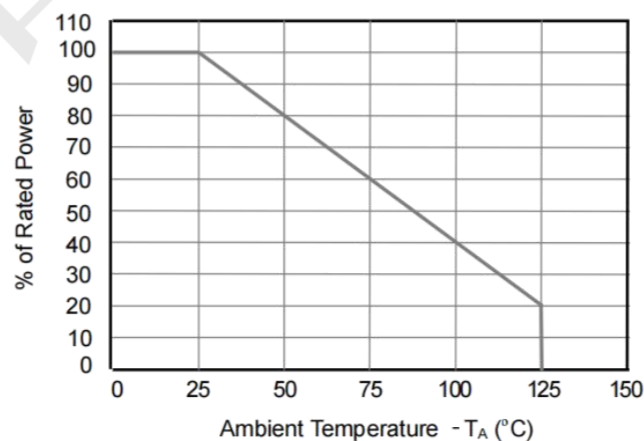
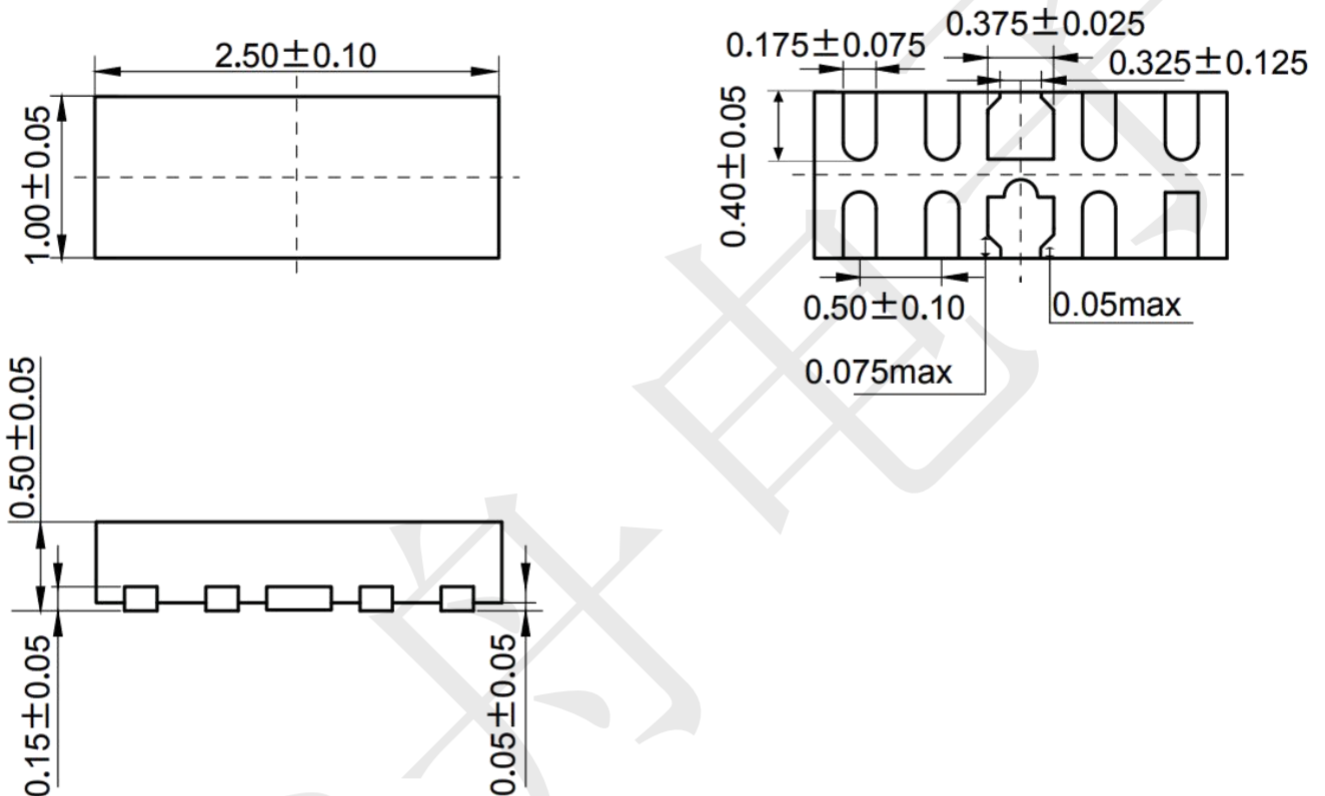


Fig3. Power Derating Curve



Package Outline Dimensions (unit: mm)

DFN2510-10L



Mounting Pad Layout (unit: mm)

