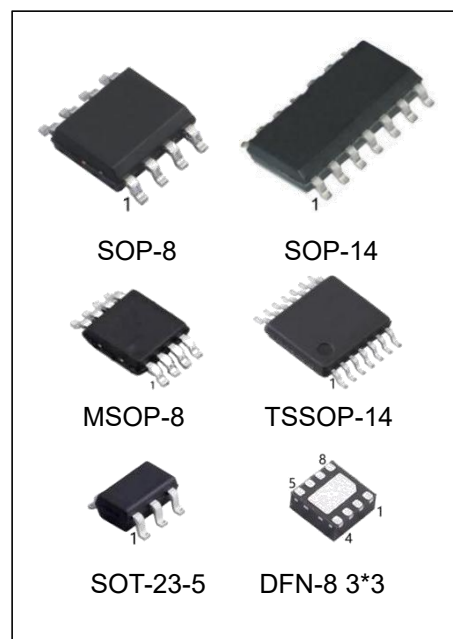


48V, Low Noise, Precision Operational Amplifiers

Features and Benefits

- Wide Supply: $\pm 2.25\text{ V}$ to $\pm 24\text{ V}$, 4.5 V to 48 V
- Low Offset Voltage: $\pm 100\text{ }\mu\text{V}$ Maximum
- Low Offset Voltage Drift: $\pm 1\text{ }\mu\text{V}/^{\circ}\text{C}$
- Bandwidth: 1.1 MHz GBW
- Slew Rate: $0.8\text{ V}/\mu\text{s}$
- Low Noise: $22\text{ nV}/\sqrt{\text{Hz}}$ at 10 kHz
- High Common-Mode Rejection: 110 dB
- Low Bias Current: $\pm 20\text{ pA}$
- EMI/RFI Filtered Inputs



Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
TSB621IM5/TR-HG	SOT-23-5	K2K	REEL	3000pcs/reel
TSB622IM/TR-HG	SOP-8	TSB622	REEL	2500pcs/reel
TSB622IMM/TR-HG	MSOP-8	B622,K2K	REEL	3000pcs/reel
TSB622IDQ3/TR-HG	DFN-8 3*3	B622,K2K	REEL	5000pcs/reel
TSB624IM/TR-HG	SOP-14	TSB624	REEL	2500pcs/reel
TSB624IMT/TR-HG	TSSOP-14	TSB624	REEL	2500pcs/reel

General Description

The TSB62x is a new generation of high voltage (48 V), low noise, precision operational amplifiers. These devices offer outstanding dc precision and ac performance, include low offset ($\pm 100\ \mu\text{V}$ maximum), low drift ($\pm 1\ \mu\text{V}/^\circ\text{C}$), 1.1-MHz bandwidth, and 22 nV/ $\sqrt{\text{Hz}}$ Input voltage noise density at 10 kHz. Unique features such as differential input-voltage range to the negative supply rail, high output current ($\pm 40\ \text{mA}$), and high capacitive load drive of up to 1 nF make the TSB62x high-performance op-amps for high-voltage industrial applications.

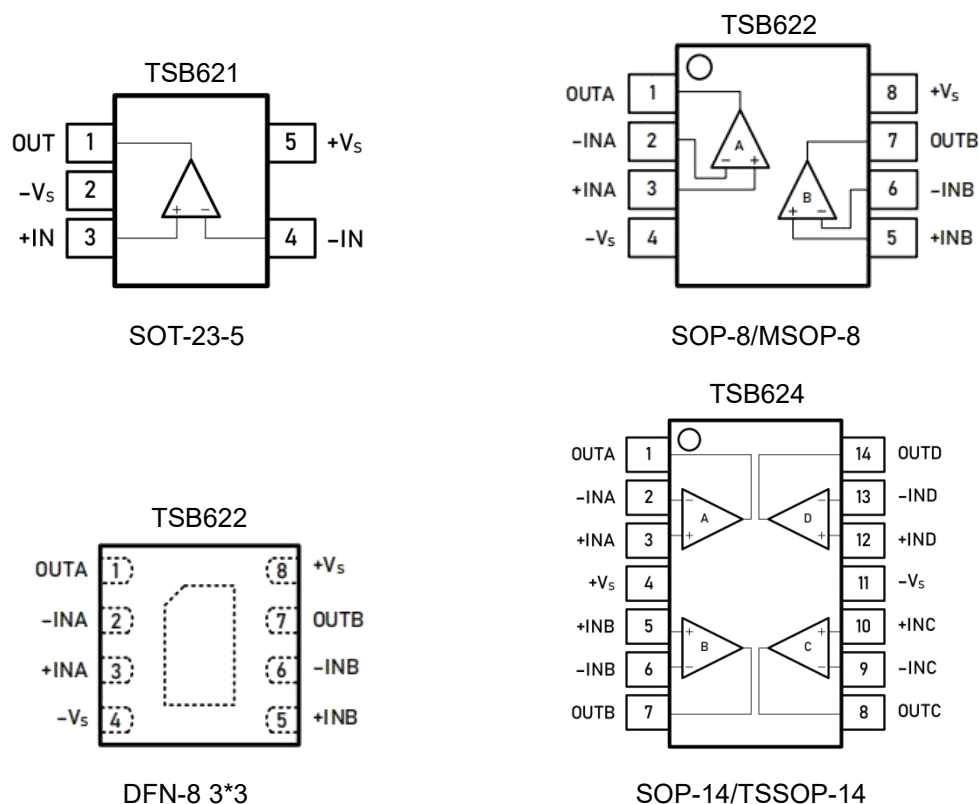
The robust design of the TSB62x family provides ease-of-use to the circuit designer: integrated RF/EMI rejection filter, no phase reversal in overdrive conditions, and high electro-static discharge (ESD) protection. The TSB62x are optimized for operation at voltages from +4.5 V ($\pm 2.25\ \text{V}$) to +48 V ($\pm 24\ \text{V}$) over the extended temperature range of -40°C to $+125^\circ\text{C}$.

The TSB621 (single) is available in both SOT-23-5 packages. The TSB622 (dual) is offered in SOP-8 and MSOP-8 packages. The quad-channel TSB624 is offered in both SOP-14 and TSSOP-14 packages.

Applications

- High-side and low-side current sensing
- Analog input and output modules
- ADC driver and reference buffer amplifier
- High Precision Comparator
- Power delivery: UPS, server, and merchant network power
- Multiplexed Data-Acquisition Systems
- Test and Measurement Equipment
- Programmable Logic Controllers

Pin Configuration (Top View)



Pin Description

Symbol	Description
-IN	Inverting input of the amplifier. The voltage range is from V_{S-} to $V_{S+} - 2V$.
+IN	Non-inverting input of the amplifier. This pin has the same voltage range as -IN.
+Vs	Positive power supply. The voltage is from 4.5V to 48V. Split supplies are possible as long as the voltage between V_{S+} and V_{S-} is from 4.5V to 48V.
-Vs	Negative power supply. It is normally tied to ground. It can also be tied to a voltage other than ground as long as the voltage between V_{S+} and V_{S-} is from 4.5V to 48V.
OUT	Amplifier output.

Limiting Value

In accordance with the Absolute Maximum Rating System (IEC 60134).

Parameter	Absolute Maximum Rating
Supply Voltage, VS+ to VS–	60 V
Signal Input Terminals: Voltage, Current	–VS – 0.3V to +VS + 0.3V, ±10 mA
Output Short-Circuit	Continuous
Storage Temperature Range, Tstg	–65 °C to +150 °C
Operating Temperature Range ⁽¹⁾ , TA	–40 °C to +125 °C
Junction Temperature, TJ	150 °C
Lead Temperature Range (Soldering 10 sec)	260 °C

Note: Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions outside those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

(1) Operating temperature range: –40°C to +125°C. This product is designed for industrial grade applications. For automotive grade versions compliant with AEC-Q100, please conduct internal screening per the standard or contact our sales team for availability.

ESD Rating

Parameter	Item	Value	Unit
Electrostatic Discharge Voltage	Human body model (HBM), per MIL-STD-883J / Method 3015.9 ⁽¹⁾	±1 500	V
	Charged device model (CDM), per ESDA/JEDEC JS-002-2014 ⁽²⁾	±1 000	
	Machine model (MM), per JESD22-A115C	±400	

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process. Manufacturing with less than 500-V HBM is possible if necessary precautions are taken.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process. Manufacturing with less than 250-V CDM is possible if necessary precautions are taken.

Electrical Characteristics

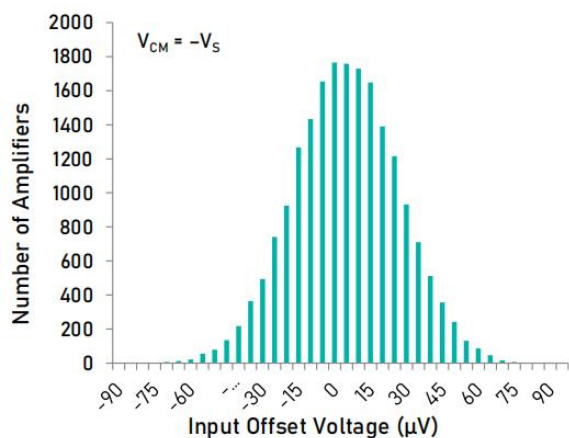
$V_S = 4.5\text{ V}$ to 48 V , $T_A = +25\text{ }^{\circ}\text{C}$, $V_{CM} = V_S/2$, $V_O = V_S/2$, and $R_L = 10\text{ k}\Omega$ connected to $V_S/2$, unless otherwise noted. Boldface limits apply over the specified temperature range, $T_A = -40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
OFFSET VOLTAGE						
V _{OS}	Input offset voltage				±100	μV
V _{OS} TC	Offset voltage drift	T _A = −40 to +125 °C		±1		μV/°C
PSRR	Power supply rejection ratio	V _S = 4.5 to 44 V, V _{CM} = 0.1 V		2		μV/V
		T _A = −40 to +125 °C		6		
INPUT BIAS CURRENT						
I _B	Input bias current			20		pA
		T _A = +85 °C		150		
		T _A = +125 °C		600		
I _{OS}	Input offset current			10		pA
NOISE						
V _n	Input voltage noise	f = 0.1 to 10 Hz		4		μV _{P-P}
e _n	Input voltage noise density	f = 1 kHz		25		nV/√Hz
		f = 10 kHz		22		
I _n	Input current noise density	f = 1 kHz		5		fA/√Hz
INPUT VOLTAGE						
V _{CM}	Common-mode voltage range		−V _S		+V _S −2	V
CMRR	Common-mode rejection ratio	V _S = 40 V, V _{CM} = 0 to 38 V		110		dB
		V _{CM} = 0.1 to 38 V, T _A = −40 to +125 °C		98		
		V _S = 5.0 V, V _{CM} = 0 to 3 V		85		
		V _{CM} = 0.1 to 3 V, T _A = −40 to +125 °C		72		
INPUT IMPEDANCE						
C _{IN}	Input capacitance	Differential		2.0		pF
		Common mode		3.5		
OPEN-LOOP GAIN						
A _{VOL}	Open-loop voltage gain	V _S = 40 V, V _O = 0.1 to 39.9 V		116		dB
		T _A = −40 to +125 °C		112		
		V _S = 5 V, V _O = 0.1 to 4.9 V		101		
		T _A = −40 to +125 °C		97		
FREQUENCY RESPONSE						
GBW	Gain bandwidth product			1.1		MHz
SR	Slew rate	V _S = 40 V, G = +1, 10 V step		0.8		V/μs

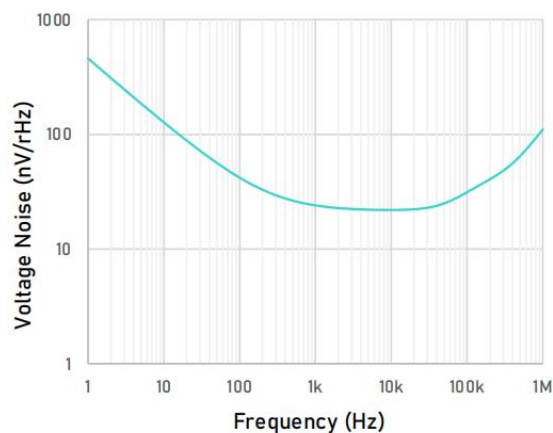
THD+N	Total harmonic distortion + noise	G = +1, f = 1 kHz, V _O = 3 VRMS		0.02		%
t _s	Settling time	To 0.1%, V _S = 40 V, G = +1, 5 V step		6		μs
		To 0.01%, V _S = 40 V, G = +1, 5 V step		12		
t _{OR}	Overload recovery time	V _{IN} × Gain > V _S		3		μs
OUTPUT						
V _{OH}	High output voltage swing	V _S = ±20 V, R _L = 10 kΩ		+V _S -97		mV
		V _S = ±20 V, R _L = 2 kΩ		+V _S -257		
V _{OL}	Low output voltage swing	V _S = ±20 V, R _L = 10 kΩ		-V _S +52		mV
		V _S = ±20 V, R _L = 2 kΩ		-V _S +232		
I _{SC}	Short-circuit current			±40		mA
POWER SUPPLY						
V _S	Operating supply voltage	T _A = -40 to +125 °C	4.5		48	V
I _Q	Quiescent current (per amplifier)	V _S = 5 V		150		μA
		V _S = 40 V		220		
THERMAL CHARACTERISTICS						
θ _{JA}	Package Thermal Resistance	SOT-23-5		190		°C/W
		MSOP-8		201		
		SOP-8		125		
		DFN-8		65		
		SOP-14		115		
		TSSOP-14		112		

Typical Performance Characteristics

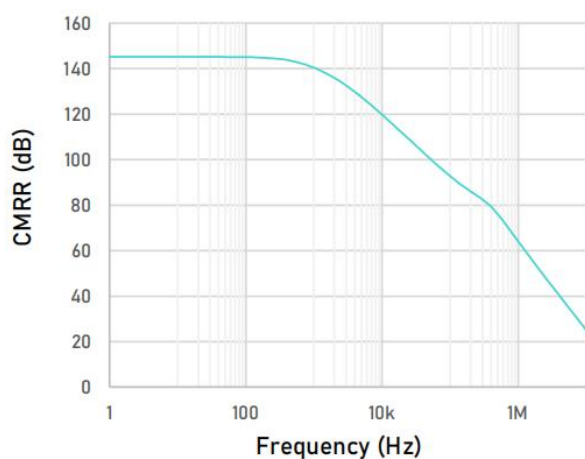
At $T_A = +25\text{ }^{\circ}\text{C}$, $V_{CM} = V_S / 2$, and $R_L = 10\text{ k}\Omega$ connected to $V_S / 2$, unless otherwise noted.



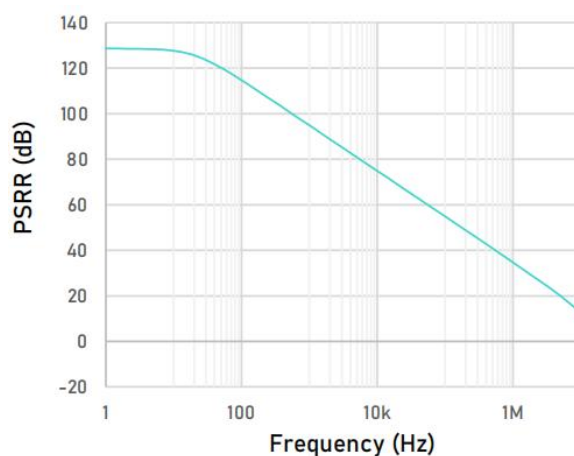
Offset Voltage Production Distribution



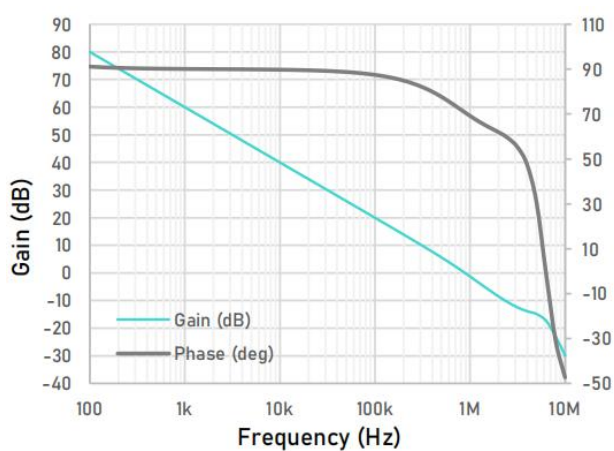
Input Voltage Noise Spectral Density as a function of Frequency



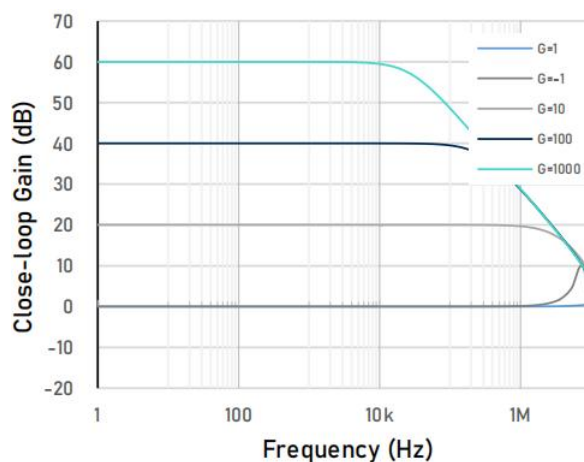
CMRR as a function of Frequency



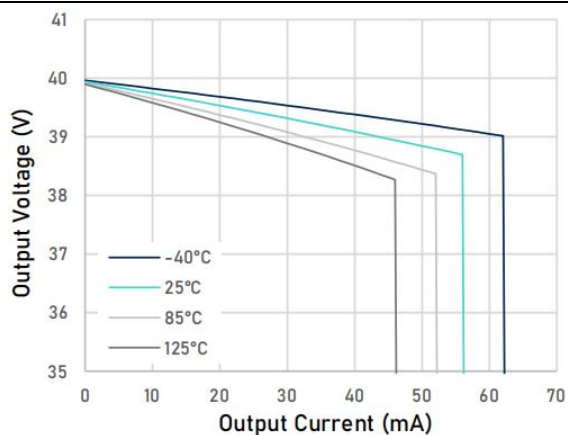
PSRR as a function of Frequency



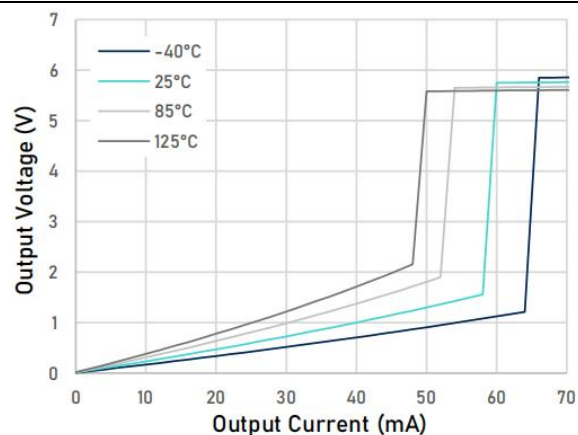
Open-loop Gain and Phase as a function of Frequency



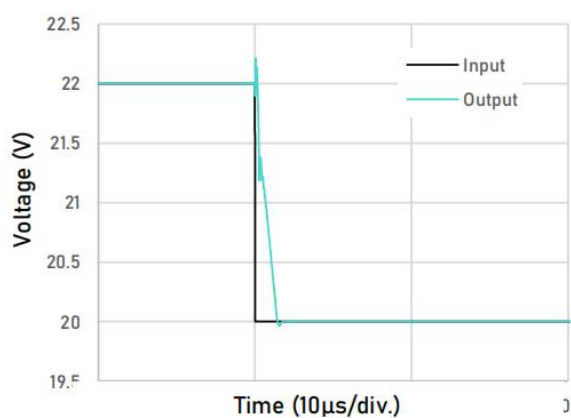
Close-loop Gain as a function of Frequency



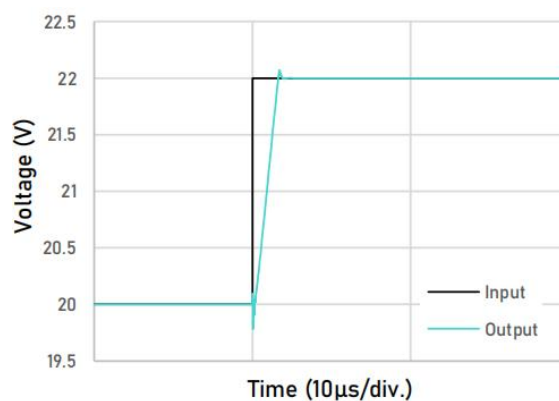
Output Voltage Swing as a function of Output Current
(Sourcing, $V_S = 40\text{ V}$)



Output Voltage Swing as a function of Output Current
(Sinking, $V_S = 40\text{ V}$)



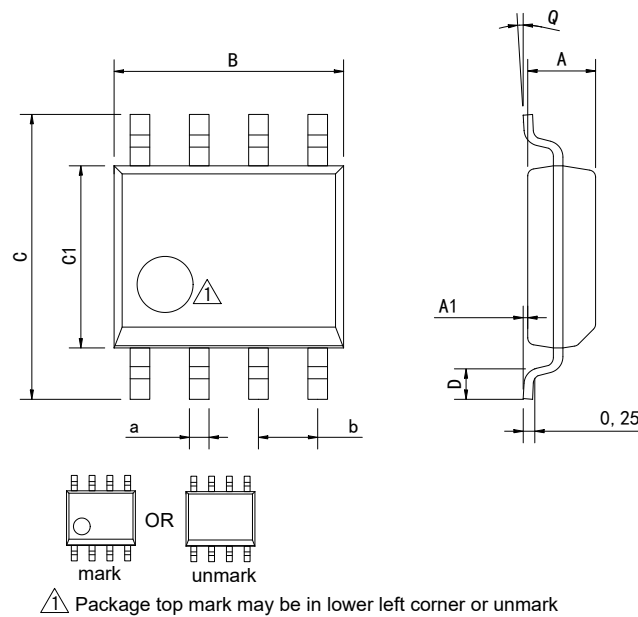
Large-Signal Step Response(Failing)



Large-Signal Step Response(Rising)

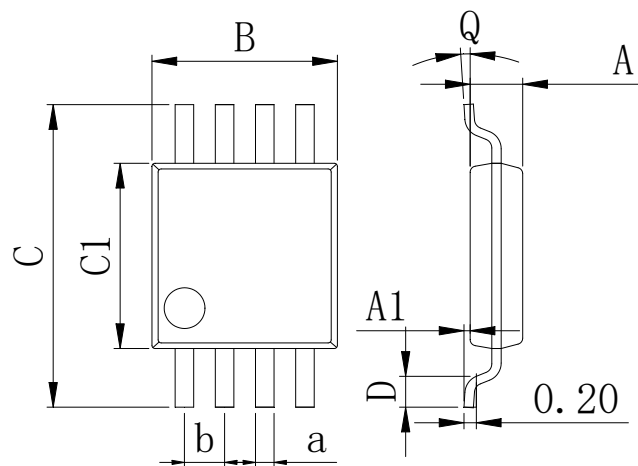
Physical Dimensions

SOP-8



Dimensions In Millimeters(SOP-8)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	

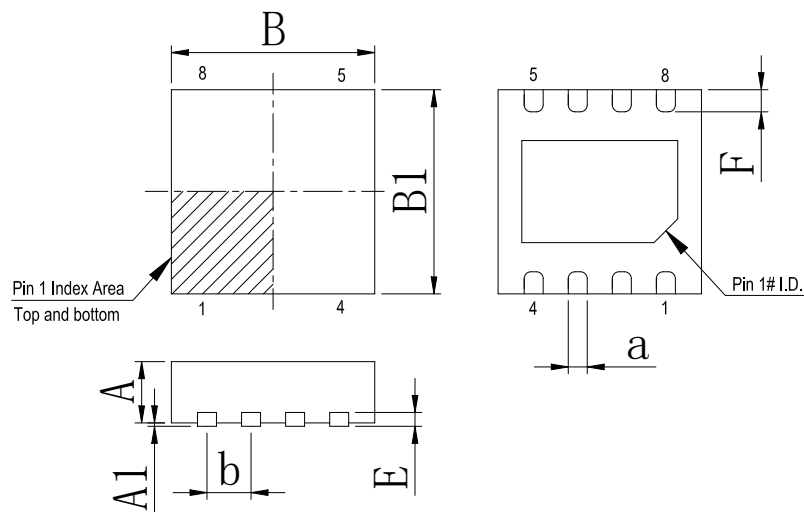
MSOP-8



Dimensions In Millimeters(MSOP-8)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65 BSC
Max:	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	

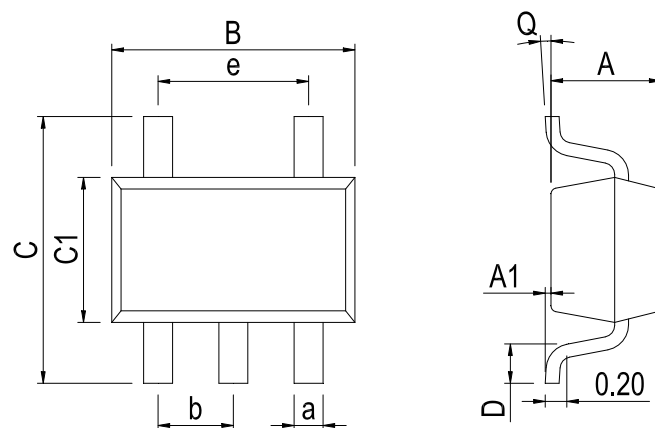
Physical Dimensions

DFN-8 3*3



Dimensions In Millimeters(DFN-8 3*3)								
Symbol:	A	A1	B	B1	E	F	a	b
Min:	0.85	0.00	2.90	2.90	0.20	0.30	0.20	0.65 BSC
Max:	0.95	0.05	3.10	3.10	0.25	0.50	0.34	

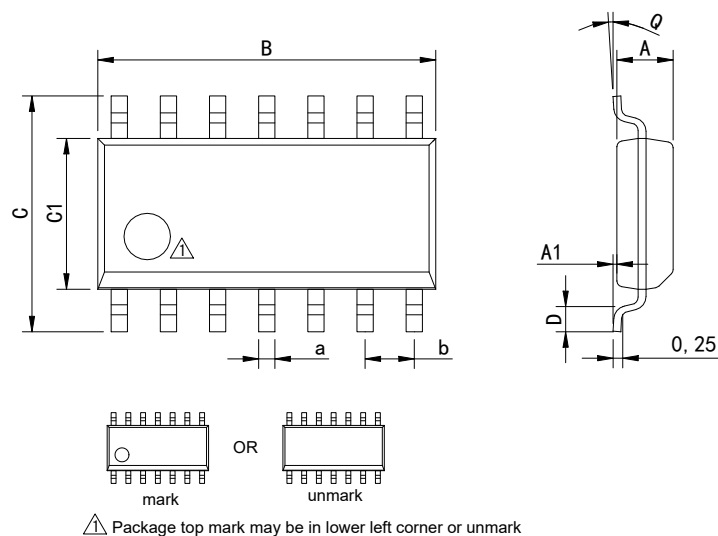
SOT-23-5



Dimensions In Millimeters(SOT-23-5)										
Symbol:	A	A1	B	C	C1	D	Q	a	b	e
Min:	1.00	0.00	2.82	2.65	1.50	0.30	0°	0.30	0.95 BSC	1.90 BSC
Max:	1.15	0.15	3.02	2.95	1.70	0.60	8°	0.50		

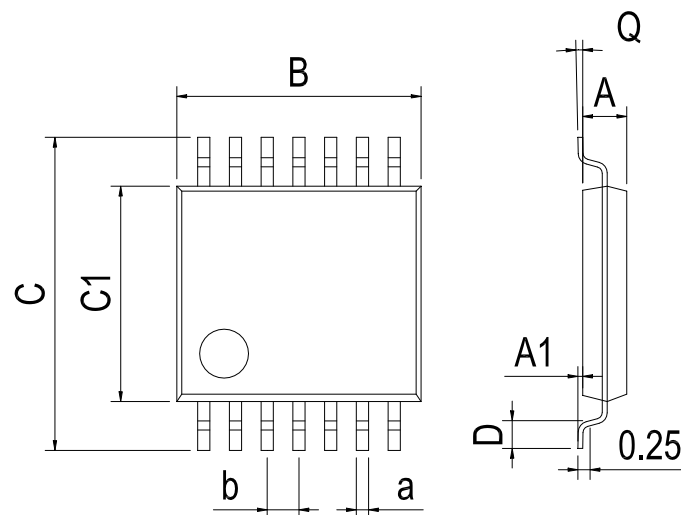
Physical Dimensions

SOP-14



Dimensions In Millimeters(SOP-14)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	8.55	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	8.75	6.20	4.00	0.80	8°	0.45	

TSSOP-14



Dimensions In Millimeters(TSSOP-14)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	0.85	0.05	4.90	6.20	4.30	0.40	0°	0.20	0.65 BSC
Max:	0.95	0.20	5.10	6.60	4.50	0.80	8°	0.25	

Revision History

REVISION NUMBER	DATE	REVISION	PAGE
V1.0	2018-11	New	1-10
V1.1	2025-6	Document Reformatting	1-12

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