

1N5819W

DATASHEET

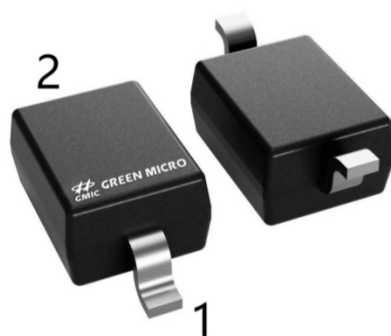
Specification Revision History:

Version	Date	Description
V1.0	2021/06	New
V1.1	2023/05	Modify Ordering Information
V1.2	2024/02	Modify Ordering Information
V1.3	2025/05	Add application precautions and overall typesetting.

FEATURES

- ※ Schottky Barrier Diode
- ※ Low Forward Voltage Drop
- ※ Guard Ring Construction for Transient Protection
- ※ Negligible Reverse Recovery Time
- ※ Low Capacitance

TOP VIEW AND PINNING



1、Cathode

2、Anode

SOD-123

ORDERING INFORMATION

Product Model	Package Type	Packing	Packing Qty
1N5819W(GMIC)	SOD-123	REEL	3000PCS/REEL
1N5819W	SOD-123	REEL	3000PCS/REEL

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Parameter	Symbols	1N5819W	1N5818W	1N5817W	Units
Peak Repetitive Reverse Voltage	V_{RRM}	40	30	20	V
RMS reverse voltage	V_{RMS}	28	21	14	V
Working Peak Reverse Voltage	V_{DC}	40	30	20	V
Peak Forward Surge Current, 8.3ms single half sine-wave superimposed on rated load (JEDEC method)	I_{FSM}	13			A
Maximum Instantaneous Forward Voltage $I_F = 20\text{ mA}$ $I_F = 200\text{ mA}$	V_F	0.37 0.60			V
Power Dissipation	P_D	400			mW
Reverse current $V_R = 30\text{ V}$ $V_R = 20\text{ V}$ $V_R = 10\text{ V}$	I_R	5	–	–	uA
		–	5	–	
		–	–	5	
Thermal Resistance, Junction to Ambient Air	$R_{\theta JA}$	300			°C/W
Reverse voltage $I_R = 100\text{ uA}$	$V_{(BR)}$	1N5819W	40		V
		1N5818W	30		
		1N5817W	20		
Reverse recovery time $I_F = I_R = 200\text{ mA}$, $I_{rr} = 0.1 \times I_R$, $R_L = 100\Omega$	t_{rr}	10			ns
Forward Continuous Current	I_{FM}	350			mA
Total capacitance $V_R = 0\text{ V}$, $f = 1\text{ MHz}$	C_{tot}	28			pF
Junction temperature	T_j	125			°C
Storage temperature	T_{stg}	-55 ~ +150			°C

TYPICAL CHARACTERISTICS

Fig.1 Forward Current Derating Curve

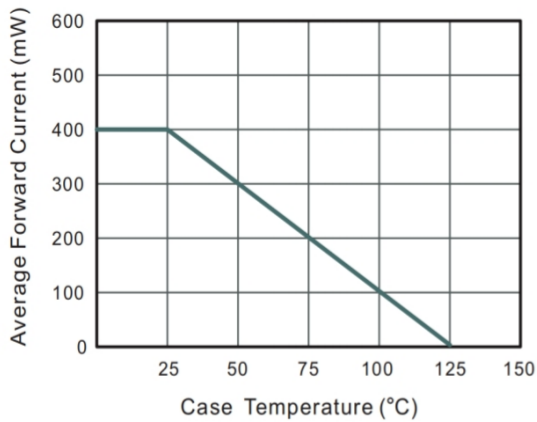


Fig.2 Typical Reverse Characteristics

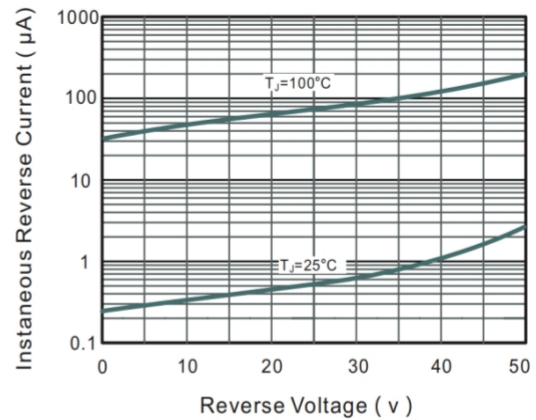


Fig.3 Forward Characteristics

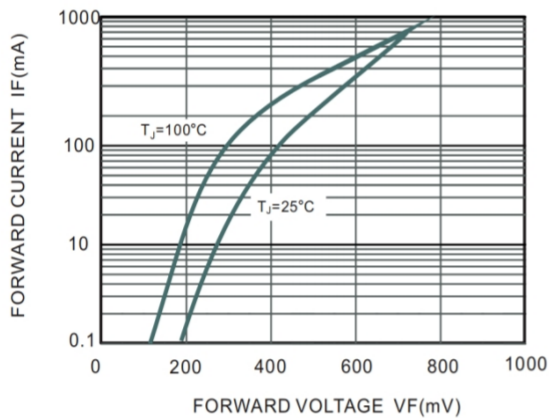


Fig.4 Maximum Non-Repetitive Peak Forward Surge Current

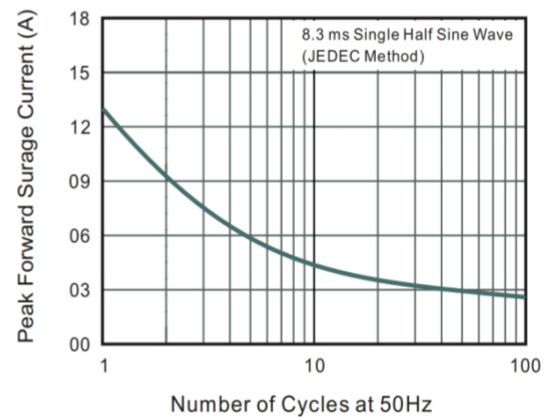


Fig.5 Typical Junction Capacitance

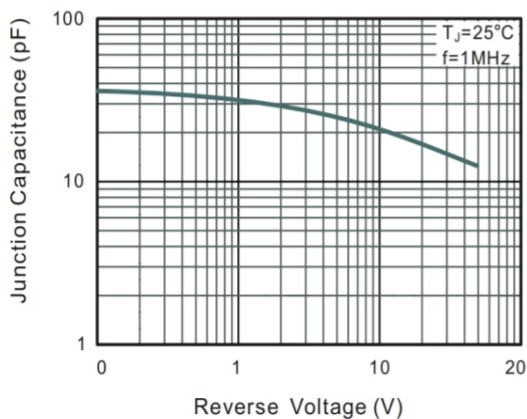
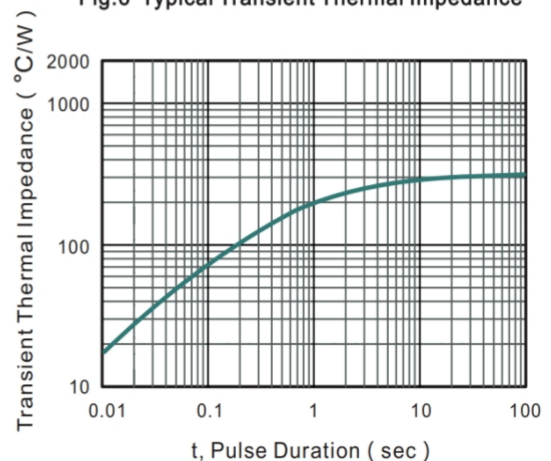
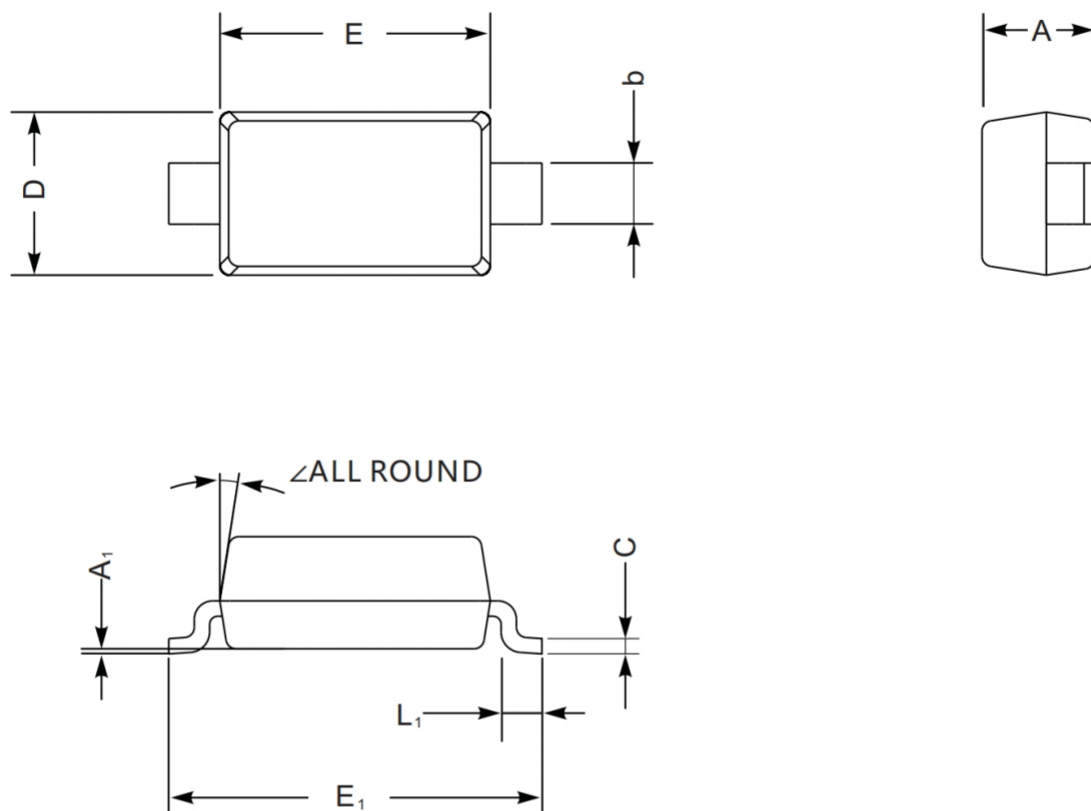


Fig.6 Typical Transient Thermal Impedance



Outline Dimensions

SOD-123



Symbol	Dimensions In Millimeters		Dimensions In Mil	
	Min	Max	Min	Max
A	0.90	1.30	35	51
C	0.09	0.22	3.5	8.7
D	1.50	1.80	59	71
E	2.50	2.80	98	110
E ₁	3.60	3.90	142	154
L ₁	0.25	0.45	10	18
b	0.50	0.70	20	28
A ₁	—	0.20	—	8
∠	9°			

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