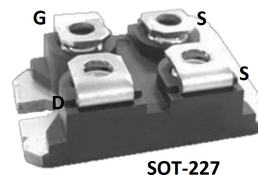


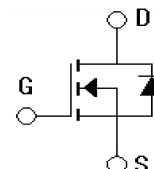
Features

- N-Channel, Low $R_{DS(on)}$
- High Current Handling Capability
- Fast Intrinsic Diode
- Avalanche Rated



Applications

- DC-DC Converters
- DC-AC Inverters
- Battery Chargers
- Switch-Mode and Resonant-Mode Power Supplies
- DC Choppers
- Temperature and Lighting Controls



Absolute Ratings ($T_c=25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	1000	V
Transient Gate-Source Voltage	V_{GSM}	± 30	V
Continuous Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current -continuous	I_D	50	A
Drain Current - pulse(note 1)	I_{DM}	110	A
Single Pulsed Avalanche Energy(note 2)	E_{AS}	4	J
Peak Diode Recovery voltage slope(note 3)	dv/dt	50	V/ns
Power Dissipation	P_D	900	W
Operating and Storage Temperature Range	T_j, T_{STG}	$-55 \sim +150$	$^\circ\text{C}$
Maximum Lead Temperature for Soldering Purposes	T_L	300	$^\circ\text{C}$

Electrical Characteristics ($T_{CASE}=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Tests conditions	Min	Type	Max	Unit
Off-Characteristics						
Drain-Source Voltage	BV_{DSS}	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	1000	-	-	V
Drain cut-off current	I_{DSS}	$V_{DS}=1000\text{V}, V_{GS}=0\text{V}$	-	-	50	μA
Gate-body leakage current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$	-	-	± 200	nA

On-Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	3.0	-	5.0	V
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =25A (note 4)	-	220	-	mΩ
Forward transconductance	G _{fs}	V _{DS} =20V, I _D =25A (note 4)	-	40	-	S
Dynamic Characteristics						
Input capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1MHz(note 5)	-	13	-	nF
Output capacitance	C _{oss}		-	1000	-	pF
Reverse transfer capacitance	C _{rss}		-	85	-	pF
Switching Characteristics						
Turn-On delay time	td(on)	V _{DD} =500V, I _D =25A Rg=0.5Ω	-	50	-	ns
Turn-On rise time	t _r		-	30	-	ns
Turn-Off delay time	T _{d(off)}		-	65	-	ns
Turn-Off Fall time	t _f		-	25	-	ns
Total Gate Charge	Q _g	V _{DD} =500V, I _D =25A V _{GS} =10V(note 5)	-	260	-	nC
Gate-Source charge	Q _{gs}		-	75	-	nC
Gate-Drain charge	Q _{gd}		-	113	-	nC
Drain-Source Diode Characteristics						
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =30A, (note 4)	-	0.84	1.2	V
Continuous Drain-Source Diode Forward Current	I _S		-	50	-	A
Reverse recovery time	t _{rr}	I _F =20A, dI _F /dt=-100A/us V _R =100V	-	300	-	ns
Reverse recovery charge	Q _{rr}		-	2	-	uC

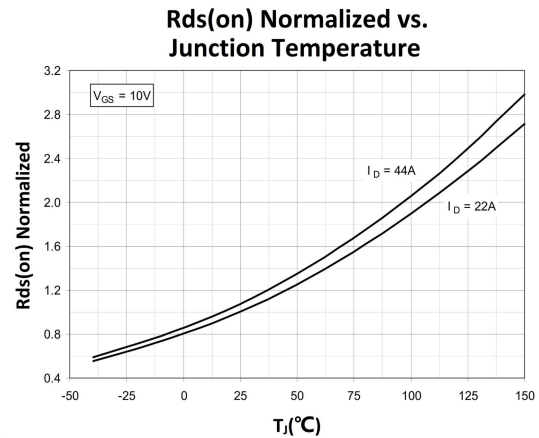
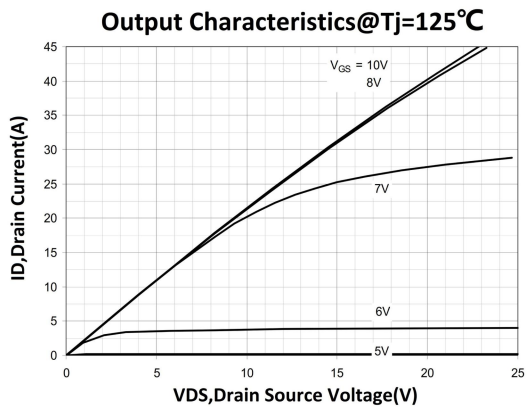
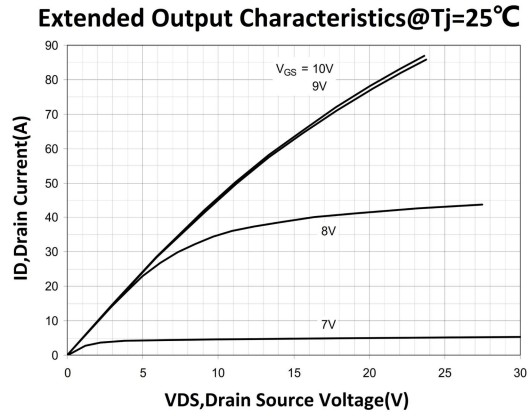
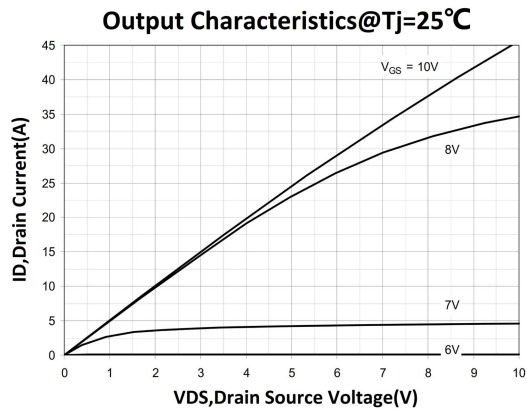
Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case, Max	$R_{th(j-c)}$	0.138	$^{\circ}C/W$
Thermal Resistance, Junction-to-Ambient, Max	$R_{th(j-a)}$	40	

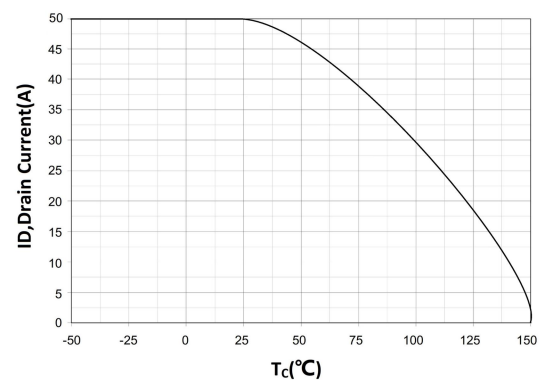
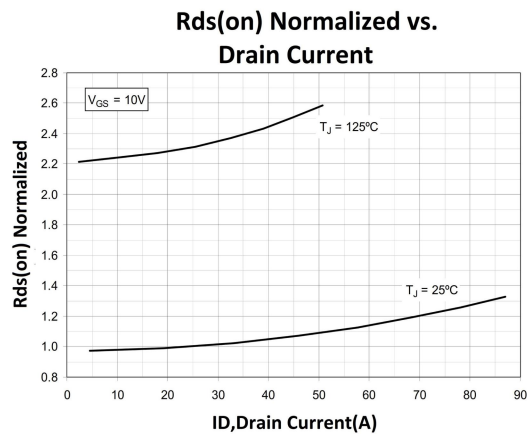
Notes:

- Pulse width limited by maximum junction temperature
- $L=19mH, I_{AS}=20A, V_{DD}=50V$, starting $T_J=25^{\circ}C, T_{Jmax} \leq 150^{\circ}C$
- $I_S \leq I_{DM}, V_{DD} \leq V_{DSS}, T_J \leq 150^{\circ}C$
- Pulse test, $t \leq 300\mu s$, duty cycle, $d \leq 2\%$.
- Essentially independent of operating temperature.

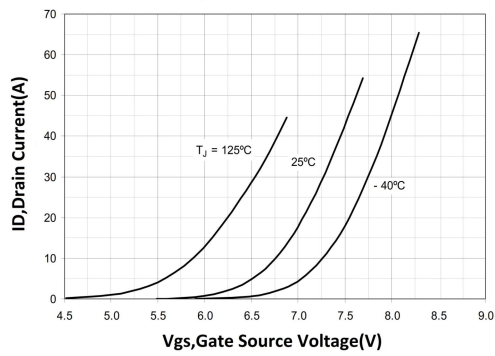
Typical Electrical and Thermal Characteristics (Curves)



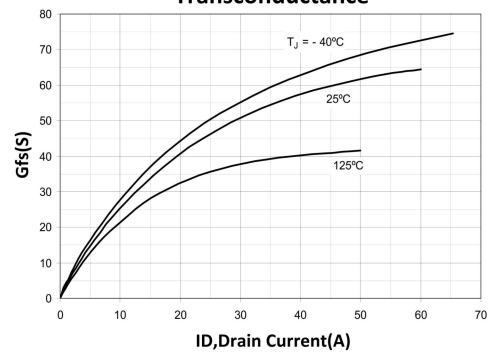
Maximum Drain Current vs. Case Temperature



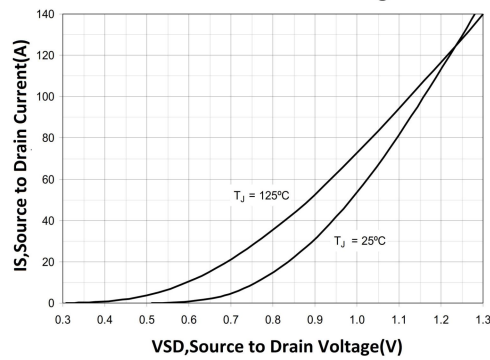
Input Admittance



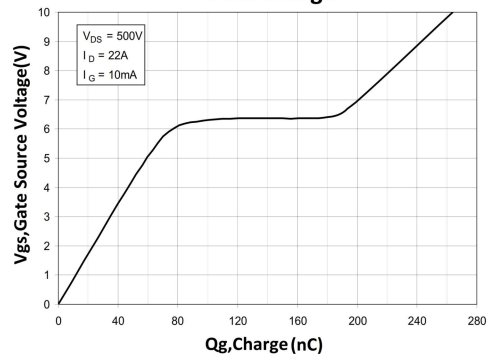
Transconductance



Diode Forward Voltage



Gate Charge



Capacitance

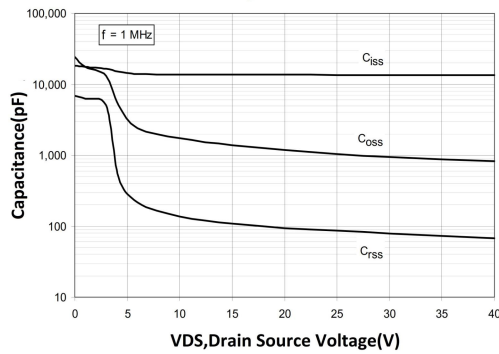
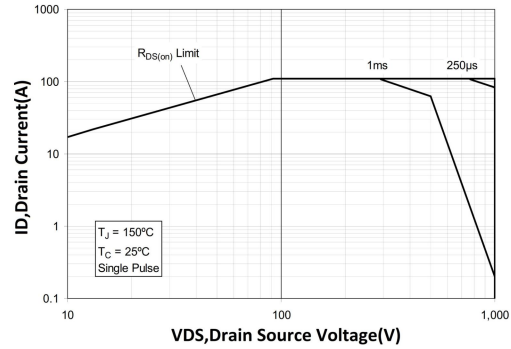
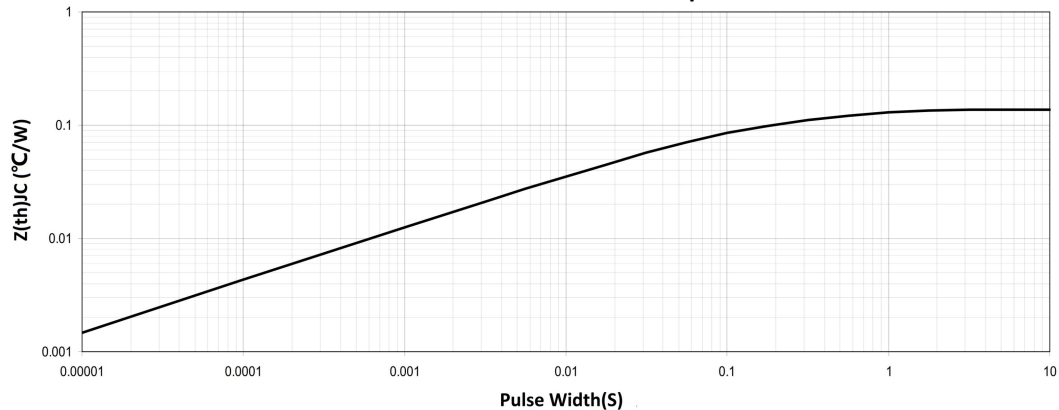


Fig. 12. Forward-Bias Safe Operating Area



Maximum Transient Thermal Impedance



Package Mechanical DATA

