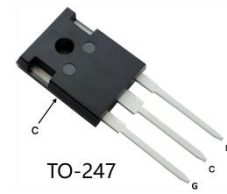


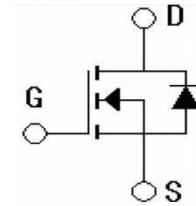
Features

- $V_{DS}=200V, I_D=100A$
 $R_{DS(on)} \leq 0.038\Omega @ V_{GS}=10V$
- High density cell design for ultra low $R_{DS(on)}$
- Low gate charge
- Improved dv/dt capability
- RoHS product



Applications

- Power switching application
- Isolated DC/DC converters in Telecom and Industrial
- Synchronous Rectification in DC/DC Converters



Absolute Ratings ($T_C=25^\circ C$)

Parameter	Symbol		Limit	Unit
Drain-Source Voltage	V _{DSS}		200	V
Transient Gate-Source Voltage	V _{GSM}		±30	V
Continuous Gate-Source Voltage	V _{GSS}		±20	V
Drain Current-continuous	I _D	I _C =25℃	100	A
		I _C =100℃	80	A
Drain Current-pulse (note 1)	I _{DM}		400	A
Single Pulsed Avalanche Energy (note 2)	E _{AS}		560	mJ
Maximum Power Dissipation	P _D , T _C =25℃ Derate above 25℃		250	W
			2.0	W/℃
Operating and Storage Temperature Range	T _J , T _{STG}		-55~+150	℃
Maximum Lead Temperature for Soldering Purposes	T _L		300	℃

Electrical Characteristics ($T_{CASE}=25^\circ C$ unless otherwise specified)

Parameter	Symbol	Tests conditions	Min	Typ.	Max	Units
Drain-Source Voltage	BV_{DSS}	$I_D=250\mu A, V_{GS}=0V$	200	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$	-	-	25	μA
Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
On-Characteristics						

Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	3	-	4.5	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{DS}=10V, I_D=50A$	-	-	0.038	Ω
Forward Transconductance	g_{fs}	$V_{DS}=50V, I_D=50A$ (note 4)	-	52	-	S
Dynamic Characteristics						
Input capacitance	C_{iss}	$V_{DS}=25V,$ $V_{GS}=0V,$ $f=1MHz$	-	4400	-	pF
Output capacitance	C_{oss}		-	734	-	pF
Reverse transfer capacitance	C_{rss}		-	78	-	pF

Electrical Characteristics($T_{CASE}=25^\circ C$ unless otherwise specified)

Parameter	Symbol	Tests conditions	Min	Typ	Max	Units
Switching-Characteristics						
Turn-On delay time	t _{d(on)}	V _{DD} = 100V, I _D =50A, R _G = 1.8Ω V _{GS} = 10V (note 4,5)	-	76.6	-	ns
Turn-On rise time	t _r		-	120.6	-	ns
Turn-Off delay time	t _{d(Off)}		-	239	-	ns
Turn-Off rise time	t _f		-	184	-	ns
Total Gate Charge	Q _g	V _{DS} =200V, I _D =50A, V _{GS} =10V (note 4,5)	-	123	-	nC
Gate-Source charge	Q _{gs}		-	29.4	-	nC
Gate-Drain charge	Q _{gd}		-	71.25	-	nC
Drain-Source Diode Characteristics and Maximum Ratings						
Continuous Drain-Source Diode Forward Current	V _{SD}	V _{GS} =0V, I _S =100A	-	-	1.3	V
Reverse recovery time	T _{rr}	I _S =50A, di/dt=100A/μs	-	267	-	ns
Reverse recovery charge	Q _{rr}		-	3.2	-	uC

Thermal Characteristic

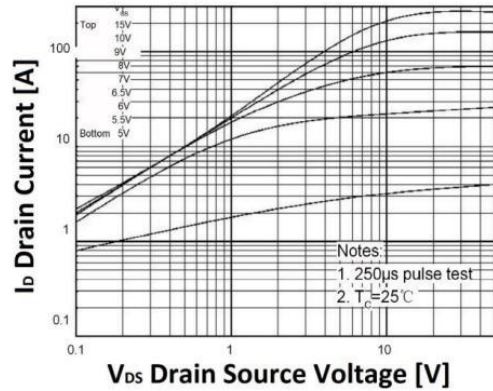
Parameter	Symbol	Value	Unit
Thermal Resistance, junction to Case	$R_{th(j-C)}$	0.50	$^\circ C/W$
Thermal Resistance, Junction to Ambient	$R_{th(j-A)}$	40	$^\circ C/W$

Notes:

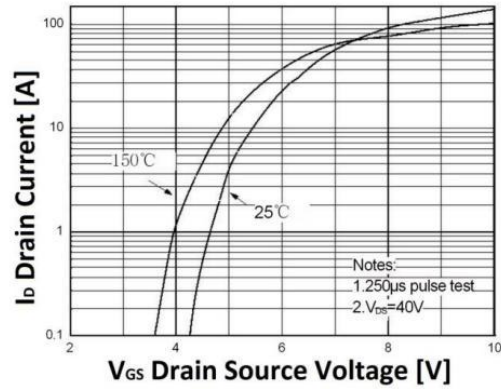
1. Repetitive rating; pulse width limited by max. junction temperature.
2. $I_{AS}=50A, L=1.5mH, R_G=25\Omega$, Starting $T_J=25^\circ C$
3. $I_{SD} \leq 50A, di/dt = 486A/\mu s, V_{DD} \leq BV_{DSS}$, Starting $T_J \leq 150^\circ C$
4. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

Electrical Characteristics

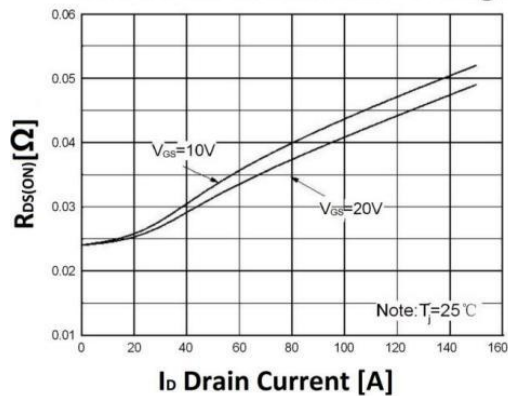
On-Region Characteristics



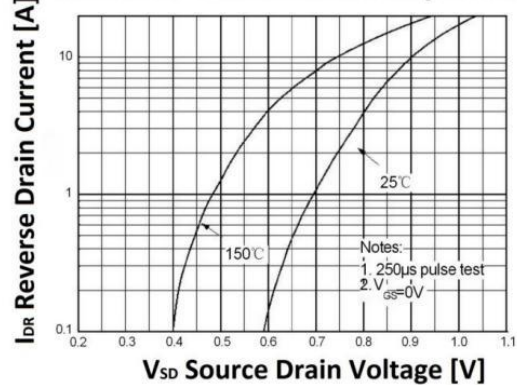
Transfer Characteristics



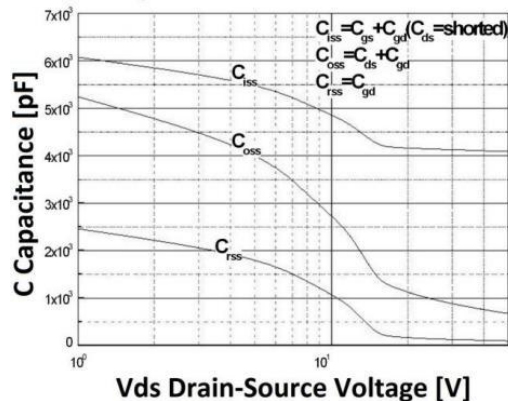
On-Resistance Variation vs. Drain Current and Gate Voltage



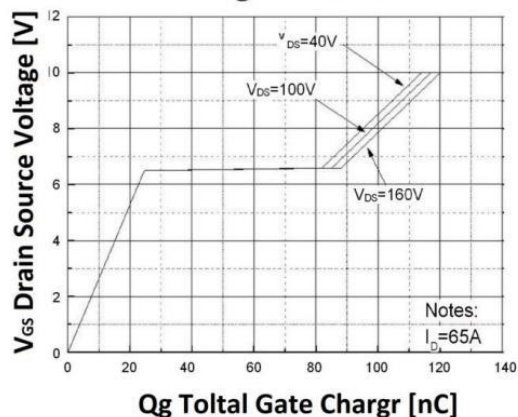
Body Diode Forward Voltage Variation vs. Source Current and Temperature

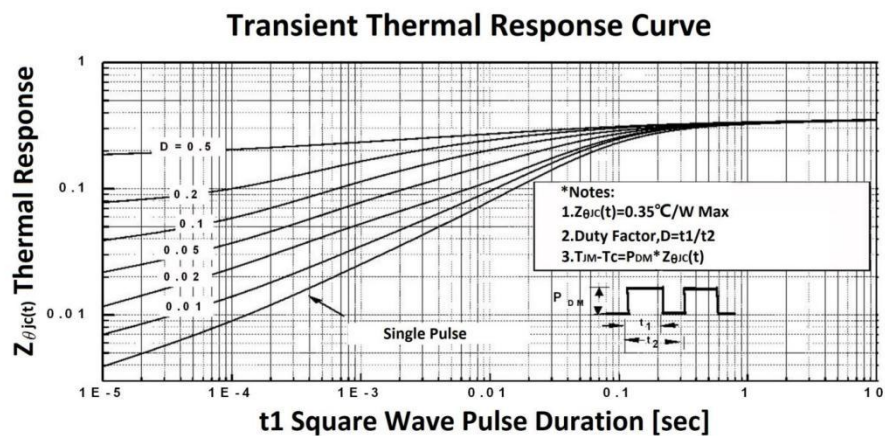
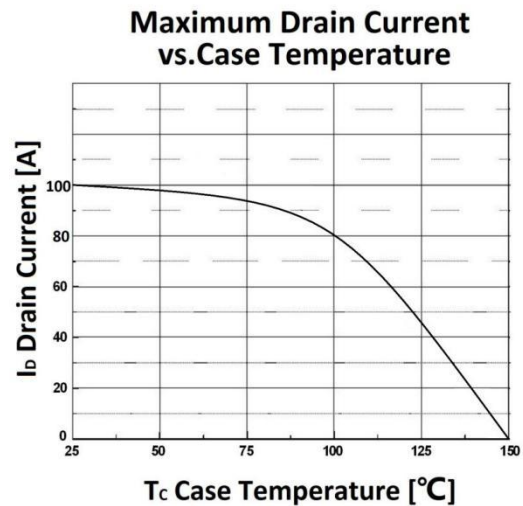
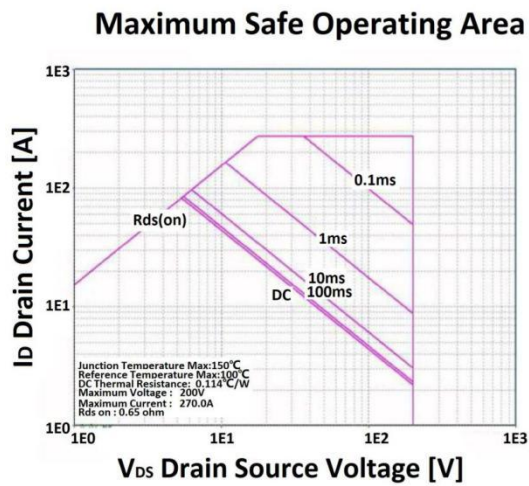
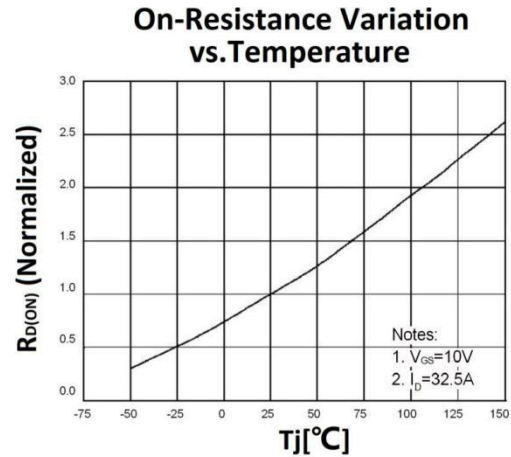
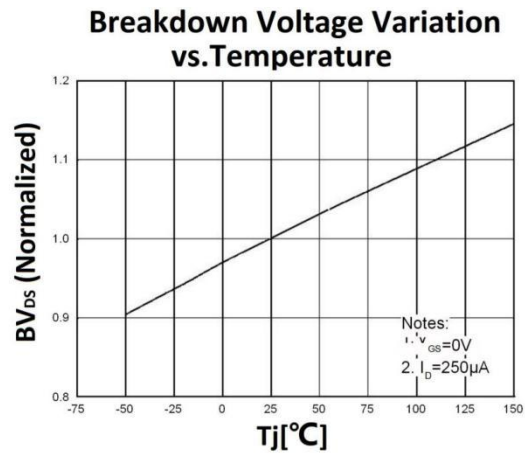


Capacitance Characteristics



Gate Charge Characteristics





Package Mechanical DATA

