

1. Description

BL6N120, the silicon N-channel Enhanced MOSFETs, is obtained by advanced MOSFET technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor is suitable device for SMPS, high speed switching and general purpose applications.

KEY CHARACTERISTICS

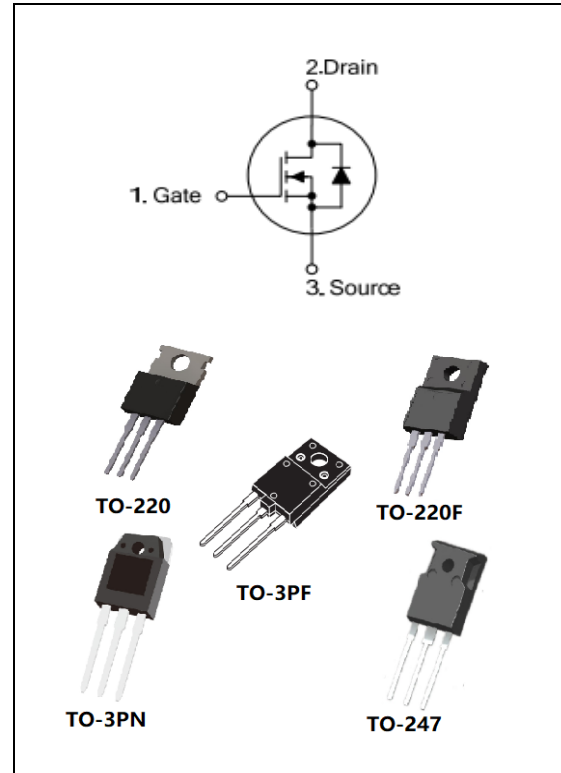
Parameter	Value	Unit
$V_{DS@Tj,max}$	1300	V
I_D	6	A
$R_{DS(ON).Typ}$	2.3	Ω

FEATURES

- Fast Switching
- Low C_{rss}
- 100% avalanche tested
- Improved dv/dt capability
- RoHS product

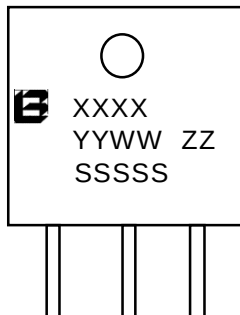
APPLICATIONS

- High frequency switching mode power supply



ORDERING INFORMATION

Ordering Codes	Package	Product Code	Packing
BL6N120-P	TO-220	6N120	Tube
BL6N120-A	TO-220F		Tube
BL6N120-W	TO-3PN		Tube
BL6N120-K	TO-3PF		Tube
BL6N120-F	TO-247		Tube

BL6N120-A (2) Package type (1) Chip name (1) BL6N120:1200V 6A (2) A:TO-220F P:TO-220 W:TO-3PN K:TO-3PF F:TO-247	 XXXX: Product Code YYWW: Year&Week ZZ: Assembly Code SSSS: Lot Code
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2. ABSOLUTE RATINGS

 at $T_C = 25^{\circ}\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Units
V_{DS}	Drain-to-Source Voltage	1200	V
I_D	Continuous Drain Current	6	A
	Continuous Drain Current $T_C = 100^{\circ}\text{C}$	3.5	A
I_{DM}	Pulsed Drain Current(Note1)	24	A
V_{GS}	Gate-to-Source Voltage	± 30	V
E_{AS}	Single Pulse Avalanche Energy(Note2)	480	mJ
dv/dt	Peak Diode Recovery dv/dt (Note3)	5.0	V/ns
P_D	Power Dissipation TO-220, TO-3PN, TO-247	200	W
	Derating Factor above 25°C	1.6	W/ $^{\circ}\text{C}$
P_D	Power Dissipation TO-220F, TO-3PF	63	W
	Derating Factor above 25°C	0.5	W/ $^{\circ}\text{C}$
T_J, T_{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	$^{\circ}\text{C}$
T_L	Maximum Temperature for Soldering	300	$^{\circ}\text{C}$

3. Thermal characteristics

Thermal characteristics (No FullPAK) TO-220\TO-3PN

Symbol	Parameter	RATINGS	Units
$R_{\theta JC}$	Junction-to-Case	0.63	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Junction-to-Ambient	62.5	$^{\circ}\text{C}/\text{W}$

Thermal characteristics (FullPAK) TO-220F\TO-3PF

Symbol	Parameter	RATINGS	Units
$R_{\theta JC}$	Junction-to-Case	2	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Junction-to-Ambient	62.5	$^{\circ}\text{C}/\text{W}$

4. Electrical Characteristics

at $T_c = 25^\circ\text{C}$, unless otherwise specified

OFF Characteristics						
Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	1200	--	--	V
$\Delta BV_{DSS}/\Delta T_J$	Bvdss Temperature Coefficient	$I_D=250\mu A$, Reference 25°C	--	1.1	--	V/ $^\circ\text{C}$
I_{DSS}	Drain to Source Leakage Current	$V_{DS}=1200V$, $V_{GS}=0V$, $T_J=25^\circ\text{C}$	--	--	10	μA
		$V_{DS}=960V$, $V_{GS}=0V$, $T_J=125^\circ\text{C}$	--	--	500	μA
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS}=+30V$	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS}=-30V$	--	--	100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=10V$, $I_D=3A(\text{Note4})$	--	2.3	2.9	Ω
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D=250\mu A(\text{Note4})$	3.0	--	5.0	V

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
R_g	Gate resistance	$f=1.0\text{MHz}$	--	2.1	--	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V$ $V_{DS}=25V$ $f=1.0\text{MHz}$	--	1960	--	PF
C_{oss}	Output Capacitance		--	122	--	
C_{rss}	Reverse Transfer Capacitance		--	4.2	--	

Switching Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	ID =6A VDD =500V VGS = 10V RG =5Ω	--	21	--	ns
t_r	Rise Time		--	19	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	29	--	
t_f	Fall Time		--	36	--	
Q_g	Total Gate Charge	ID =6A VDD =960V VGS = 10V	--	38	--	nC
Q_{gs}	Gate to Source Charge		--	12	--	
Q_{gd}	Gate to Drain ("Miller")Charge		--	13	--	

Source-Drain Diode Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
I_S	Continuous Source Current (Body Diode)	TC=25 °C	--	--	6	A
I_{SM}	Maximum Pulsed Current (Body Diode)		--	--	24	A
V_{SD}	Diode Forward Voltage	IS=6A, VGS=0V(Note4)	--	--	1.2	V
T_{rr}	Reverse Recovery Time	IS=6A, Tj = 25°C dIF/dt=100A/us, VGS=0V	--	506	--	ns
Q_{rr}	Reverse Recovery Charge		--	3322	--	nC

Note1: Pulse width limited by maximum junction temperature

Note2: L=20mH, VDS=50V, Start TJ=25°C

Note3: ISD =6A, di/dt ≤100A/us, VDD≤BVDS, Start TJ=25°C

Note4: Pulse width tp≤300μs, δ≤2%

5. Characteristics Curves

Figure 1a Safe Operating Area (No FullPAK)

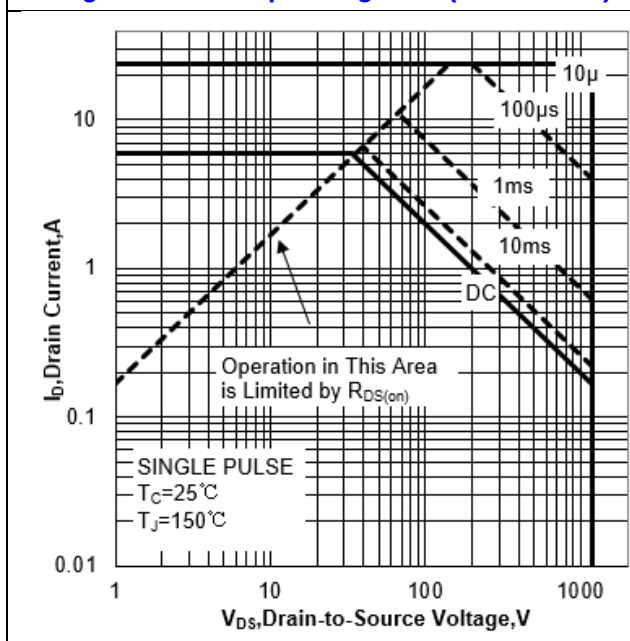


Figure 1b Safe Operating Area (FullPAK)

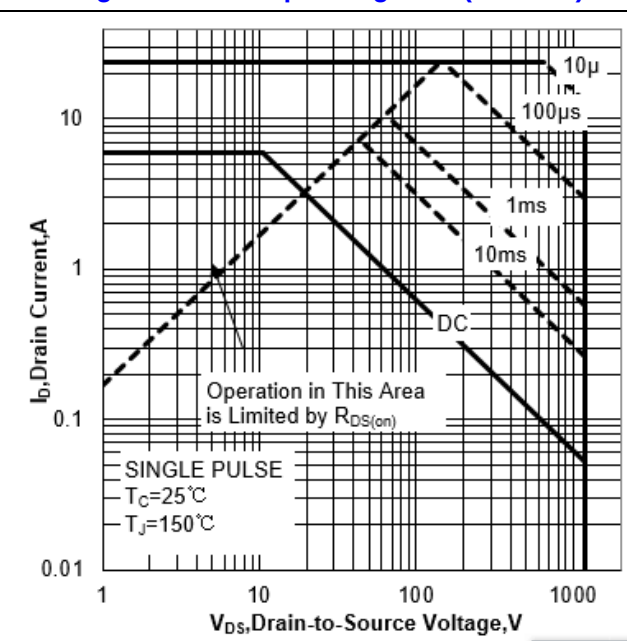


Figure 2a Power Dissipation (No FullPAK)

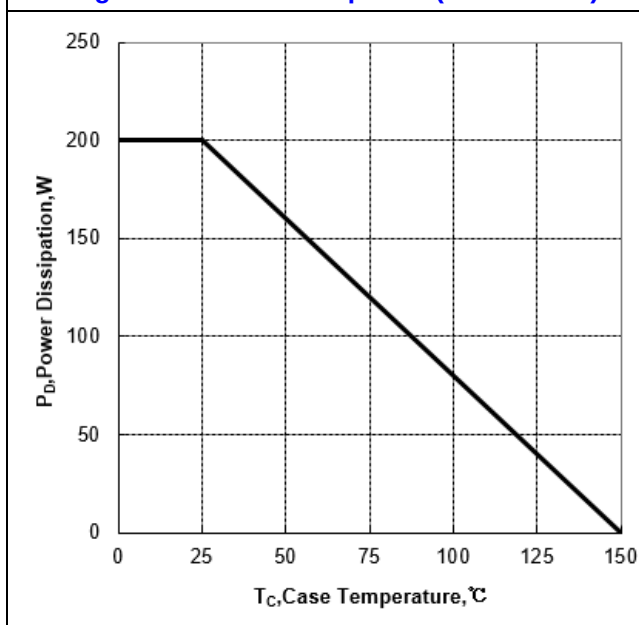


Figure 2b Power Dissipation (FullPAK)

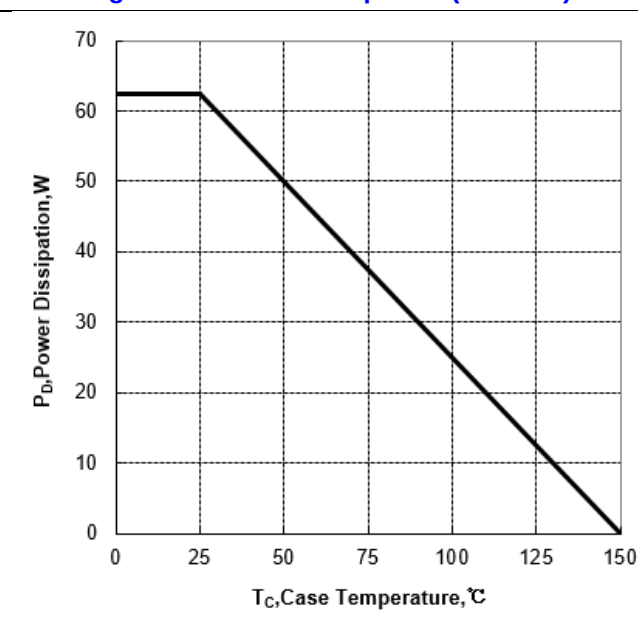


Figure 3a Max Thermal Impedance (No FullPAK)

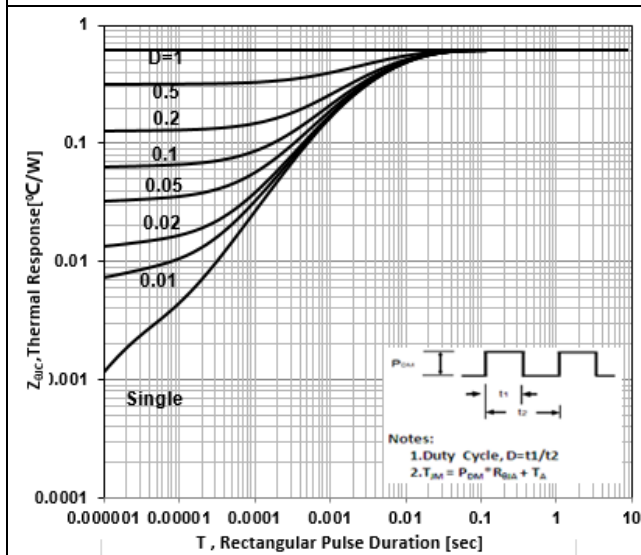


Figure 3b Max Thermal Impedance (FullPAK)

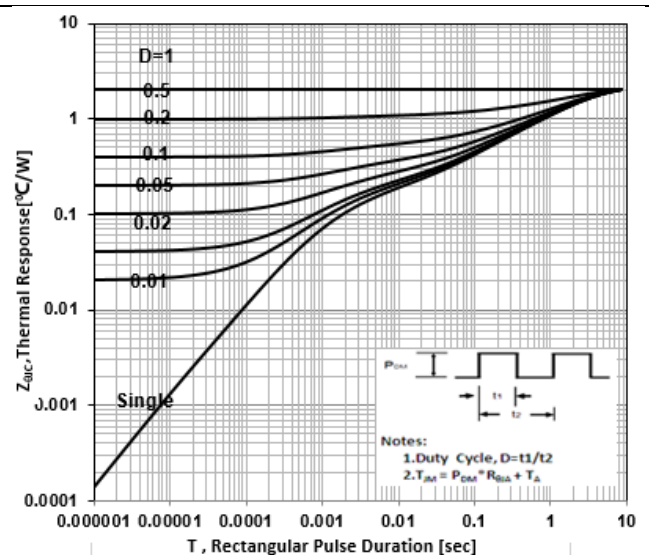


Figure 4 Typical Output Characteristics

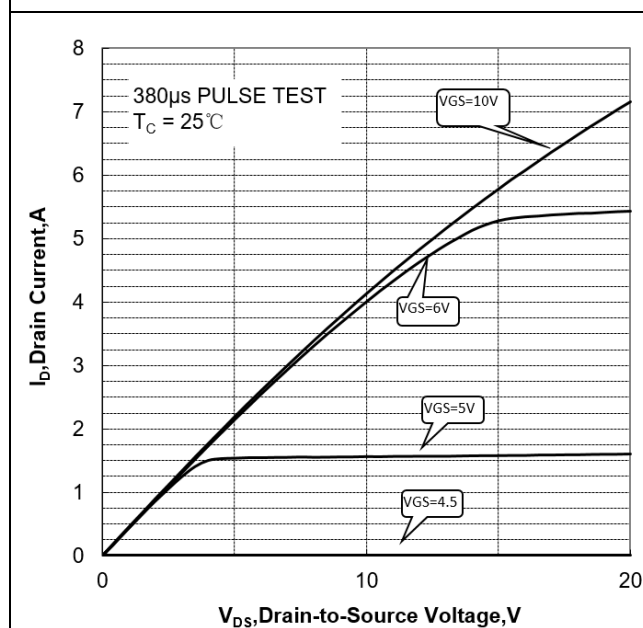


Figure 5 Typical Transfer Characteristics

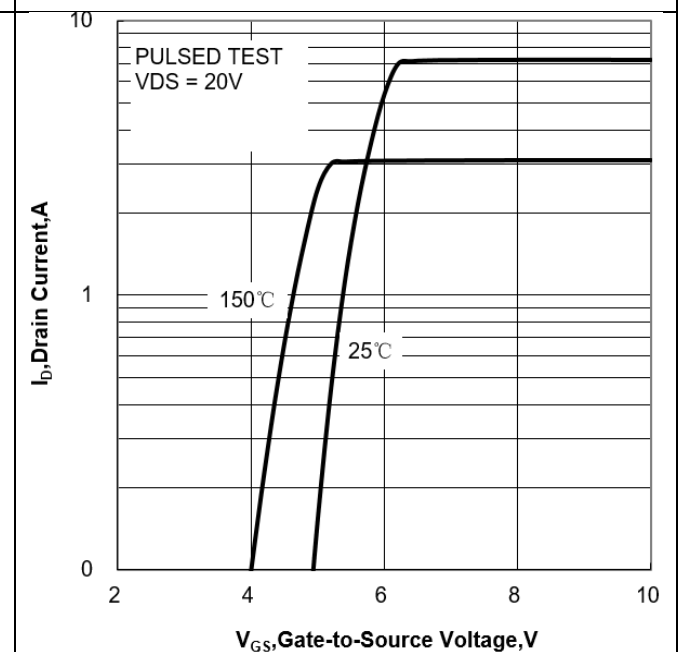


Figure 6 Typical Drain to Source ON Resistance vs Drain Current

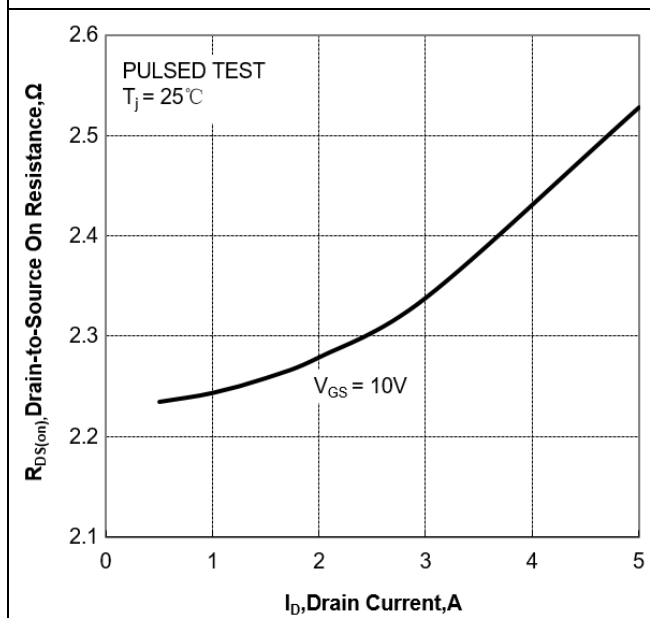


Figure 7 Typical Drain to Source on Resistance vs Junction Temperature

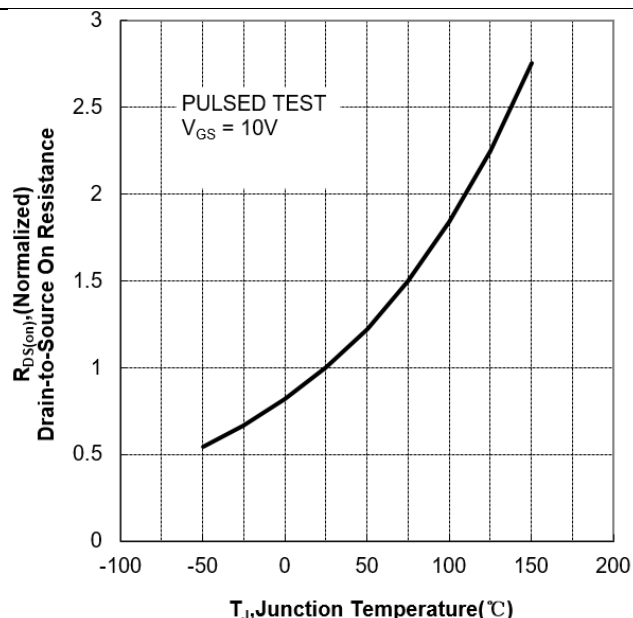


Figure 8 Typical Threshold Voltage vs Junction Temperature

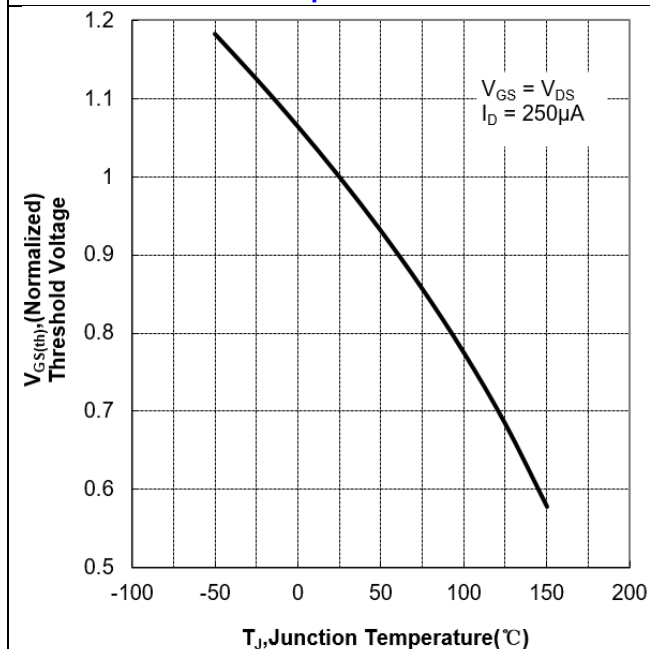


Figure 9 Typical Breakdown Voltage vs Junction Temperature

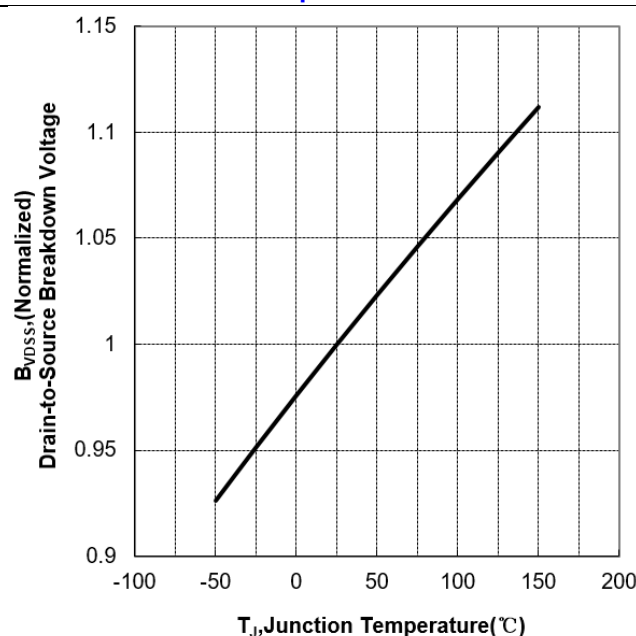


Figure 10 Typical Capacitance vs Drain to Source Voltage

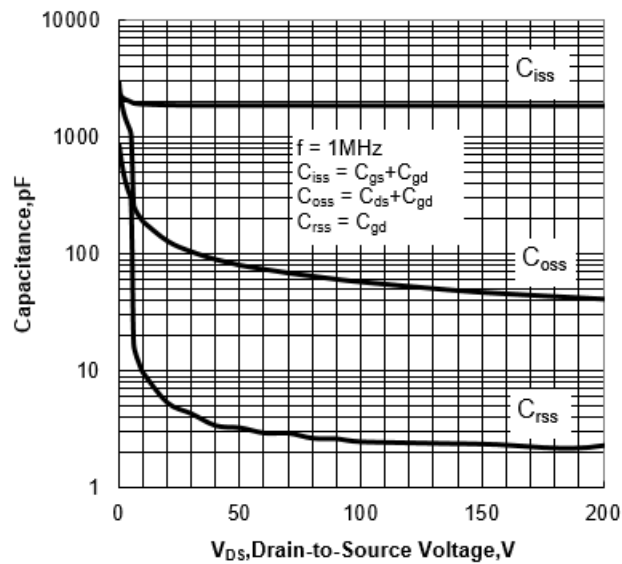
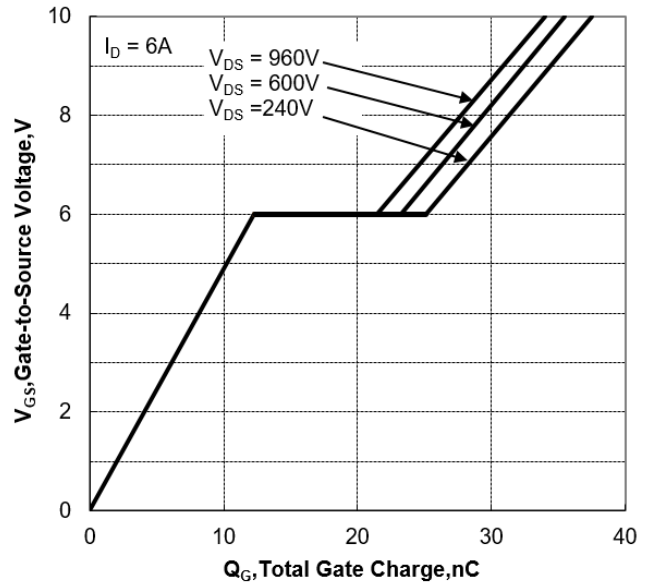
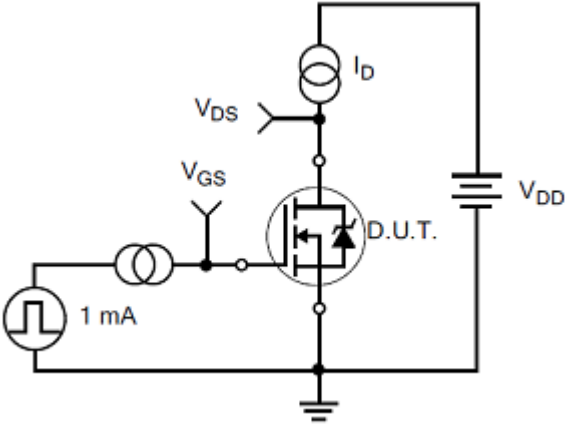
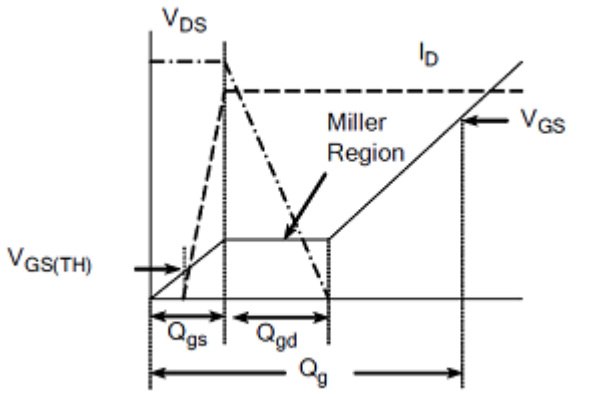
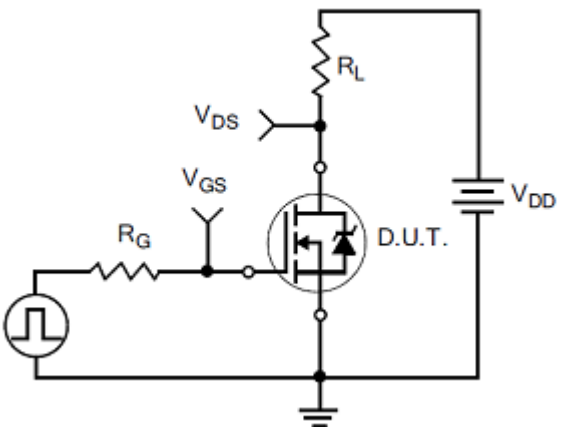
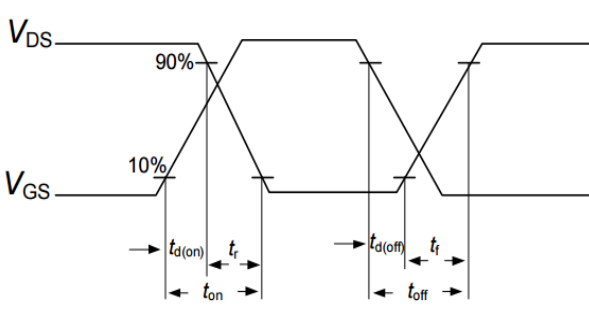


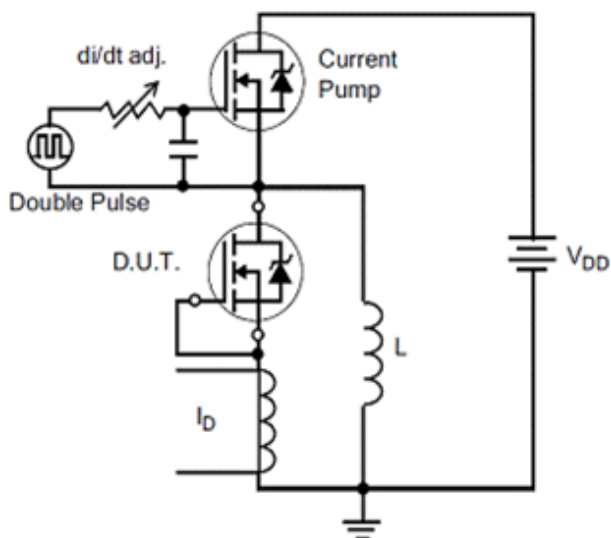
Figure 11 Typical Gate Charge vs Gate to Source Voltage



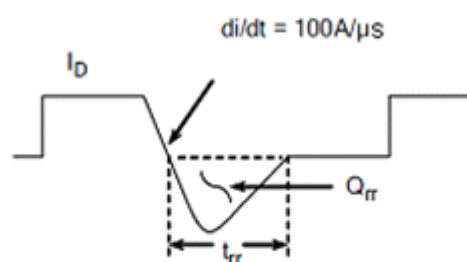
6. Test Circuit and Waveform

Gate Charge Test Circuit	Gate Charge Waveforms
 The diagram shows a MOSFET (D.U.T.) in a common-source configuration. The gate is driven by a 1 mA current source. The drain is connected to a load resistor and a drain current source I_D. The supply voltage is V_{DD}. The gate-source voltage is V_{GS} and the drain-source voltage is V_{DS}.	 The graph shows V_{DS} and I_D versus gate charge Q_g. The Miller Region is indicated. Key points include $V_{GS(TH)}$, Q_{gs}, Q_{gd}, and Q_g. The V_{GS} curve shows a plateau during the Miller region.
Resistive Switching Test Circuit	Resistive Switching Waveforms
 The diagram shows a MOSFET (D.U.T.) in a common-source configuration. The gate is driven by a current source through a gate resistor R_G. The drain is connected to a load resistor R_L and the supply voltage V_{DD}. The gate-source voltage is V_{GS} and the drain-source voltage is V_{DS}.	 The graph shows V_{DS} and V_{GS} versus time. Key timing parameters are labeled: $t_{d(on)}$, t_r, t_{on}, $t_{d(off)}$, t_f, and t_{off}. The V_{DS} waveform shows a 90% rise and 10% fall, while V_{GS} shows a 10% rise and 90% fall.

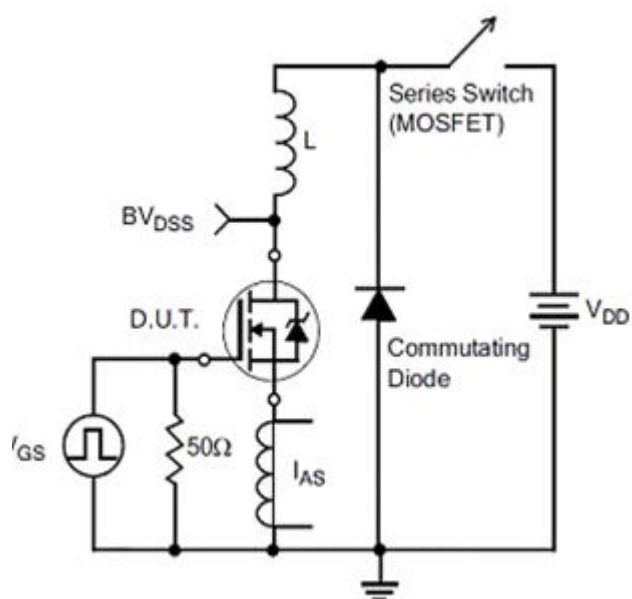
Diode Reverse Recovery Test Circuit



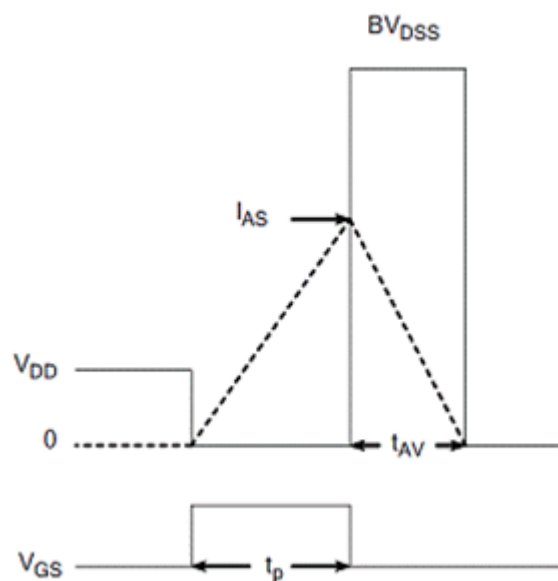
Diode Reverse Recovery Waveform



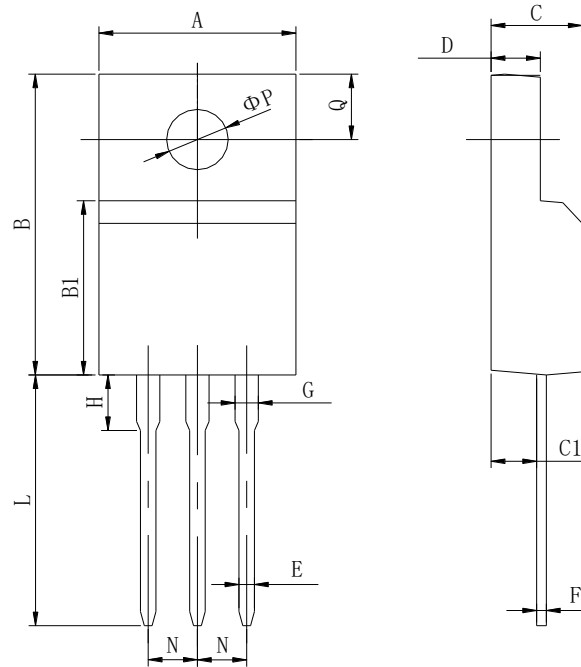
Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveform

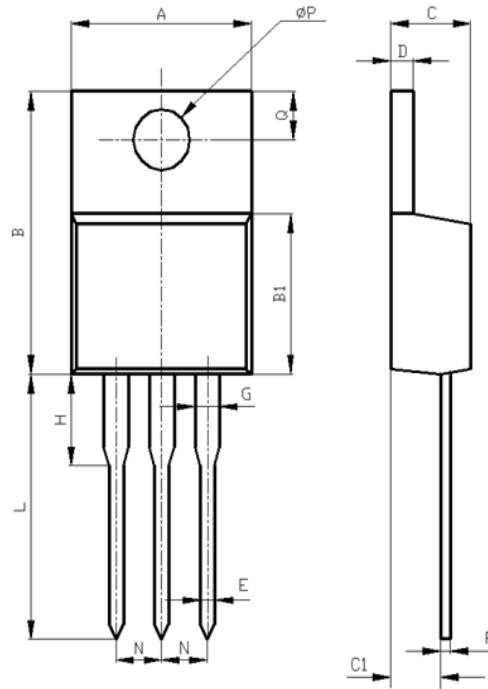


7. Package Description



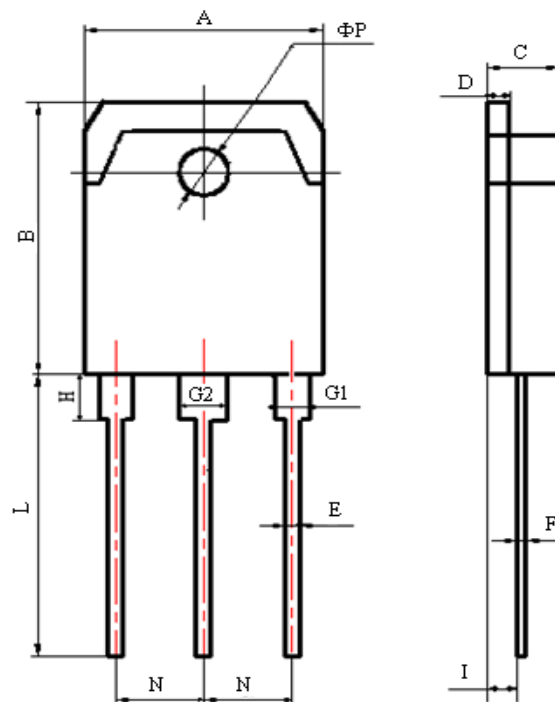
Items	Values(mm)	
	MIN	MAX
A	9.60	10.4
B	15.4	16.2
B1	8.90	9.50
C	4.30	4.90
C1	2.10	3.00
D	2.40	3.00
E	0.60	1.00
F	0.30	0.60
G	1.12	1.42
H	3.40	3.80
	1.60	2.90
L	12.0	14.0
N	2.34	2.74
Q	3.15	3.55
Φ P	2.90	3.30

TO-220F Package



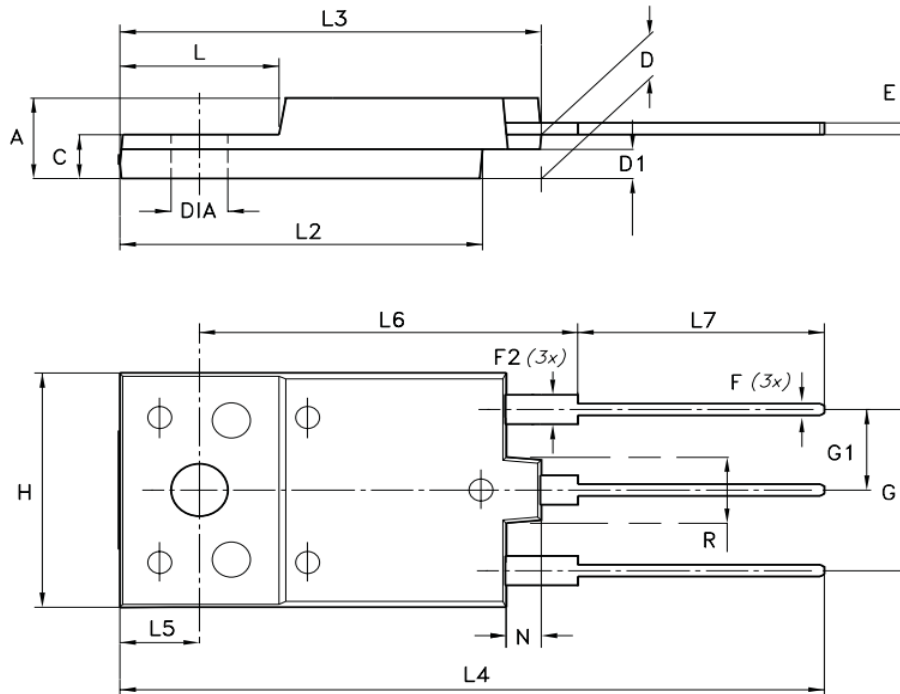
Items	Values(mm)	
	MIN	MAX
A	9.60	10.6
B	15.0	16.0
B1	8.90	9.50
C	4.30	4.80
C1	2.30	3.10
D	1.20	1.40
E	0.70	0.90
F	0.30	0.60
G	1.17	1.37
H	2.70	3.80
L	12.6	14.8
N	2.34	2.74
Q	2.40	3.00
Φ P	3.50	3.90

TO-220 Package



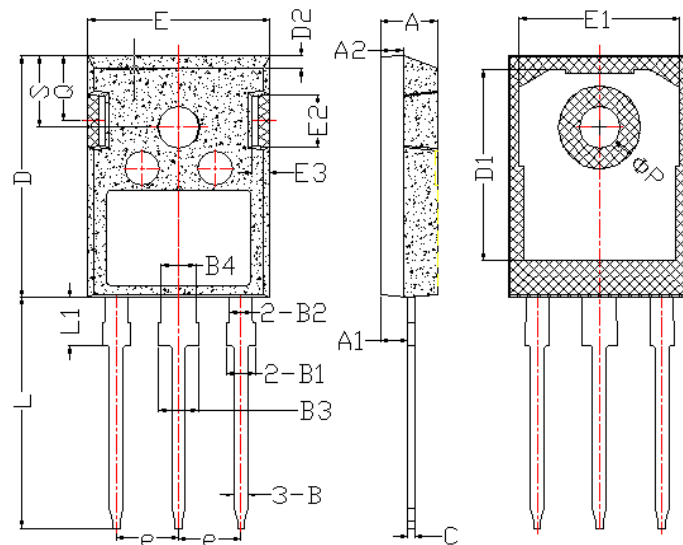
Items	Values(mm)	
	MIN	MAX
A	15.00	16.00
B	19.20	20.60
C	4.60	5.00
D	1.40	1.60
E	0.90	1.10
F	0.50	0.70
G1	2.00	2.20
G2	3.00	3.20
H	3.00	3.70
I	1.20	1.70
	2.70	2.90
L	19.00	21.00
N	5.25	5.65
Φ P	3.10	3.30

TO-3PN Package



DIM.	mm.		
	min.	typ	max.
A	5.30		5.70
C	2.80		3.20
D	3.10		3.50
D1	1.80		2.20
E	0.80		1.10
F	0.65		0.95
F2	1.80		2.20
G	10.30		11.50
G1		5.45	
H	15.30		15.70
L	9.80	10	10.20
L2	22.80		23.20
L3	26.30		26.70
L4	43.20		44.40
L5	4.30		4.70
L6	24.30		24.70
L7	14.60		15
N	1.80		2.20
R	3.80		4.20
Dia	3.40		3.80

TO-3PF Package



Items	Values(mm)	
	MIN	MAX
A	4.6	5.2
A1	2.2	2.6
B	0.9	1.4
B1	1.75	2.35
B2	1.75	2.15
B3	2.8	3.35
B4	2.8	3.15
C	0.5	0.7
D	20.60	21.30
D1	16	18
E	15.5	16.10
E1	13	14.7
E2	3.80	5.3
E3	0.8	2.60
e	5.2	5.7
L	19	20.5
L1	3.9	4.6
ΦP	2.5	3.70
Q	5.2	6.00
S	5.8	6.6

TO-247 Package

NOTE:

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shanghai Belling reserves the right to make changes in this specification sheet and is subject to change without prior notice.

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