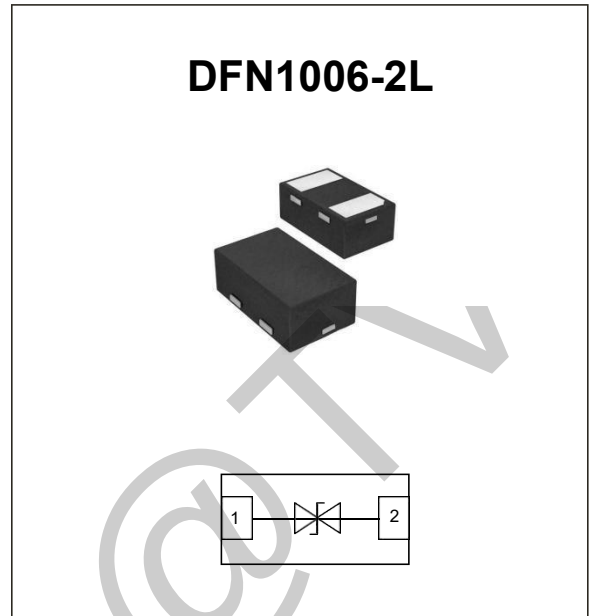


Features

- Ultra low capacitance: 0.6pF typical
- Ultra low leakage: nA level
- Operating voltage: 3.3V
- Low clamping voltage
- IEC 61000-4-2 (ESD): Air $\pm 30\text{kV}$, Contact $\pm 30\text{kV}$
- IEC 61000-4-5 (Lightning): 10A (8/20 μs)
- RoHS Compliant

Mechanical Data

- Package: DFN1006-2
- Lead Finish: Matte Tin
- Case Material: "Green" Molding Compound
- Terminal Connections: See Diagram Below



Applications

- Cellular Handsets and Accessories
- Display Ports
- MDDI Ports
- USB Ports
- Digital Visual Interface (DVI)
- PCI Express and Serial SATA Ports

Ordering information

Order code	Marking	Package	Packaging option	Base quantity	Packaging specification
DCE0310A10LCSB	AS	DFN1006-2	Tape and reel	10000pcs / reel	EIA STD RS-481

Absolute Maximum Ratings

Ratings at 25 °C, ambient temperature unless otherwise specified

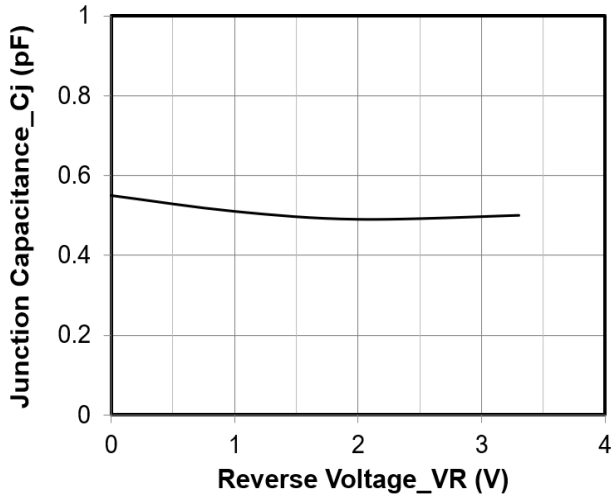
Parameter	Symbol	Value	Unit
Peak Pulse Power ($T_P=8/20\mu\text{s}$)	P_{PP}	50	W
ESD contact/air discharge (IEC-61000-4-2)	V_{ESD}	$\pm 30/\pm 30$	kV
Peak Pulse Current ($T_P = 8/20\mu\text{s}$)	I_{PP}	10	A
Junction Temperature	T_J	125	°C
Operation temperature	T_{OP}	-40 to +85	°C
Lead temperature	T_L	260	°C
Storage temperature	T_{STG}	-55 to +150	°C

Electrical Characteristics

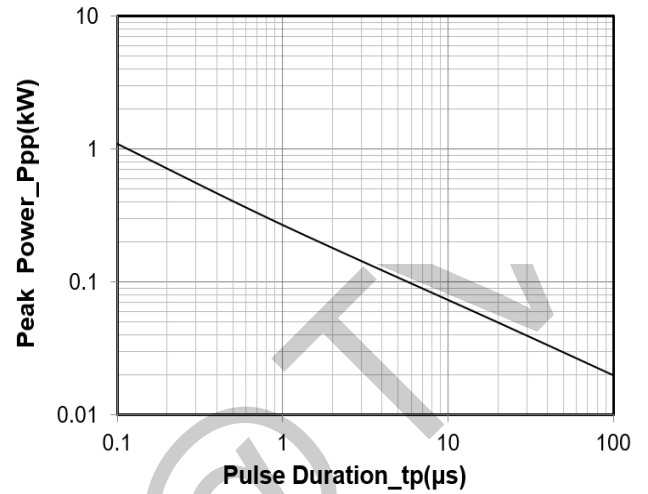
(T_A = 25 °C unless otherwise specified)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Reverse stand-off Voltage	V _{RWM}		-	-	3.3	V
Reverse Leakage Current	I _R	V _{RWM} = 3.3V	-	-	0.2	uA
Punch-Through Voltage	V _{pt}	I _T = 2 μ A	3.5	-	-	V
Snap-Back Voltage	V _{SB}	I _T = 50mA	0.8	-	-	V
Clamping Voltage(TLP)	V _{CL}	I _{PP} = 16A, t _p = 100ns	-	6	-	V
Dynamic resistance	R _{DYN}		-	0.25	-	Ω
Clamping Voltage(ESD)	V _{CE}	V _{ESD} = 8kV Contact	-	6	-	V
Clamping Voltage	V _C	I _{PP} = 1A, t _p = 8/20μs	-	1.5	-	V
Clamping Voltage	V _C	I _{PP} = 10A, t _p = 8/20μs	-	4	-	V
Junction Capacitance	C _J	V _R = 0V, f = 1MHz	-	0.6	-	pF

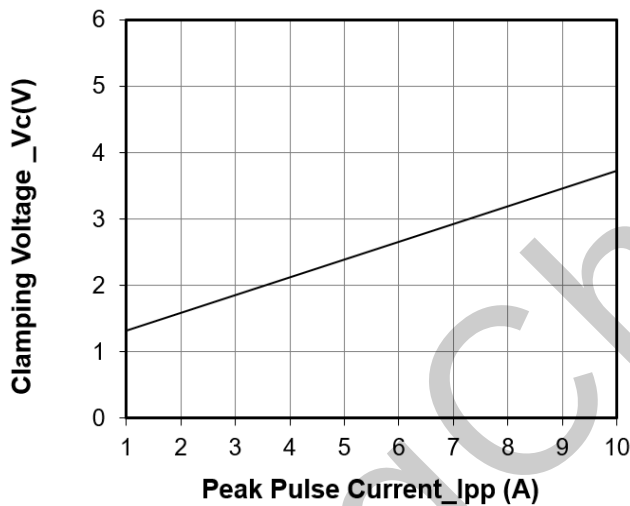
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



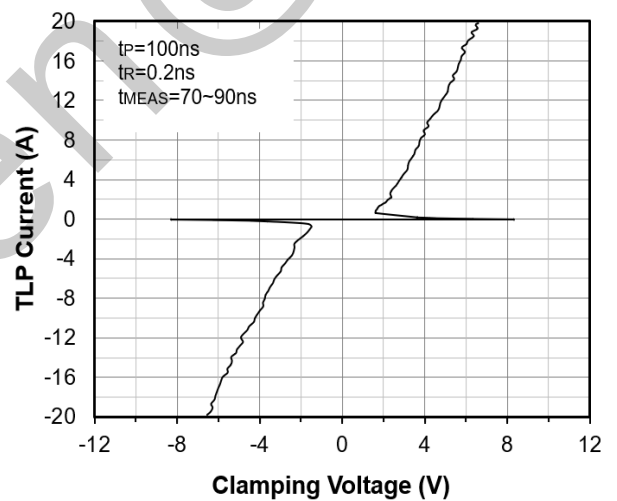
Junction Capacitance vs. Reverse Voltage



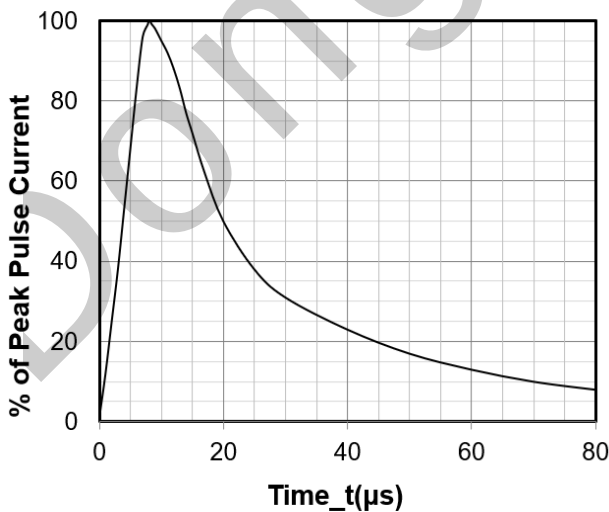
Peak Pulse Power vs. Pulse Time



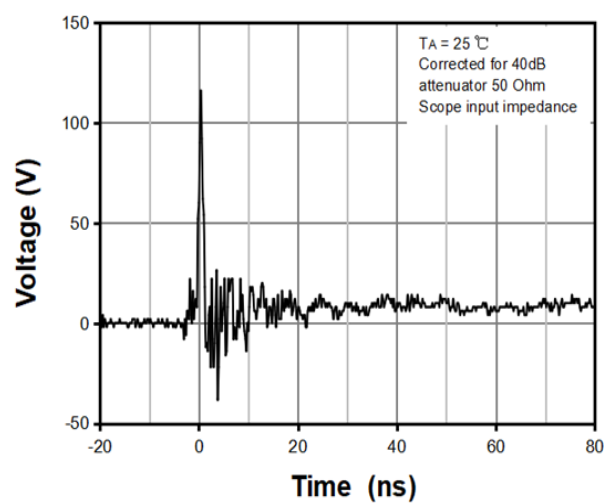
Clamping Voltage vs. Peak Pulse Current (tp = 8/20μs)



TLP Measurement



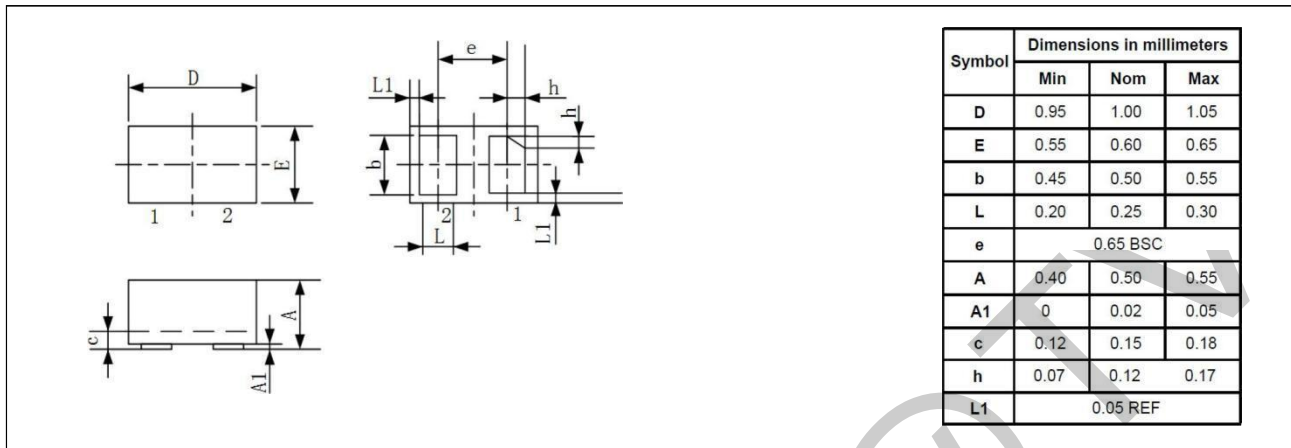
8 X 20μs Pulse Waveform



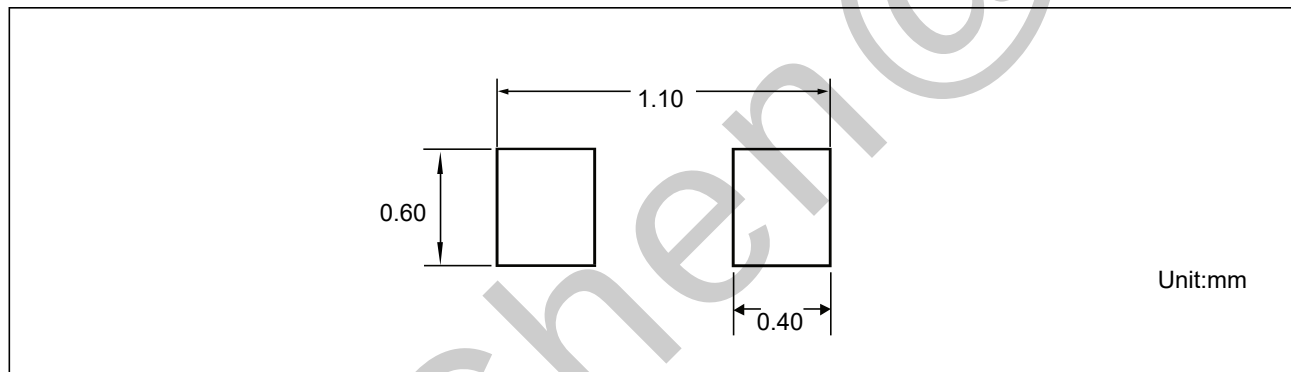
ESD Clamping Voltage

8 kV Contact per IEC61000-4-2

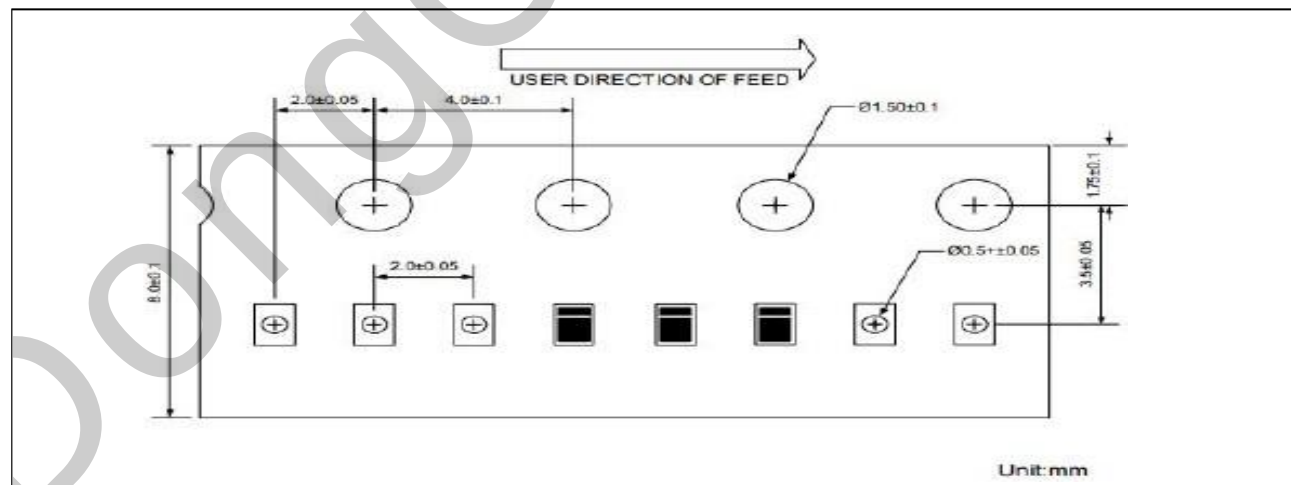
Package Dimensions



Pad Dimensions



Tape and Reel Specification



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Head Office

Contact Information:

DongChen Electronics Co.,Ltd.

Address: 2F., No.407, Sec. 2, Longgang Rd.,Zhongli Dist.,
Taoyuan City 32097, Taiwan (R.O.C.)

Tel: 03-284-1808

Fax: 03-284-1404

Mail:dc@dongchen.com.tw

Web:www.dongchen.com.tw