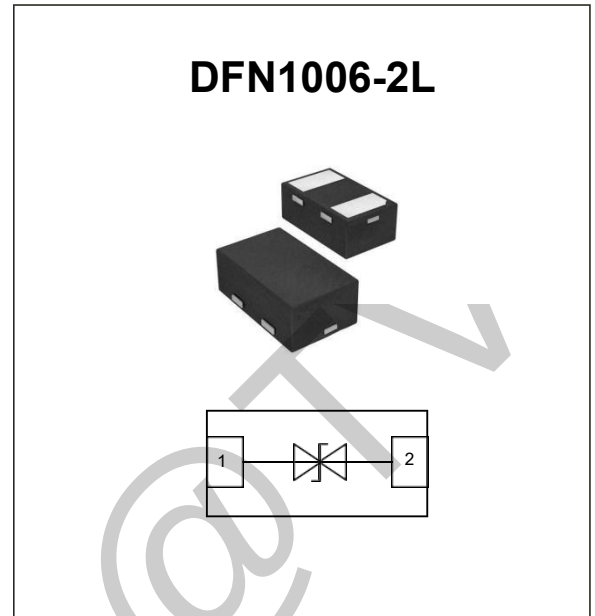


Features

- Reverse stand-off voltage: 5V max
- IEC 61000-4-2 (ESD): $\pm 30\text{kV}$ (contact and air discharge)
- IEC 61000-4-5 (surge): 5.5A (8/20 μs)
- Capacitance: $C_J = 15\text{pF}$ typ.
- Response Time < 1 ns
- Small DFN1006-2L package

Mechanical Data

- Package: DFN1006-2L
- Bi-directional TVS protection
- Marking: PB



Applications

- Cellular Handsets
- Tablets
- Laptops
- Other portable devices
- Network communication devices

Ordering information

Ordercode	Marking	Package	Packaging option	Base quantity	Packaging specification
DCE0510D2XY	PB	DFN1006-2L	Tape and reel	10000pcs / reel	EIA STD RS-481

Absolute Maximum Ratings

Ratings at 25 °C, ambient temperature unless otherwise specified

Parameter	Symbol	Value	Unit
Peak Pulse Power ($T_P=8/20\mu\text{s}$)	P_{PP}	66	W
ESD contact/air discharge (IEC-61000-4-2)	V_{ESD}	$\pm 30/\pm 30$	kV
Peak Pulse Current ($T_P = 8/20\mu\text{s}$)	I_{PP}	5.5	A
Junction Temperature	T_J	125	°C
Operation temperature	T_{OP}	-40~125	°C
Storage temperature	T_{STG}	-55 to +150	°C

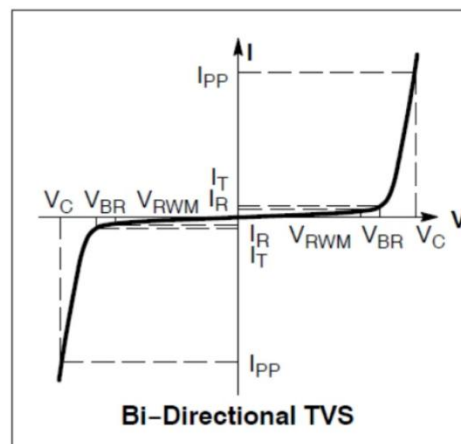
Electrical Characteristics

($T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Reverse stand-off Voltage	V_{RWM}		-	-	5	V
Holding Voltage	V_{BR}	$I_T = 1\text{mA}$	5.6	-	8.4	V
Reverse Leakage Current	I_R	$V_{RWM} = 5\text{V}$	-	-	0.1	μA
Clamping Voltage	V_C	$I_{PP} = 1\text{A}, t_p = 8/20 \mu\text{s}$	-	-	10	V
Clamping Voltage	V_C	$I_{PP} = 5.5\text{A}, t_p = 8/20 \mu\text{s}$	-	-	12	V
Junction Capacitance	C_J	$V_R = 0\text{V}, f = 1\text{MHz}$	-	15	18	pF

Electrical characteristics ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
P_{pk}	Peak Power Dissipation
C	Capacitance @ $V_R = 0$ and $f = 1.0\text{ MHz}$



Typical Characteristics

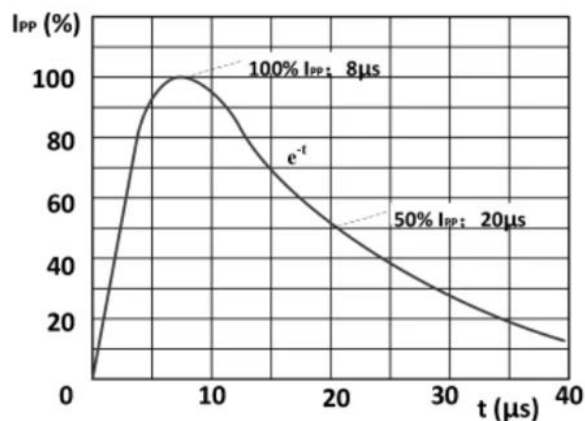


Fig. 1 . 8/20 us pulse waveform according to IEC 61000-4-5 and IEC 61643-321

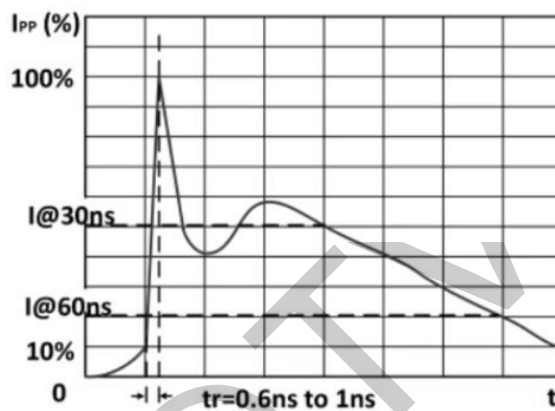


Fig. 2 . ESD pulse waveform according to IEC61000-4-2

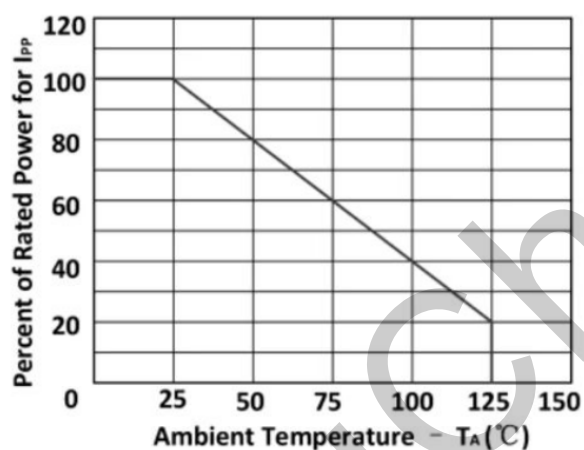


Fig. 3 . Power Derating Curve

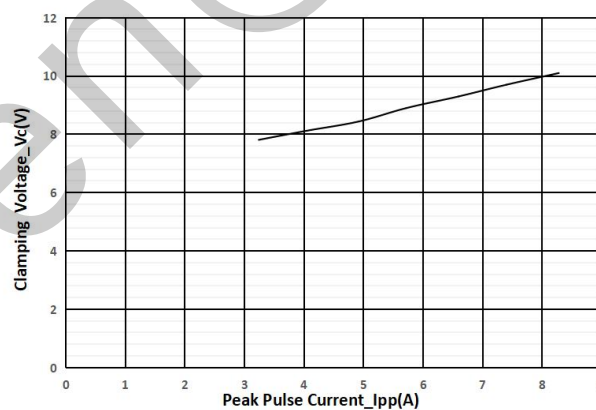
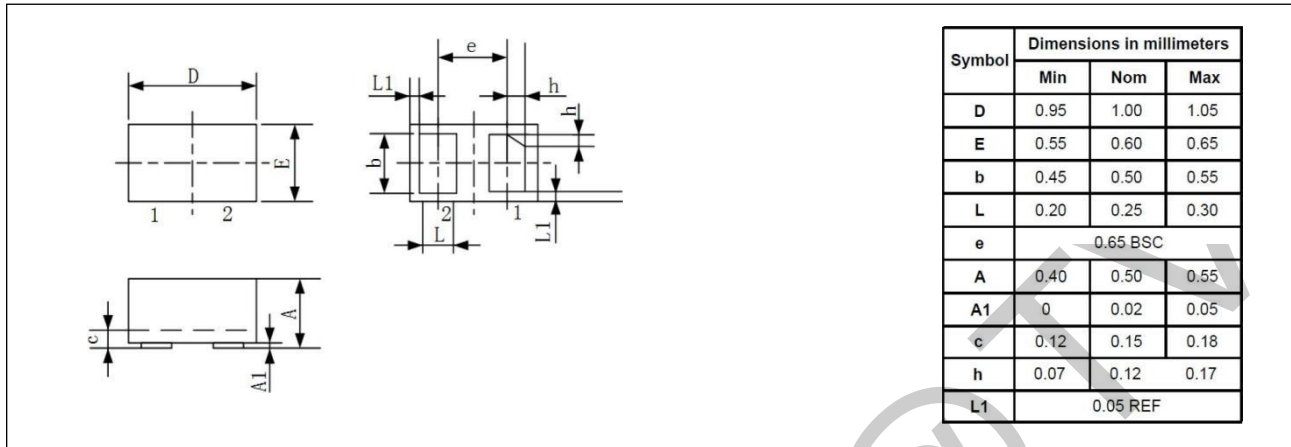
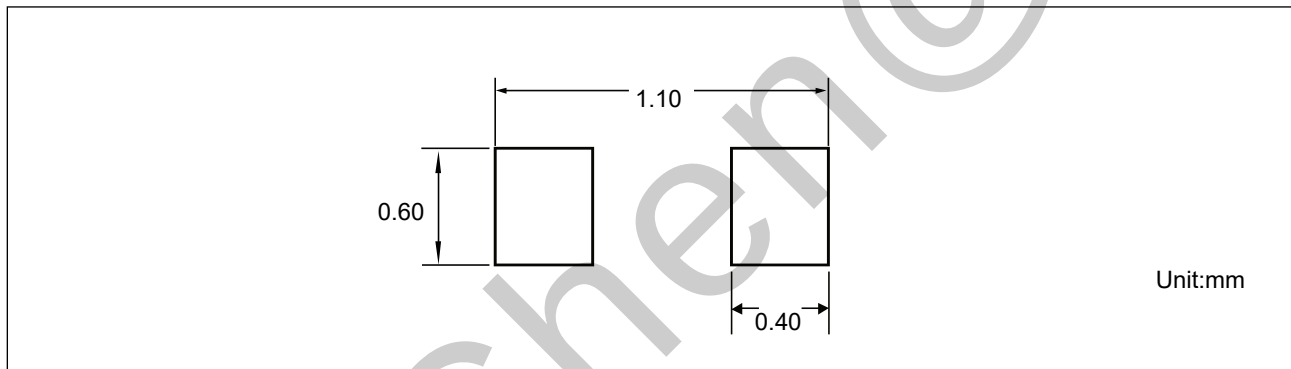


Fig. 4 . Peak Pulse Current vs Clamping Voltage

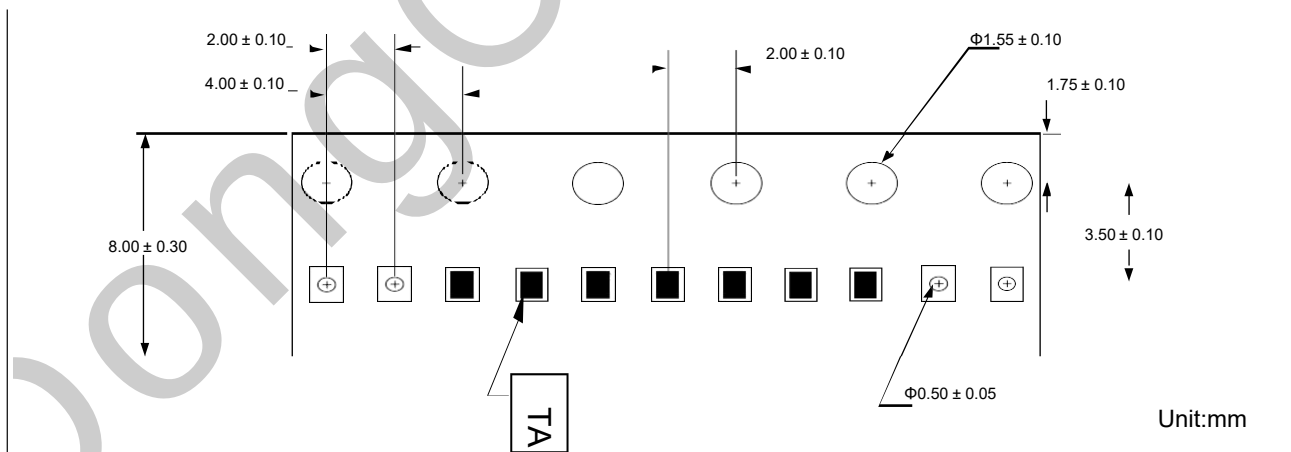
Package Dimensions



Pad Dimensions



Tape and Reel Specification



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