



QNHCHIP

2SJ201

Product Specification

2SJ201

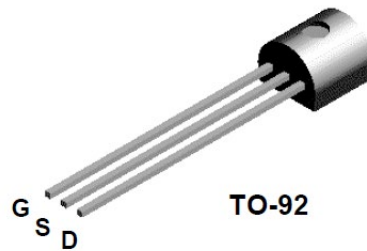
Silicon P Channel MOS Type



Description

- High Power Amplifier Application
- High breakdown voltage : $V_{DSS} = -200\text{ V}$
- High forward transfer admittance : $|Y_{fs}| = 5.0\text{ S (typ.)}$
- Complementary to 2SK1530

PIN Configuration



Number	PIN	PIN Function
1	G	Gate
2	S	Source
3	D	Drain

Maximum Ratings*

($T_A = 25^\circ\text{C}$)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	-200	V
Gate-source voltage	V_{GSS}	± 20	V
Drain current (Note 1)	I_D	-12	A
Drain power dissipation ($T_c = 25^\circ\text{C}$)	P_D	150	W
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55~150	$^\circ\text{C}$



Electrical Characteristics

(TA = 25°C)

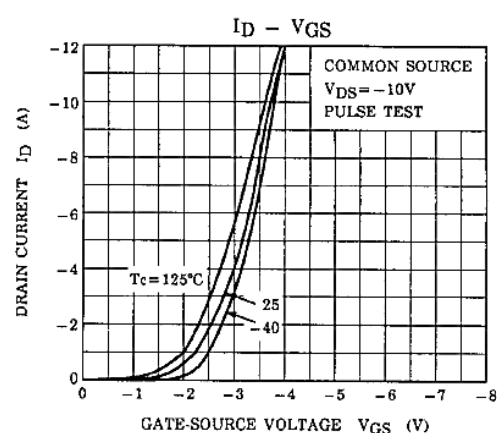
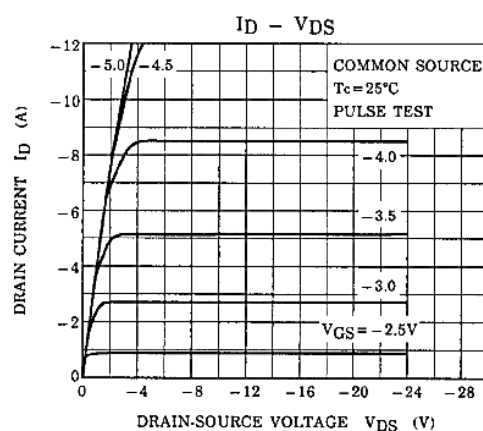
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Drain cut-off current	I_{DSS}	$V_{DS} = -200\text{ V}, V_{GS} = 0$	—	—	-1.0	mA
Gate leakage current	I_{GSS}	$V_{DS} = 0, V_{GS} = \pm 20\text{ V}$	—	—	± 0.5	μA
Drain-source breakdown voltage	$V_{(BR)DSS}$	$I_D = -10\text{ mA}, V_{GS} = 0$	-200	—	—	V
Gate-source cut-off voltage(Note 2)	$V_{GS(OFF)}$	$V_{DS} = -10\text{ V}, I_D = -0.1\text{ A}$	-0.8	—	-2.8	V
Drain-source saturation voltage	$V_{DS(ON)}$	$I_D = -8\text{ A}, V_{GS} = -10\text{ V}$	—	-2.0	-5.0	V
Forward transfer admittance	$ Y_{fs} $	$V_{DS} = -10\text{ V}, I_D = -5\text{ A}$	—	5.0	—	S
Input capacitance	C_{iss}	$V_{DS} = -30\text{ V}, V_{GS} = 0,$ $f = 1\text{ MHz}$	—	1500	—	pF
Output capacitance	C_{oss}	$V_{DS} = -30\text{ V}, V_{GS} = 0,$ $f = 1\text{ MHz}$	—	430	—	
Reverse transfer capacitance	C_{rss}	$V_{DS} = -30\text{ V}, V_{GS} = 0,$ $f = 1\text{ MHz}$	—	230	—	

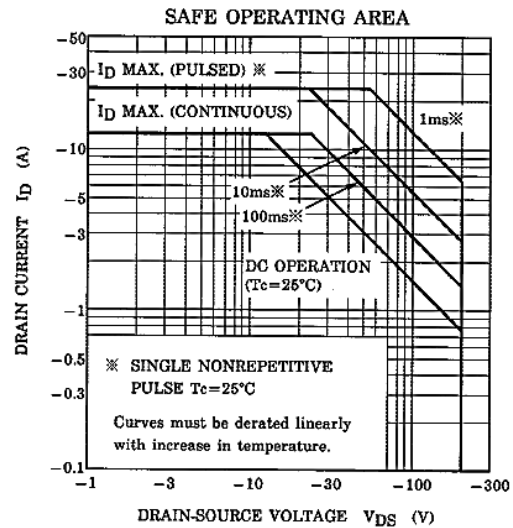
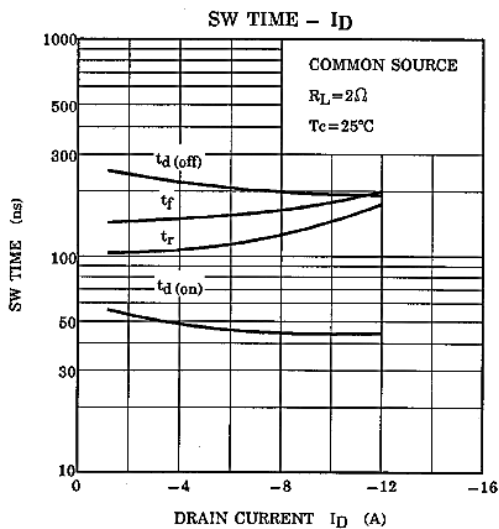
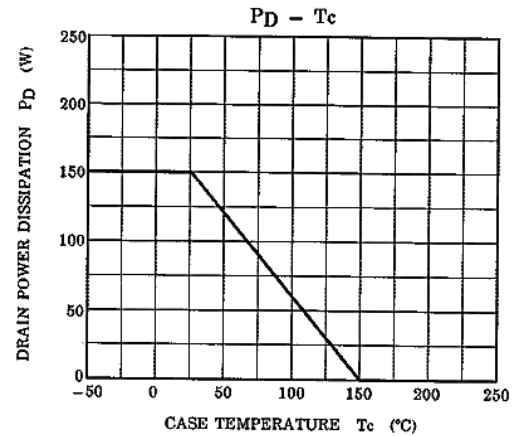
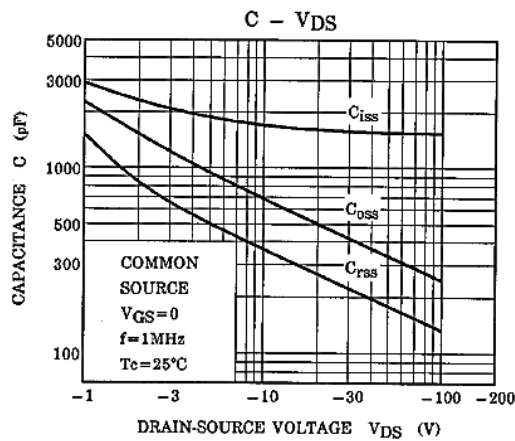
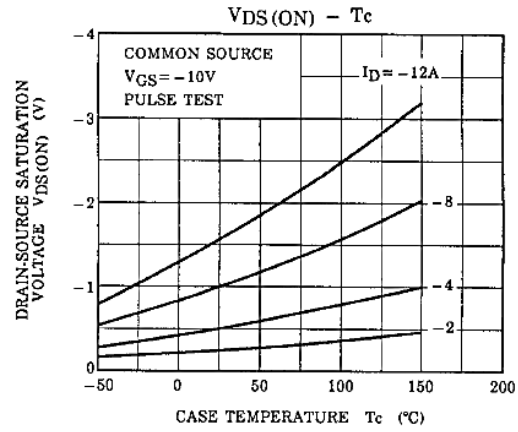
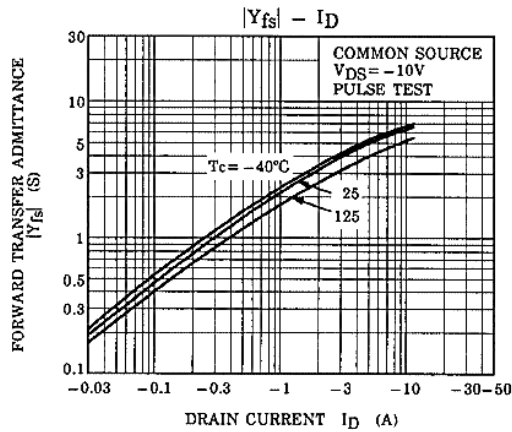
Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: $V_{GS(OFF)}$ Classification O: -0.8~-1.6, Y: -1.4~-2.8

This transistor is an electrostatic sensitive device.

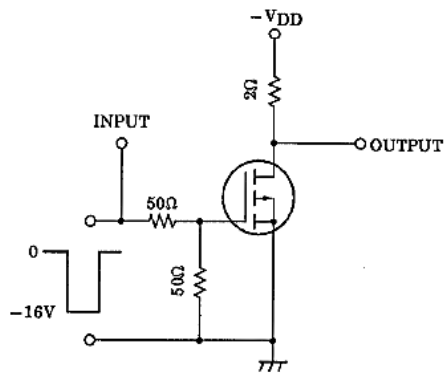
Please handle with caution.



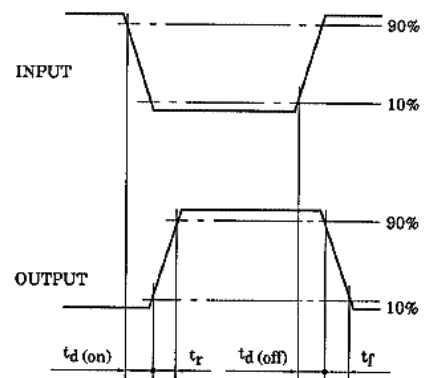




Switching Time Test Circuit

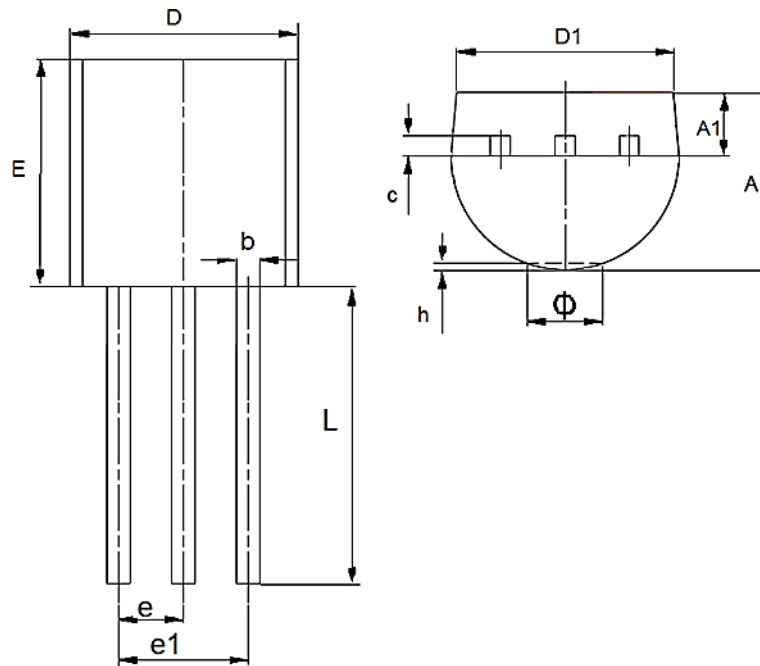


Waveforms





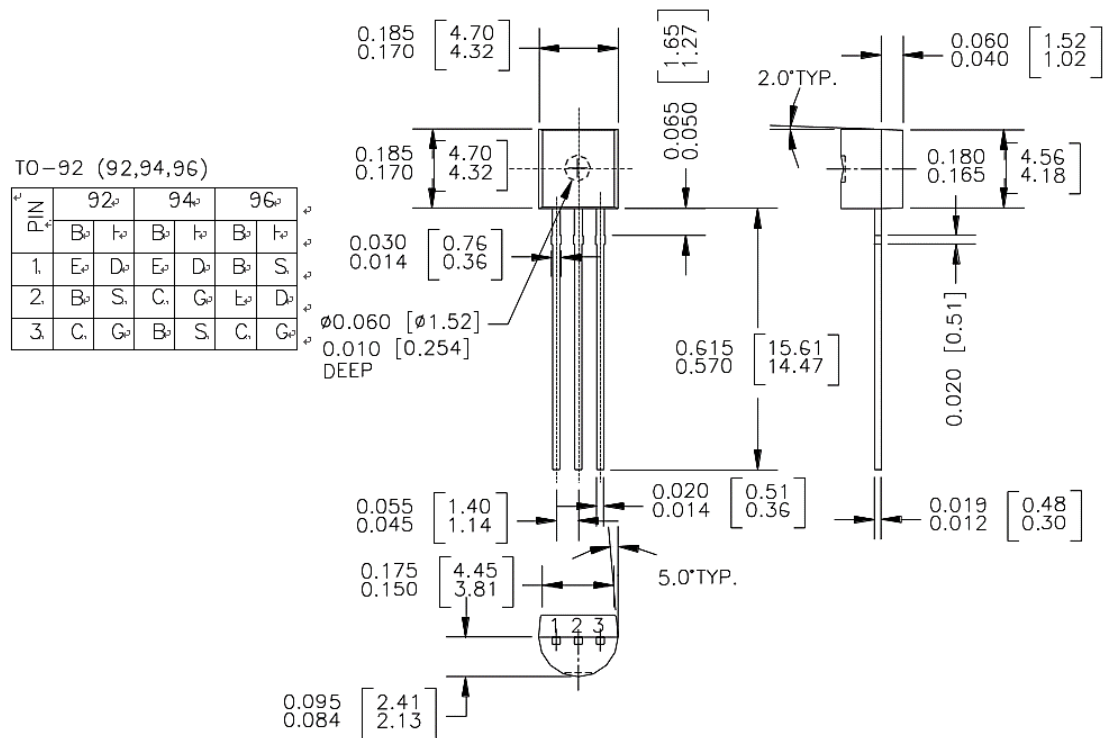
TO-92 Package Information



参数	尺寸 (mm)		尺寸 (Inch)	
	最小值	最大值	最小值	最大值
A	3.3	3.7	0.1299	0.1457
A1	1.1	1.4	0.0433	0.0551
b	0.38	0.55	0.015	0.0217
c	0.36	0.51	0.0142	0.0201
D	4.3	4.7	0.1693	0.185
D1	3.43	—	0.135	—
E	4.3	4.7	0.1693	0.185
e	1.27		0.05	
e1	2.44	2.64	0.0961	0.1039
L	14.1	14.5	0.5551	0.5709
h	0	0.38	0	0.015
Φ	—	1.6	—	0.063



TO-92 Package Dimensions



Ordering information

Order Code	V _{GS} (OFF)	Package Type	Minimum Packing QTY	Belivery Mode
2SJ201	-1.4~-2.8	TO-92	3000	Tape & Reel