

Enhancement Mode N-Channel Power MOSFET

PRIMARY CHARACTERISTICS

BV _{DSS}	R _{DS(ON)} MAX	I _D
30V	27mΩ@V _{GS} =10V	5.8A
	33mΩ@V _{GS} =4.5V	

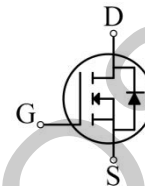
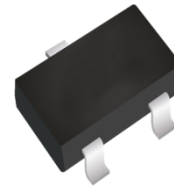
Features

- Trench Power LV MOSFET technology
- High-density cell design for low R_{DS(on)}
- High-speed switching
- RoHS compliant
- Halogen free

Mechanical Data

- Package: SOT-23
- Surface mount device
- 3 terminals
- RoHS and halogen free compliant

SOT-23



Applications

- Battery protection
- Load switch
- Power management

Ordering Information

Orderable Part Number	Package Type	Device Marking	Form	Quantity (pcs)
DC3400B	SOT-23	-	7" Tape&Reel	3,000

Maximum Ratings (@ T_A = 25°C, unless otherwise specified.)

Parameter	Symbol	Value	Unit
Drain - Source Voltage	V _{DS}	30	V
Gate - Source Voltage	V _{GS}	±20	V
Continuous Drain Current (V _{GS} = 4.5V) ⁽¹⁾	I _D	5.8	A
		4.5	A
Pulsed Drain Current ⁽²⁾	I _{DM}	23	A
Power Dissipation	P _D	1.2	W
Junction & Storage Temperature Range	T _J , T _{STG}	-55 ~ +150	°C

Thermal Characteristics

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Ambient ⁽³⁾	R _{θJA}	104	-	°C/W

Electrical Characteristics (@ $T_J = 25^\circ\text{C}$, unless otherwise specified.)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Off Characteristics ⁽⁴⁾						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	VGS=0V, ID=250μA	30	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	VDS=30V VGS = 0V, TJ=25°C	-	-	1	μA
Gate-Source Leakage Current	I _{GSS}	VGS=±20V, VDS = 0V	-	-	100	nA
On Characteristics ⁽⁴⁾						
Gate Threshold Voltage	V _{GS(th)}	VDS=VGS, ID=250μA	0.65	0.9	1.5	V
Static Drain-Source On-Resistance	R _{DS(ON)}	VGS=2.5V, ID=3.0A	-	31	51	mΩ
		VGS=4.5V, ID=5.0A	-	25	33	mΩ
		VGS=10V, ID=5.8A	-	21	27	mΩ
Diodes Forward Voltage	V _{SD}	IS=5.8A, VGS=0V	-	0.8	1.2	V
Dynamic Characteristics ⁽⁴⁾						
Input Capacitance	C _{iss}	VDS=15V, VGS=0V, f=1MHz	-	702	-	pF
Output Capacitance	C _{oss}		-	66	-	pF
Reverse Transfer Capacitance	C _{rss}		-	52	-	pF
Switching Characteristics ⁽⁵⁾						
Turn-On DelayTime	t _{d(on)}	VGS=4.5V, VDD=15V ID=1A, RGEN=2.8Ω	-	12	-	ns
Rise Time	t _r		-	52	-	ns
Turn-Off DelayTime	t _{d(off)}		-	17	-	ns
Fall Time	t _f		-	10	-	ns
Gate Charge Characteristics ⁽⁵⁾						
Total Gate Charge	Q _g	VDS=15V, ID=5.8A, VGS=4.5V	-	4.8	-	nC
Gate-Source Charge	Q _{gs}		-	1.2	-	nC
Gate-Drain Charge	Q _{gd}		-	1.7	-	nC
Drain-Source Diode Characteristics ⁽⁵⁾						
Diode Forward Current	I _s	TA = 25°C	-	-	5.8	A
Notes: 1. This current is chip limited, which is calculated based on Rthja. 2. This current is calculated on single pulse with 10μs Single Pulse . 3. Device mounted on FR-4 substrate PC board with 2oz copper in 1inch square cooling area. 4. Short duration pulse test used to minimize self-heating effect. 5. Defined by design, not subject to production.						

■ Typical Performance Characteristics

Figure 1: Output Characteristics

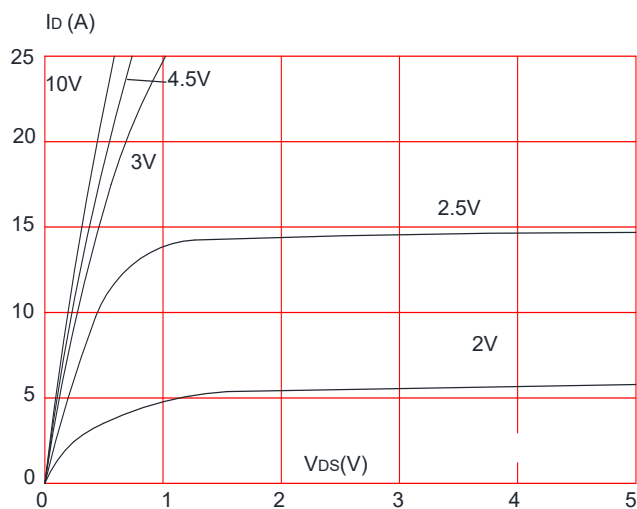


Figure 2: Typical Transfer Characteristics

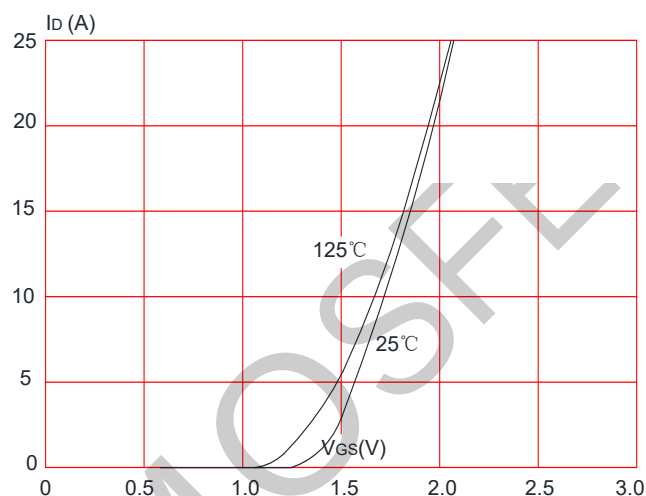


Figure 3: On-resistance vs. Drain Current

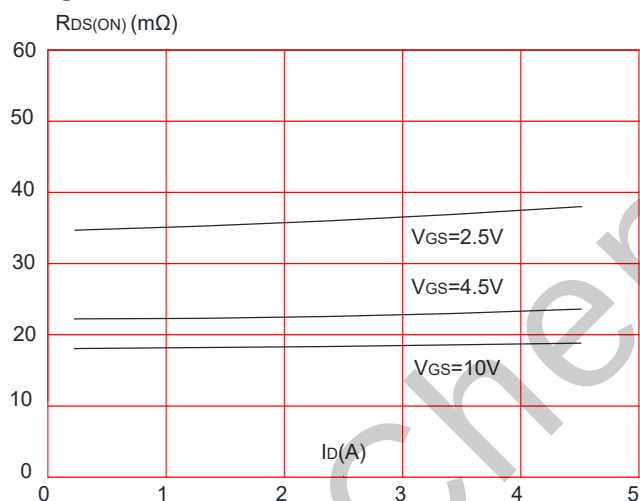


Figure 4: Body Diode Characteristics

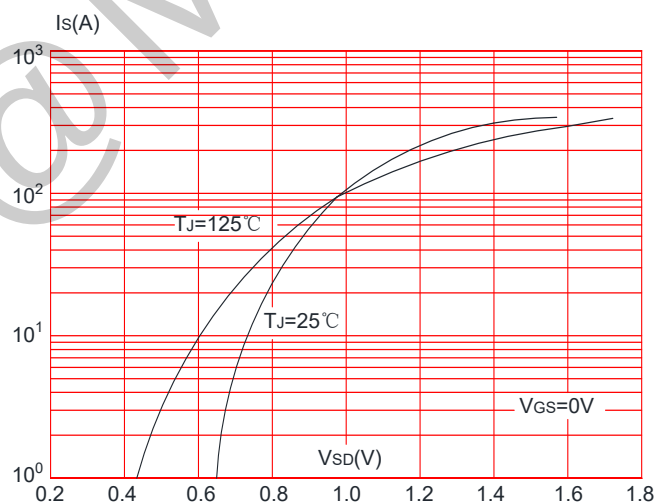


Figure 5: Gate Charge Characteristics

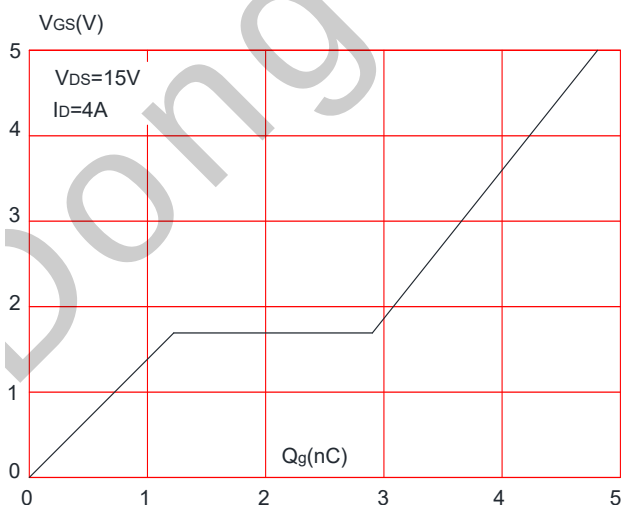


Figure 6: Capacitance Characteristics

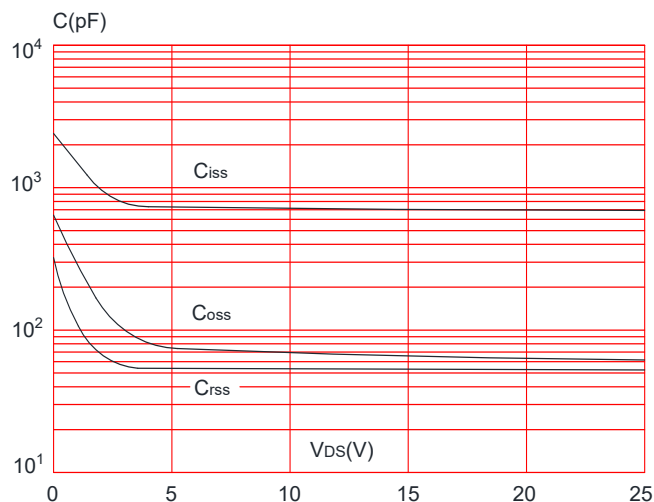


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

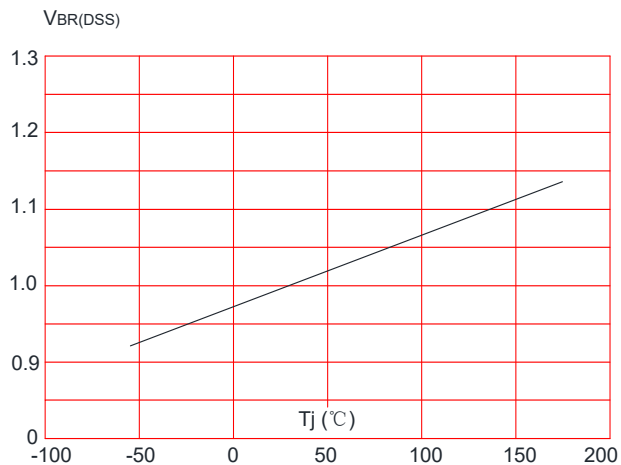


Figure 8: Normalized on Resistance vs. Junction Temperature

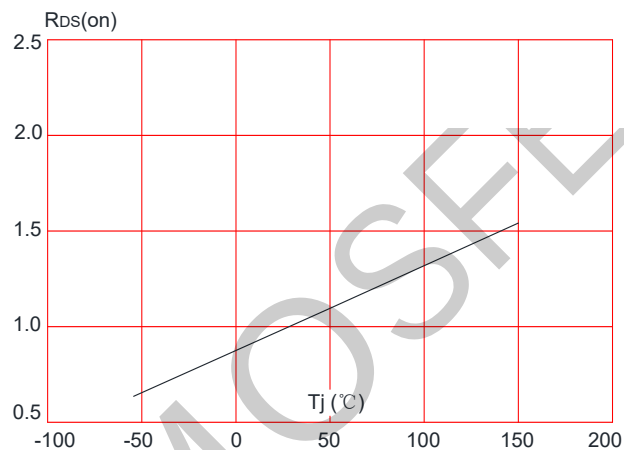


Figure 9: Maximum Safe Operating Area

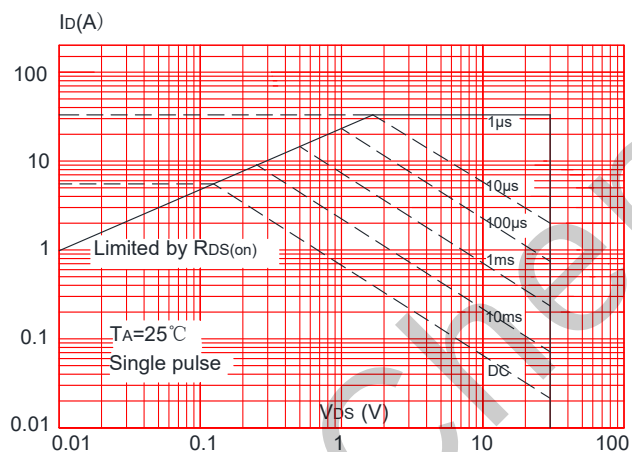
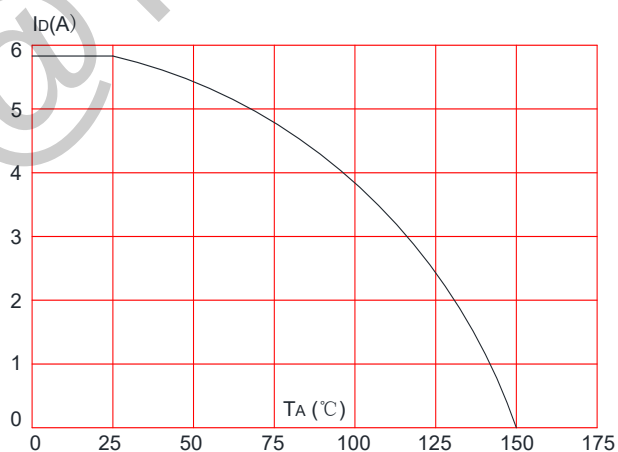
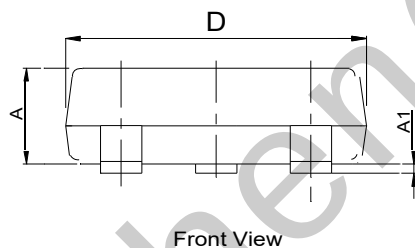
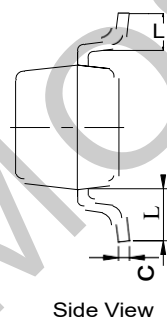
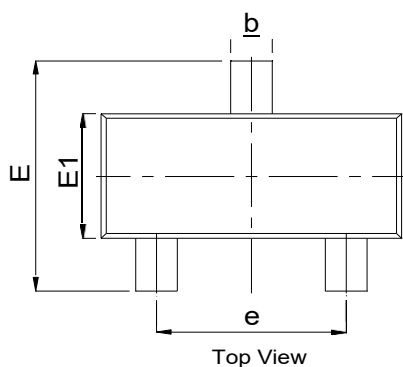


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature



Outline Drawing

SOT-23

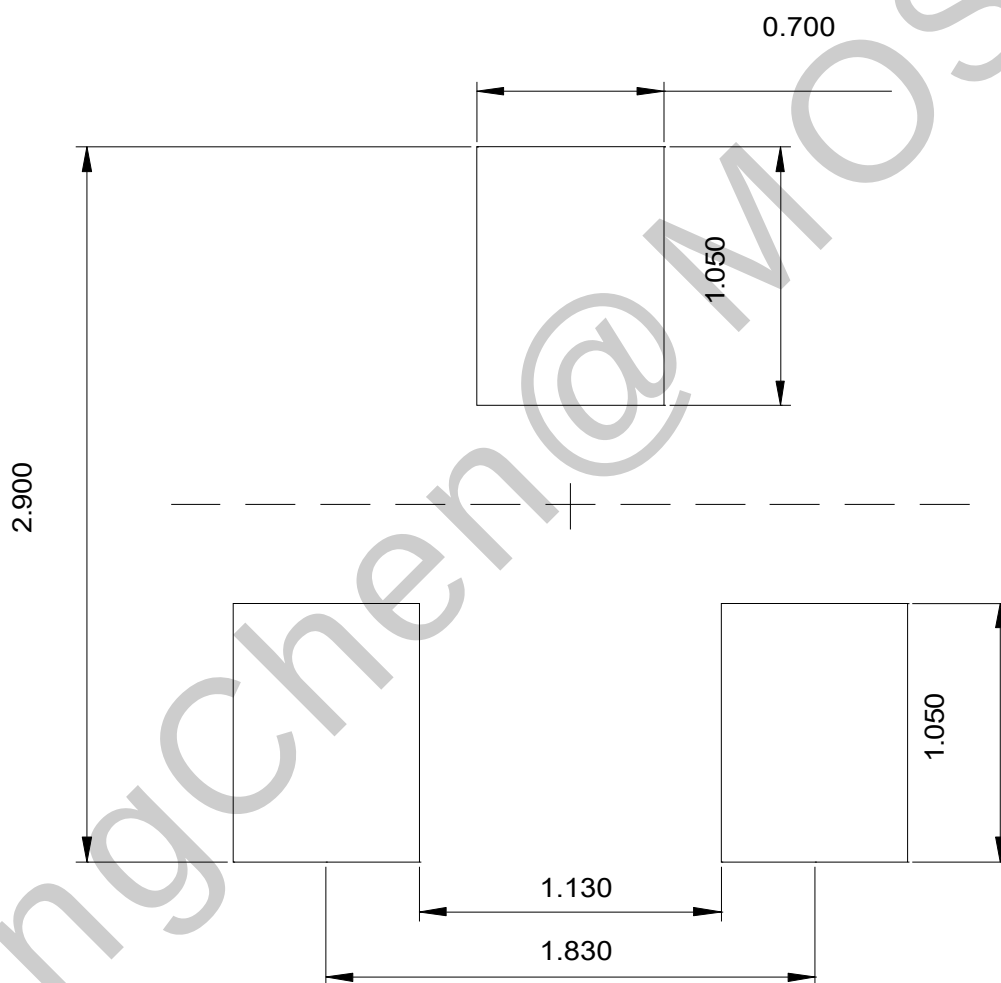


DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
A1	0.00	-	0.10
D	2.80	2.90	3.00
E	2.30	2.40	2.50
E1	1.20	1.30	1.40
L	0.45	0.55	0.65
L1	0.20	-	0.55
b	0.30	0.40	0.50
c	-	0.10	0.15
e	-	1.90	-

Dimensions in inches and (millimeters)

Suggested Soldering Pad Layout

SOT-23



Dimensions in inches and (millimeters)

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